

UNIVERSITY OF CALIFORNIA AT BERKELEY
College of Engineering
Department of Electrical Engineering and Computer Sciences

Discussion Notes #10

EE 105
Prof. Wu

Spring 2007

The following notes might be useful as a review for the final exam. This is not a comprehensive review, but it covers the key concepts that were discussed this semester.

Semiconductor Physics

- Intrinsic carrier concentration at 300 K: $n_i \sim 10^{10} \text{ cm}^{-3}$
- Mass action law (at thermal equilibrium): $n_0 p_0 = n_i^2$
- Doping
 - n-type: $n_0 = N_d - N_a$ and $p_0 = \frac{n_i^2}{N_d - N_a}$
 - p-type: $p_0 = N_a - N_d$ and $n_0 = \frac{n_i^2}{N_a - N_d}$
- Drift current: current due to movement of charged particles in an electric field

$$J_{drift} = J_{n,drift} + J_{p,drift} = q(\mu_n n_0 + \mu_p p_0)E = \sigma E$$

- Diffusion current: movement of charge carriers from high conc. to low conc.

$$J_{diff} = J_{n,diff} + J_{p,diff} = q\left(D_n \frac{dn}{dx} - D_p \frac{dp}{dx}\right)$$

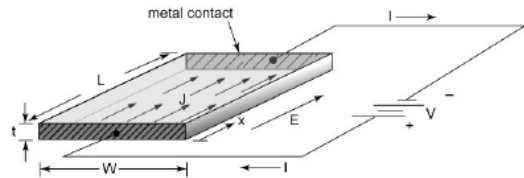
- Einstein's relation: $\frac{D}{\mu} = \frac{kT}{q}$

- Resistivity

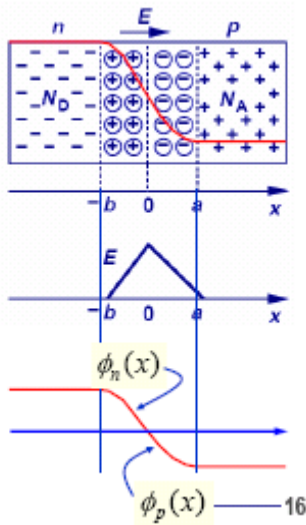
- Conductivity: $\sigma = n_0 q \mu_n + p_0 q \mu_p$

- Resistivity: $\rho = \frac{1}{\sigma} = \frac{1}{n_0 q \mu_n + p_0 q \mu_p}$

- Resistance: $R = \rho \frac{L}{tW} = R_S \frac{L}{W}$, where R_S is the sheet resistance



pn Junctions



- Depletion region:
 - Region of immobile ions at junction
 - Depletion region edge on n-side: $x_{n0} = \sqrt{\frac{2\epsilon_s \phi_{bi}}{qN_d} \left(\frac{N_a}{N_a + N_d} \right)}$
 - Depletion region edge on p-side: $x_{p0} = \sqrt{\frac{2\epsilon_s \phi_{bi}}{qN_a} \left(\frac{N_d}{N_a + N_d} \right)}$
 - Depletion width: $x_{d0} = x_{n0} + x_{p0} = \sqrt{\frac{2\epsilon_s \phi_{bi}}{qN_d} \left(\frac{1}{N_a} + \frac{1}{N_d} \right)}$

- Charge density: $\rho_0(x) \cong \begin{cases} +qN_d & -x_{n0} < x < 0 \\ -qN_a & 0 < x < x_{p0} \end{cases}$

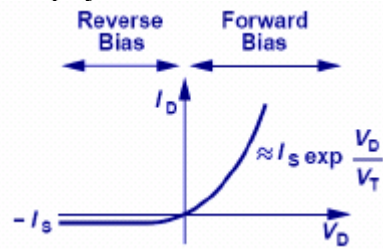
- Electric field: solve using Gauss' Law: $\frac{dE_0}{dx} = \frac{\rho_0(x)}{\epsilon_s}$

$$E_0(x) = \frac{qN_d}{\epsilon_s}(x + x_{n0}) \quad E_0(x) = \frac{qN_a}{\epsilon_s}(x_{p0} - x)$$

The graph shows the electric field $E_0(x)$ as a function of position x . The field is zero outside the depletion region and increases linearly from zero at $x = -x_{n0}$ to a maximum at $x = 0$, then decreases linearly to zero at $x = x_{p0}$. The depletion region boundaries are labeled $-b$ and a on the x-axis.

- Potential profile:
 - Potential on n-side (outside depletion region): $\phi_n = V_{th} \ln \frac{N_d}{n_i}$, where $V_{th} = \frac{kT}{q}$
 - Potential on p-side (outside depletion region): $\phi_p = V_{th} \ln \frac{n_i}{N_a}$
 - Built-in potential: $\phi_{bi} = \phi_n - \phi_p$

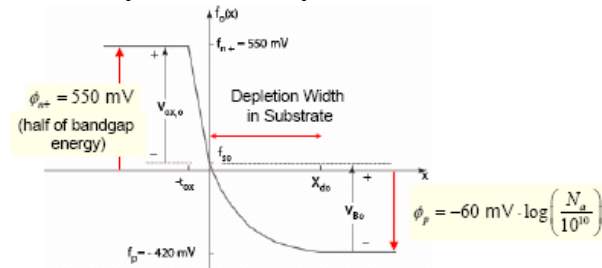
- Reverse bias: depletion region width \uparrow , electric field in junction \uparrow
 - Depletion width: $X_d(V_D) = X_{d0} \sqrt{1 - \frac{V_D}{\phi_{bi}}}$, where V_D is the reverse bias voltage
 - Junction capacitance: $C_j = \frac{C_{j0}}{\sqrt{1 - \frac{V_D}{\phi_{bi}}}}$, where $C_{j0} = \sqrt{\frac{q\epsilon_s}{2\phi_{bi}} \left(\frac{N_a N_d}{N_a + N_d} \right)}$
- IV curve for pn junction:



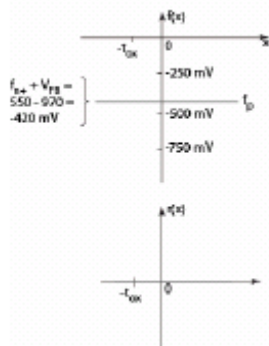
Note: breakdown occurs under a large reverse bias

Device Physics

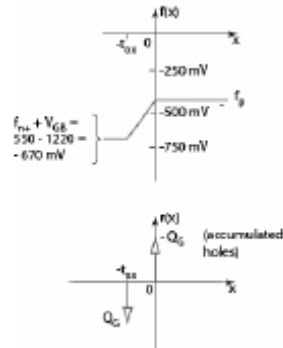
- MOS capacitor
 - NMOS cap. at thermal eq.:



- NMOS cap. at flatband (no net charge):



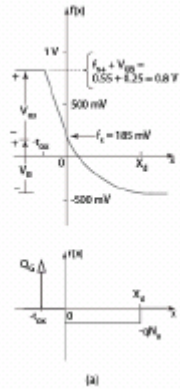
- NMOS cap. in accumulation ($V_{GB} < V_{FB}$):



$$Q_G = C_{ox} \cdot (V_{GB} - V_{FB})$$

$$C_{ox} = \frac{\epsilon_{ox}}{t_{ox}} \quad [\text{F/cm}^2]$$

- NMOS cap. in depletion ($V_{FB} < V_{GB} < V_{Tn}$):



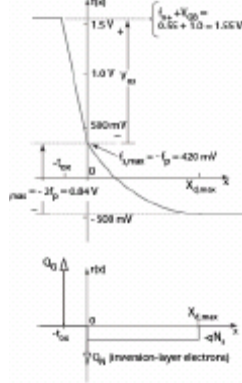
$$V_{GB} + \phi_{n+} - \phi_p = V_{ox} + V_B$$

$$= \frac{qN_a x_d}{C_{ox}} + \frac{qN_a x_d^2}{2\epsilon_s}$$

$$= V_{GB} - V_{FB}$$

$$x_d = t_{ox} \frac{\epsilon_s}{\epsilon_{ox}} \left(\sqrt{1 + \frac{2C_{ox}^2 (V_{GB} - V_{FB})}{q\epsilon N_a}} - 1 \right)$$

- NMOS cap. in inversion ($V_{GB} > V_{Tn}$):



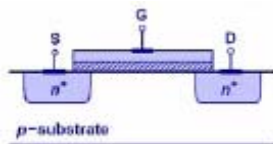
$$n_s = n_i e^{\frac{q\phi_s}{kT}} = N_a$$

$$\phi_s = -\phi_p$$

$$V_{Tn} = V_{FB} - 2\phi_p + \frac{1}{C_{ox}} \sqrt{2q\epsilon_s N_a (-2\phi_p)}$$

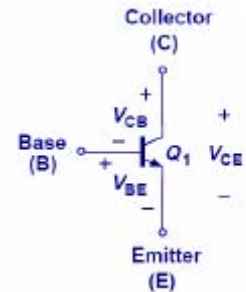
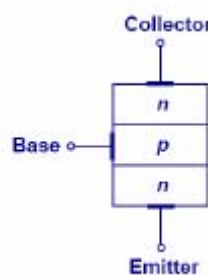
MOSFETs (nMOS)

Structure

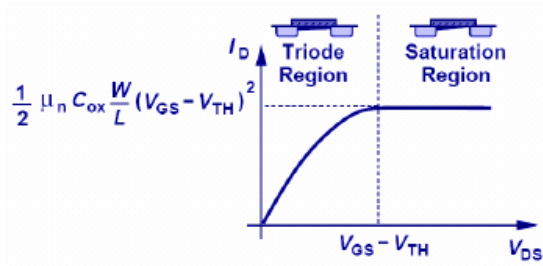


BJTs (nnp)

Structure



MOSFET IV curve



MOSFET drain current (saturation):

$$I_D = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{Tn})^2 (1 - \lambda V_{DS})$$

Transconductance and r_o :

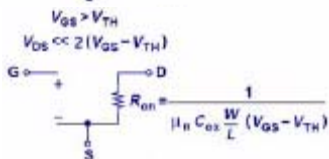
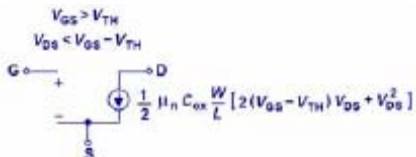
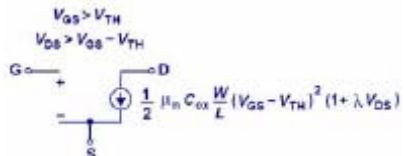
$$g_m = \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{Tn})$$

$$g_m = \sqrt{2 \mu_n C_{ox} \frac{W}{L} I_D}$$

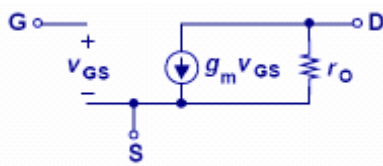
$$g_m = \frac{2I_D}{V_{GS} - V_{Tn}}$$

$$r_o \approx \frac{1}{\lambda I_D}$$

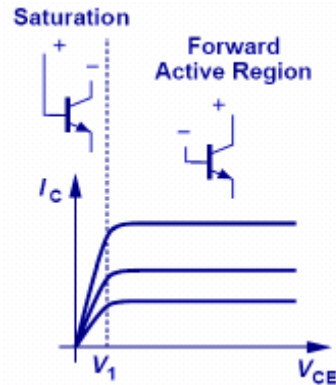
Large-signal models



Small-signal model



BJT IV curve



BJT currents

$$I_C = I_S \exp \frac{V_{BE}}{V_T}$$

$$I_B = \frac{1}{\beta} I_C$$

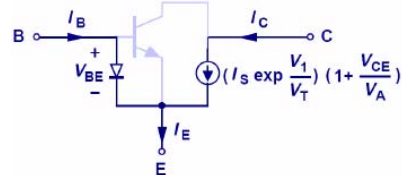
$$I_E = I_C + I_B$$

Transconductance and r_o :

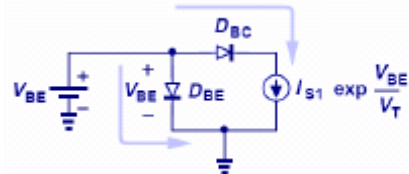
$$g_m = \frac{I_C}{V_T}$$

$$r_o \approx \frac{V_A}{I_C}$$

Large-signal models

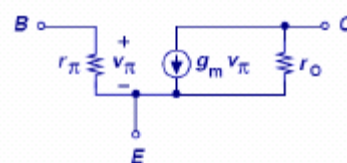


Forward-active



Saturation

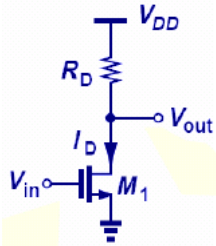
Small-signal model



Single-Stage Amplifiers

MOSFETs (nMOS)

Common-source

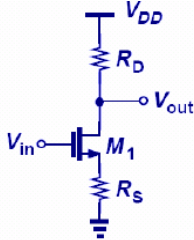


$$A_v = -g_m (R_D \parallel r_o)$$

$$R_{in} = \infty$$

$$R_{out} = R_D \parallel r_o$$

Common-source with degeneration:

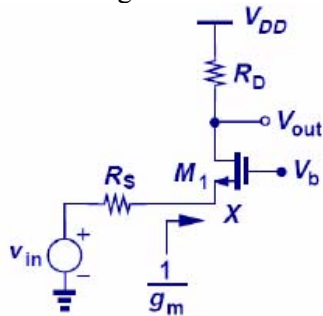


$$A_v = -\frac{R_D}{\frac{1}{g_m} + R_S}$$

$$R_{in} = \infty$$

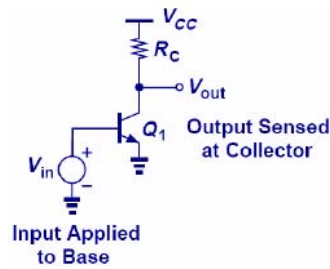
$$R_{out} = [(1 + g_m r_o) R_S + r_o] \parallel R_D$$

Common-gate:



BJTs (nnp)

Common-emitter

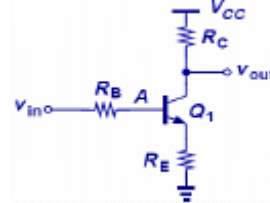


$$A_v = -g_m (R_C \parallel r_o)$$

$$R_{in} = r_\pi$$

$$R_{out} = R_C \parallel r_o$$

Common-emitter with degeneration:

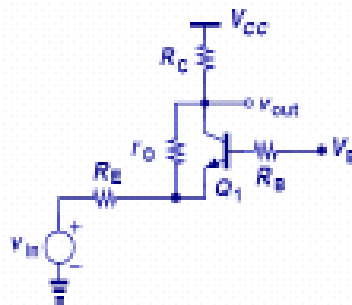


$$\frac{v_{out}}{v_{in}} = \frac{-\beta R_C}{r_\pi + (\beta + 1)R_E + R_B}$$

$$R_{in} = r_\pi + (\beta + 1)R_E + R_B$$

$$R_{out} = R_C \parallel r_o$$

Common-base:

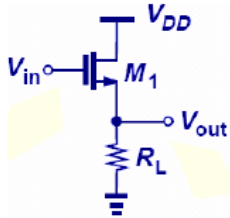


$$A_v = \frac{R_D}{\frac{1}{g_m} + R_S}$$

$$R_{in} = \frac{1}{g_m}$$

$$R_{out} = [(1 + g_m r_o) R_S + r_o] \parallel R_D$$

Common-drain (source follower):



$$A_v = \frac{r_o \parallel R_L}{\frac{1}{g_m} + r_o \parallel R_L}$$

$$R_{in} = \infty$$

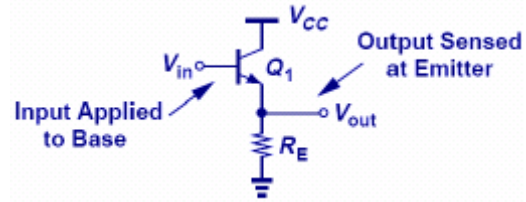
$$R_{out} = \frac{1}{g_m} \parallel r_o \parallel R_L$$

$$A_v = \frac{g_m R_C}{1 + R_E \left(g_m + \frac{1}{r_\pi} \right) + \frac{R_B}{r_\pi}}$$

$$R_{in} = \frac{r_\pi + R_B}{1 + g_m r_\pi} + R_E$$

$$R_{out} = R_C \parallel r_o$$

Common-collector (emitter follower):



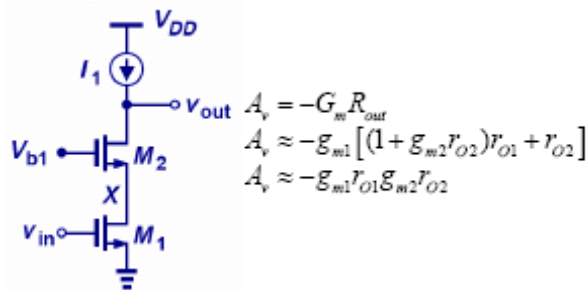
$$\frac{v_{out}}{v_{in}} = \frac{1}{1 + \frac{r_\pi}{\beta + 1} \cdot \frac{1}{R_E}} \approx \frac{R_E}{R_E + \frac{1}{g_m}}$$

$$R_{in} = r_\pi + (\beta + 1) R_E$$

$$R_{out} = R_E \parallel \frac{1}{g_m} \parallel r_o$$

Multistage Amplifiers

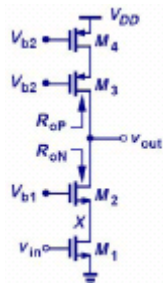
- Cascode



$$A_v = -G_m R_{out}$$

$$A_v \approx -g_{m1} [(1 + g_{m2} r_{o2}) r_{o1} + r_{o2}]$$

$$A_v \approx -g_{m1} r_{o1} g_{m2} r_{o2}$$

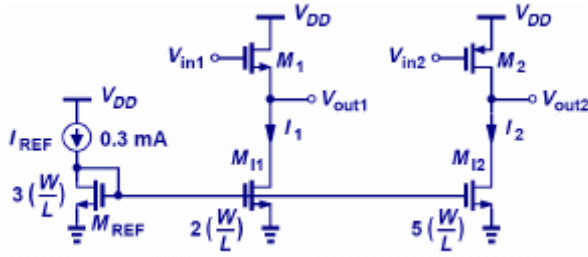


$$R_{on} \approx g_{m2} r_{o2} r_{o1}$$

$$R_{op} \approx g_{m3} r_{o3} r_{o4}$$

$$R_{out} = R_{on} \parallel R_{op}$$

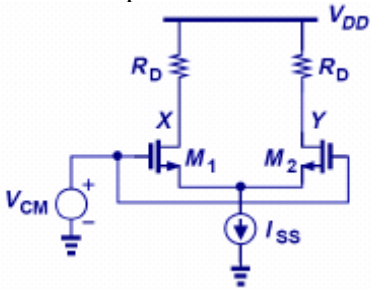
- Current mirror



Current mirrors formed by M_{REF} & M_{11} , and by M_{REF} & M_{12}

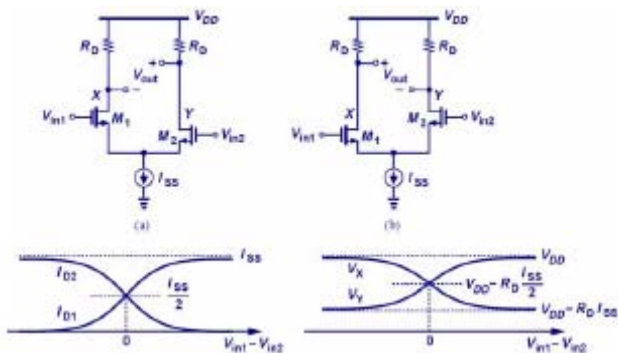
$$I_1 = \frac{\left(\frac{W}{L}\right)_1}{\left(\frac{W}{L}\right)_{REF}} I_{REF} \quad \text{and} \quad I_2 = \frac{\left(\frac{W}{L}\right)_2}{\left(\frac{W}{L}\right)_{REF}} I_{REF}$$

- Differential pair



$$V_X = V_Y = V_{DD} - R_D \frac{I_{SS}}{2}$$

Minimum common-mode output voltage: $V_{DD} - R_D \frac{I_{SS}}{2} > V_{CM} - V_{TH}$



Frequency Response

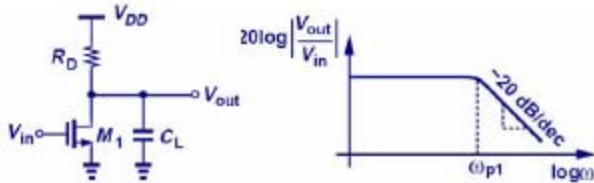
- Bode plots

Transfer function:

$$H(s) = A_0 \frac{\left(1 + \frac{s}{\omega_{z1}}\right) \left(1 + \frac{s}{\omega_{z2}}\right) \dots}{\left(1 + \frac{s}{\omega_{p1}}\right) \left(1 + \frac{s}{\omega_{p2}}\right) \dots}$$

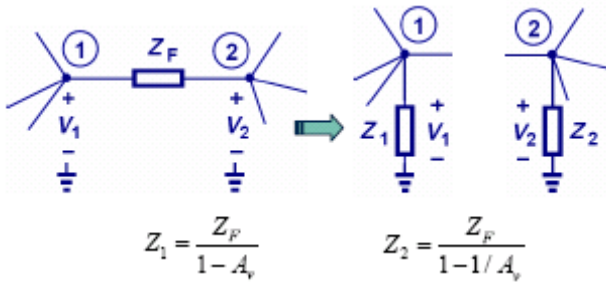
Magnitude rises with slope of +20 dB/dec after a zero is reached
 Magnitude falls with slope of -20 dB/dec after a pole is reached

Example Bode plot:



$$|\omega_{p1}| = \frac{1}{R_D C_L}$$

- Miller's Theorem (use to approximate floating capacitors):



- Intrinsic MOSFET capacitances

