

## Lecture 14

### OUTLINE

- Frequency Response (cont'd)
  - CB stage
  - Emitter follower
  - Cascode stage

Reading: Chapter 11.4-11.6

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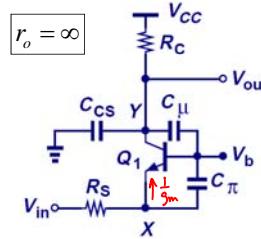
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## CB Stage: Pole Frequencies

- Note that there is no capacitance between input & output nodes  
→ No Miller multiplication effect!

**CB stage with BJT capacitances shown**



$$\omega_{p,Y} = \frac{1}{R_c C_Y}$$

$$C_Y = C_\mu + C_{CS}$$

$$\omega_{p,X} = \frac{1}{\left(R_s \parallel \frac{1}{g_m}\right) C_X} > \omega_p$$

$$C_X = C_\pi$$

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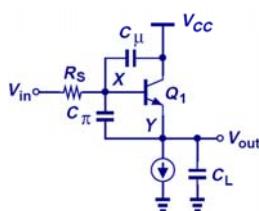
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## Emitter Follower

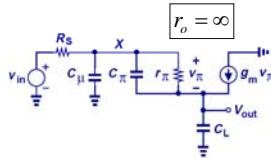
- Recall that the emitter follower provides high input impedance and low output impedance, and is used as a voltage buffer.

**Follower stage with BJT capacitances shown**

- $C_L$  is the load capacitance



**Circuit for small-signal analysis ( $A_v$ )**



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## AC Analysis of Emitter Follower

$$v_X = v_{out} + v_\pi$$

- KCL at node X:

$$\frac{v_{out} + v_\pi - v_{in}}{R_s} + \frac{v_{out} + v_\pi}{r_\pi} + \frac{v_\pi}{j\omega C_\mu} + \frac{v_\pi}{j\omega C_\pi} = 0$$

- KCL at output node:
- $$\frac{v_\pi}{r_\pi} + \frac{v_\pi}{j\omega C_\pi} + g_m v_\pi = \frac{v_{out}}{j\omega C_L}$$

$$\Rightarrow \frac{v_{out}}{v_{in}} \cong \frac{1 + \frac{C_\pi}{g_m} (j\omega)}{a(j\omega)^2 + b(j\omega) + 1}$$

$$a = \frac{R_s}{g_m} (C_\mu C_\pi + C_\mu C_L + C_\pi C_L)$$

$$b = R_s C_\mu + \frac{C_\pi}{g_m} + \left(1 + \frac{R_s}{r_\pi}\right) \frac{C_L}{g_m}$$

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## Follower: Zero and Pole Frequencies

$$\frac{v_{out}}{v_{in}} \cong \frac{1 + \frac{C_\pi}{g_m}(j\omega)}{a(j\omega)^2 + b(j\omega) + 1}$$

$$a = \frac{R_s}{g_m} (C_\mu C_\pi + C_\mu C_L + C_\pi C_L)$$

$$b = R_s C_\mu + \frac{C_\pi}{g_m} \left(1 + \frac{R_s}{r_\pi}\right) C_L$$

- The follower has one zero:

$$\omega_z = \frac{g_m}{C_\pi} = 2\pi f_T$$

- The follower has two poles at lower frequencies:

$$a(j\omega)^2 + b(j\omega) + 1 = \left(1 + \frac{j\omega}{\omega_{p1}}\right) \left(1 + \frac{j\omega}{\omega_{p2}}\right) = |1 + \left(\frac{1}{\omega_{p1}} + \frac{1}{\omega_{p2}}\right)(j\omega)|^2$$

$$\alpha = \frac{1}{\omega_{p1} \omega_{p2}}; \quad b = \frac{1}{\omega_{p1}} + \frac{1}{\omega_{p2}}$$

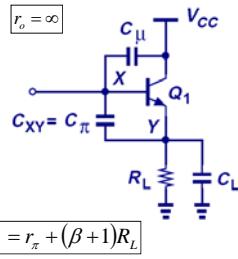
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## Emitter Follower: Input Capacitance

- Recall that the voltage gain of an emitter follower is  $A_v = \frac{R_L}{R_L + \frac{1}{g_m}}$

Follower stage with BJT capacitances shown

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- $C_{XY}$  can be decomposed into  $C_X$  and  $C_Y$  at the input and output nodes, respectively:

$$C_X = (1 - A_v) C_\pi = \frac{C_\pi}{1 + g_m R_L}$$

$$C_Y = \left(1 - \frac{1}{A_v}\right) C_\pi = \frac{-C_\pi}{g_m R_L}$$

$$C_{in} = C_\mu + \frac{C_\pi}{1 + g_m R_L}$$

## Emitter Follower: Output Impedance

$r_o = \infty$

$$v_\pi = -(i_x + g_m v_\pi) \left( r_\pi \parallel \frac{1}{j\omega C_\pi} \right)$$

$$v_x = -v_\pi + (i_x + g_m v_\pi) R_S$$

$$Z_{out} \equiv \frac{v_x}{i_x} = \frac{R_S r_\pi C_\pi (j\omega) + r_\pi + R_S}{r_\pi C_\pi (j\omega) + \beta + 1} = \frac{\frac{1 + j\omega}{(r_\pi + R_S) / R_S r_\pi C_\pi}}{1 + \frac{j\omega}{(\beta + 1) / r_\pi C_\pi}} = \begin{cases} r_\pi + R_S & \text{for low } \omega \\ 1 + \beta & \\ R_S & \text{for high } \omega \end{cases}$$

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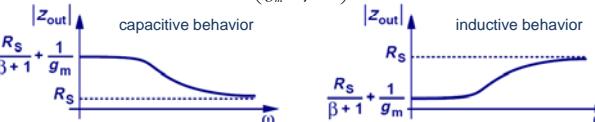
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## Emitter Follower as Active Inductor

$$Z_{out} \equiv \frac{v_x}{i_x} = \frac{R_S r_\pi C_\pi (j\omega) + r_\pi + R_S}{r_\pi C_\pi (j\omega) + \beta + 1} = \left( \frac{r_\pi + R_S}{\beta + 1} \right) \cdot \frac{1 + \frac{j\omega}{(r_\pi + R_S) / R_S r_\pi C_\pi}}{1 + \frac{j\omega}{(\beta + 1) / r_\pi C_\pi}}$$

$$\text{CASE 1: } R_S < 1/g_m \quad \left( \frac{1}{g_m} + \frac{R_S}{\beta + 1} \right) \quad \text{CASE 2: } R_S > 1/g_m$$



- A follower is typically used to lower the driving impedance  $\rightarrow R_S > 1/g_m$  so that the "active inductor" characteristic on the right is usually observed.

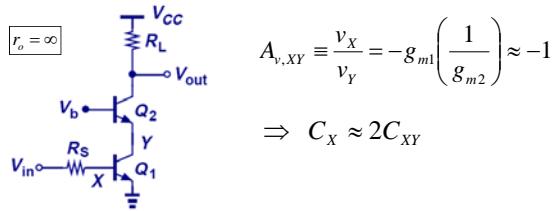
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## Cascode Stage

- Review:
  - A CE stage has large  $R_{in}$  but suffers from the Miller effect.
  - A CB stage is free from the Miller effect, but has small  $R_{in}$ .
- A cascode stage provides high  $R_{in}$  with minimal Miller effect.**



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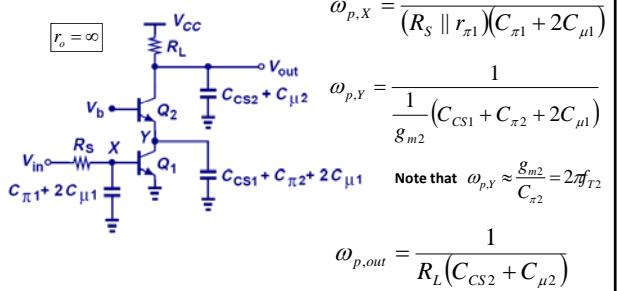
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## Cascode Stage: Pole Frequencies

### Cascode stage with BJT capacitances shown

(Miller approximation applied)

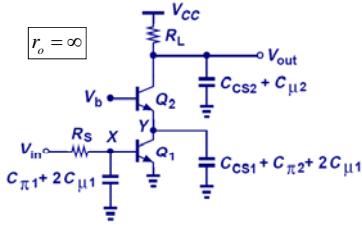


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## Cascode Stage: I/O Impedances



$$Z_{in} = r_{\pi 1} \parallel \frac{1}{j\omega(C_{\pi 1} + 2C_{\mu 1})}$$

$$Z_{out} = R_L \parallel \frac{1}{j\omega(C_{\mu 2} + C_{CS2})}$$

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## Summary of Cascode Stage Benefits

- A cascode stage has high output impedance, which is advantageous for
  - achieving high voltage gain
  - use as a current source
- In a cascode stage, the Miller effect is reduced, for improved performance at high frequencies.

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