Lecture 8

OUTLINE

Bipolar Amplifier Topologies (Cont'd)
 Common-Emitter Amplifiers

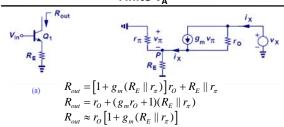
Reading: Chapter 5.3.1

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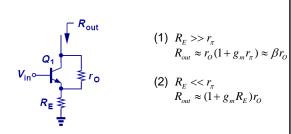
Output Impedance of Degenerated Stage with Finite $\mathbf{V}_{\!\scriptscriptstyle\Delta}$



- Emitter degeneration boosts the output impedance by a factor of $1+g_m(R_F||r_\pi)$.
- This improves the gain of the amplifier and makes the circuit a better current source.

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Two Special Cases

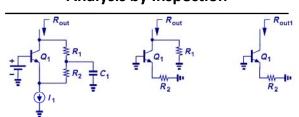


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Analysis by Inspection



 $R_{out} = R_1 \parallel R_{out1} \longrightarrow R_{out1} = \left[1 + g_m(R_2 \parallel r_\pi)\right] r_O \longrightarrow R_{out} = \left[1 + g_m(R_2 \parallel r_\pi)\right] r_O \parallel R_1$

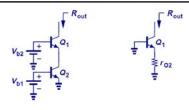
 This seemingly complicated circuit can be greatly simplified by first recognizing that the capacitor creates an AC short to ground, and gradually transforming the circuit to a known topology.

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Example: Degeneration by Another Transistor

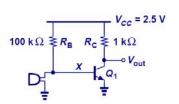


$$R_{out} = [1 + g_{m1}(r_{O2} || r_{\pi 1})] r_{O1}$$

• Called a "cascode", the circuit offers many advantages that are described later in the book.

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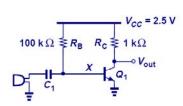
Bad Input Connection



• Since the microphone has a very low resistance that connects from the base of Q₁ to ground, it attenuates the base voltage and renders Q₁ without a bias current.

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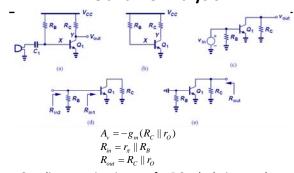
Use of Coupling Capacitor



• Capacitor isolates the bias network from the microphone at DC but shorts the microphone to the amplifier at higher frequencies.

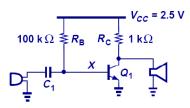
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DC and AC Analysis



• Coupling capacitor is open for DC calculations and shorted for AC calculations.
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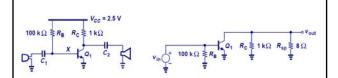
Bad Output Connection



• Since the speaker has an inductor, connecting it directly to the amplifier would short the collector at DC and therefore push the transistor into deep saturation.

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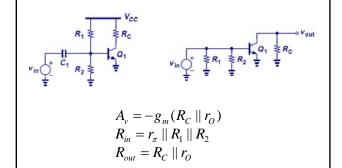
Still No Gain!!!



• In this example, the AC coupling indeed allows correct biasing. However, due to the speaker's small input impedance, the overall gain drops considerably.

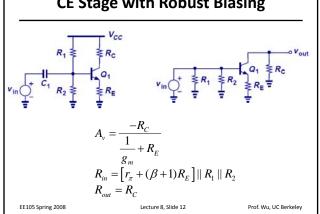
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CE Stage with Biasing



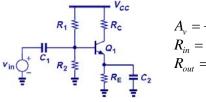
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CE Stage with Robust Biasing



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Removal of Degeneration for Signals at AC

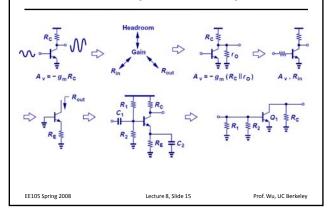


 $\begin{aligned} R_{v} &= -g_{m}R_{C} \\ R_{in} &= r_{\pi} \parallel R_{1} \parallel R_{2} \\ R_{out} &= R_{C} \end{aligned}$

• Capacitor shorts out RE at higher frequencies and removes degeneration.

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Summary of CE Concepts



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