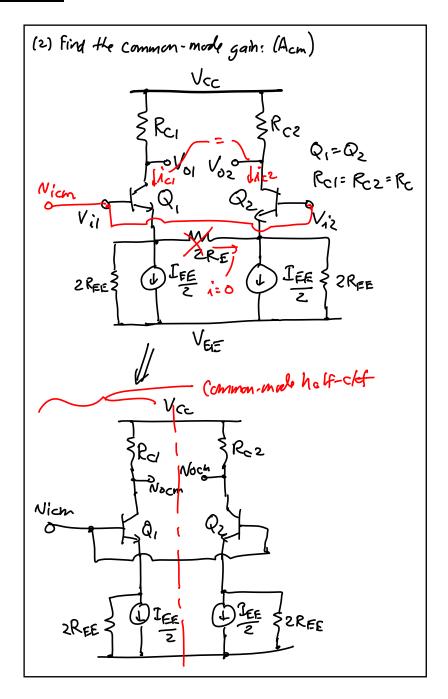
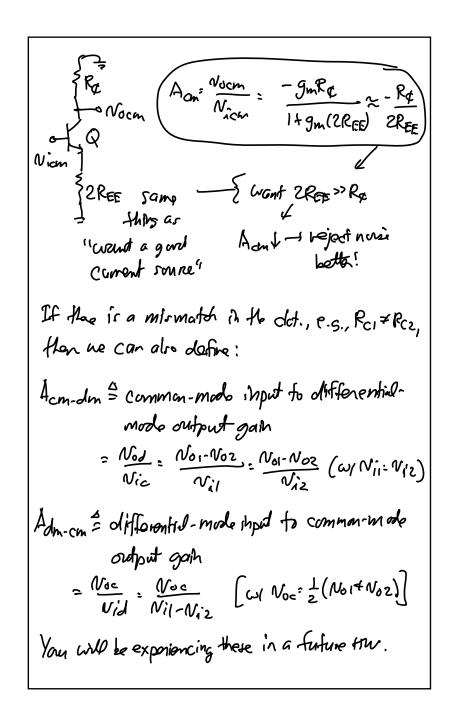
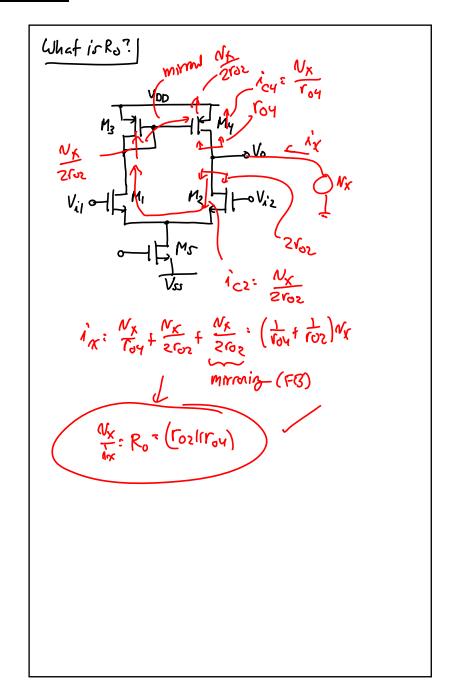


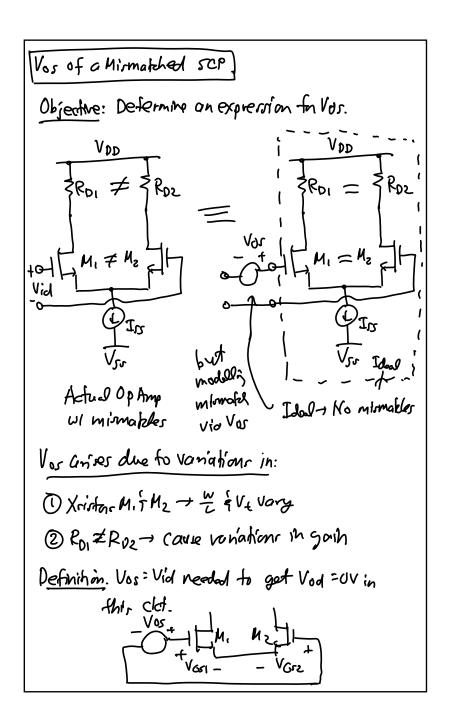
EE 140/240A: Analog Integrated Circuits Lecture 14w: SCP & Current Mirror Load





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kvl:
$$V_{0S} = V_{GS} + V_{GS} = 0$$

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Reorroughy:
$$\begin{array}{c|c}
I_{D1} = I_D + \frac{\Delta I_D}{2} \\
I_{D2} = I_D - \frac{\Delta I_D}{2}
\end{array}$$

$$\begin{array}{c|c}
(w)_1 = (w)_1 + \frac{\Delta(w)_1}{2}
\end{array}$$

$$\begin{array}{c|c}
V_{C1} = (w)_1 + \frac{\Delta(w)_1}{2}
\end{array}$$

$$\begin{array}{c|c}
V_{C2} = I_D - \frac{\Delta I_D}{2}
\end{array}$$

$$\begin{array}{c|c}
V_{C3} = AV_{C4} + \frac{2(I_D + \Delta I_D/2)}{AnC_{C4}(w)}
\end{array}$$

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V_{C3} = AV_{C4} + \frac{2(I_D + \Delta I_D/2)}{AnC_{C4}(w)}
\end{array}$$

$$\begin{array}{c|c}
V_{C3} = V_{C4} = \frac{2I_D}{AnC_{C4}(w)}
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