### EE 140: Analog Integrated Circuits

### Lecture 26: Feedback I

#### · Announcements:

♦ Project is due this Friday, 5/1, at 8 p.m.

#### · Today:

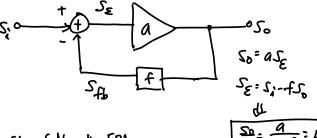
- ♥ Feedback: Pros & Cons
- ⋄ Inspection Analysis of FB Ckts
- ♥ Effect of Feedback on Z<sub>i</sub> and Z<sub>a</sub>

#### Last Time -

- Anished CMOS op amp design

I New Topic (well, not entirely new . \_ but there's a lot feedback more important stuff to know)

> we know this:



# Benefitr of Negative FB1

- O Stabilizer the gain of the amp against parameter changes is active device variations
- 2 Modifies Ri and Ro basically improves their values according to the type of amplifier implemented e.g., voltage amp: Ri:large, Ro: Small

@ output: No Ro 3 Rc (Rocc Rc)

current forvoltage amp: R1: 5mall, R0: 5mall weltage to current amp. R1: large, R0= large current-to-current amp: R1: 5mall, R0=large

- @ Roduer distortion; improves liverity.
- 4) Increases bandwidth (W.3dB).

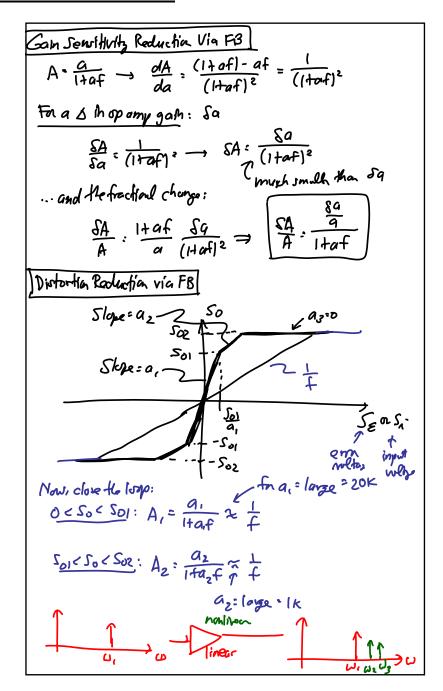
## Diradvantages of Neg. FB |

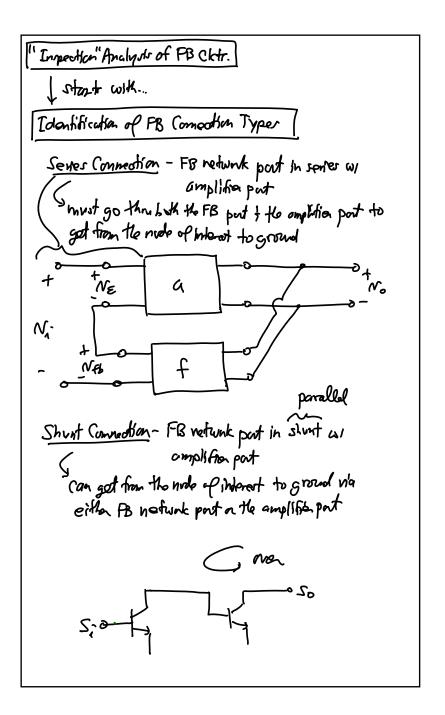
(1) Gain is reduced -> reduction Factor ~ equal to the around of goin stabilization, distortion reduction, et...

Salution: Add more steges of gain - s but the adds cort & power ...

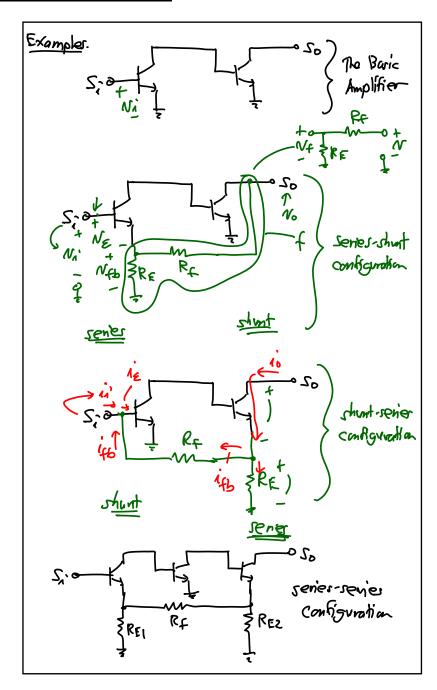
2) Feedback courser stability problems (if not componented property)

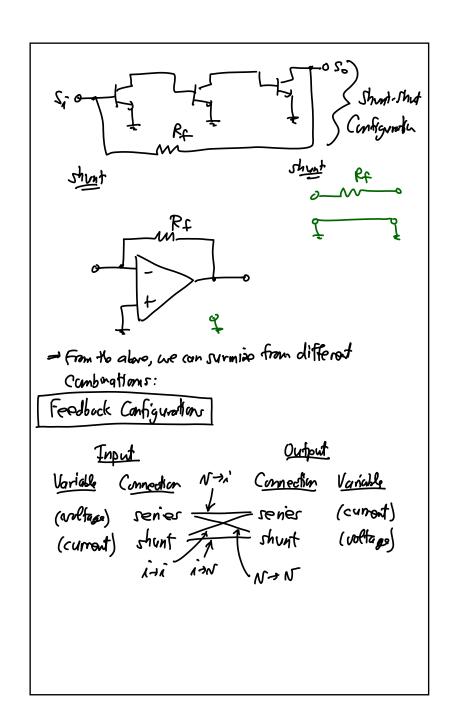
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