

Lecture 17: Slew Rate & Output Stages

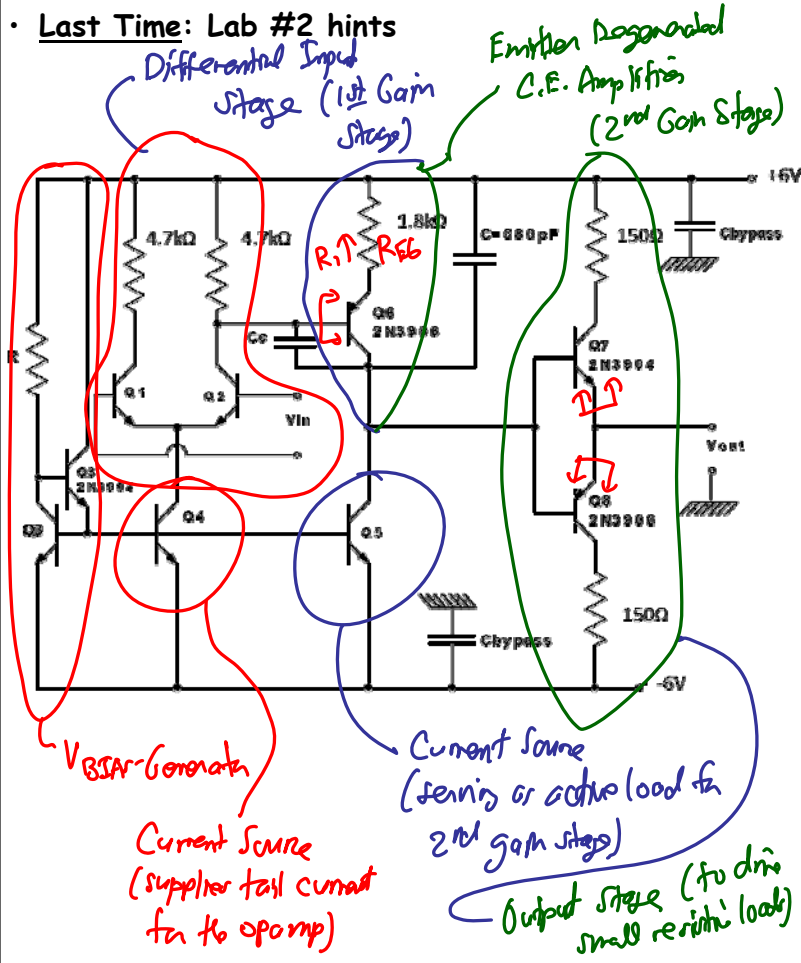
Announcements:

- HW#7 due today; HW#8 online
- Lab#2 due week of 3/19 (next week)
- Midterm Exam next Thursday, 3/22

Lecture Topics:

- Slew Rate; Output Stages

Last Time: Lab #2 hints



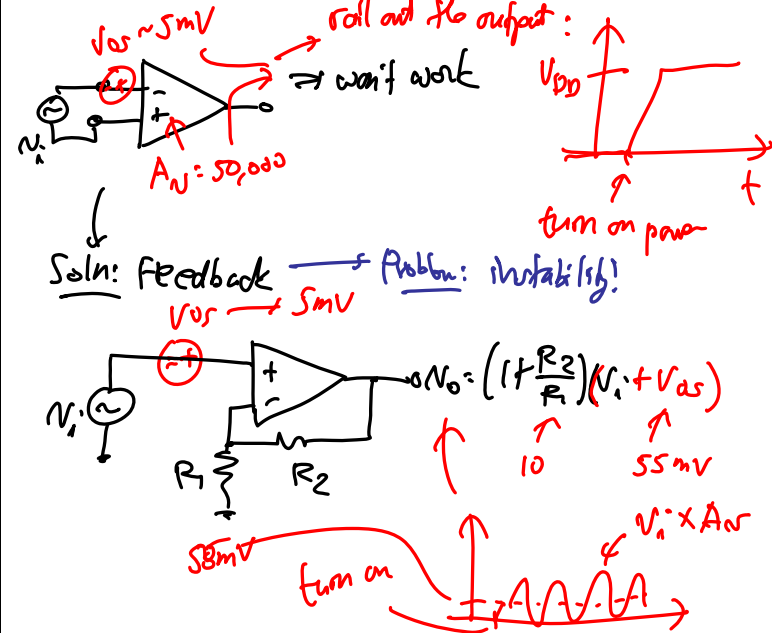
Remarks.

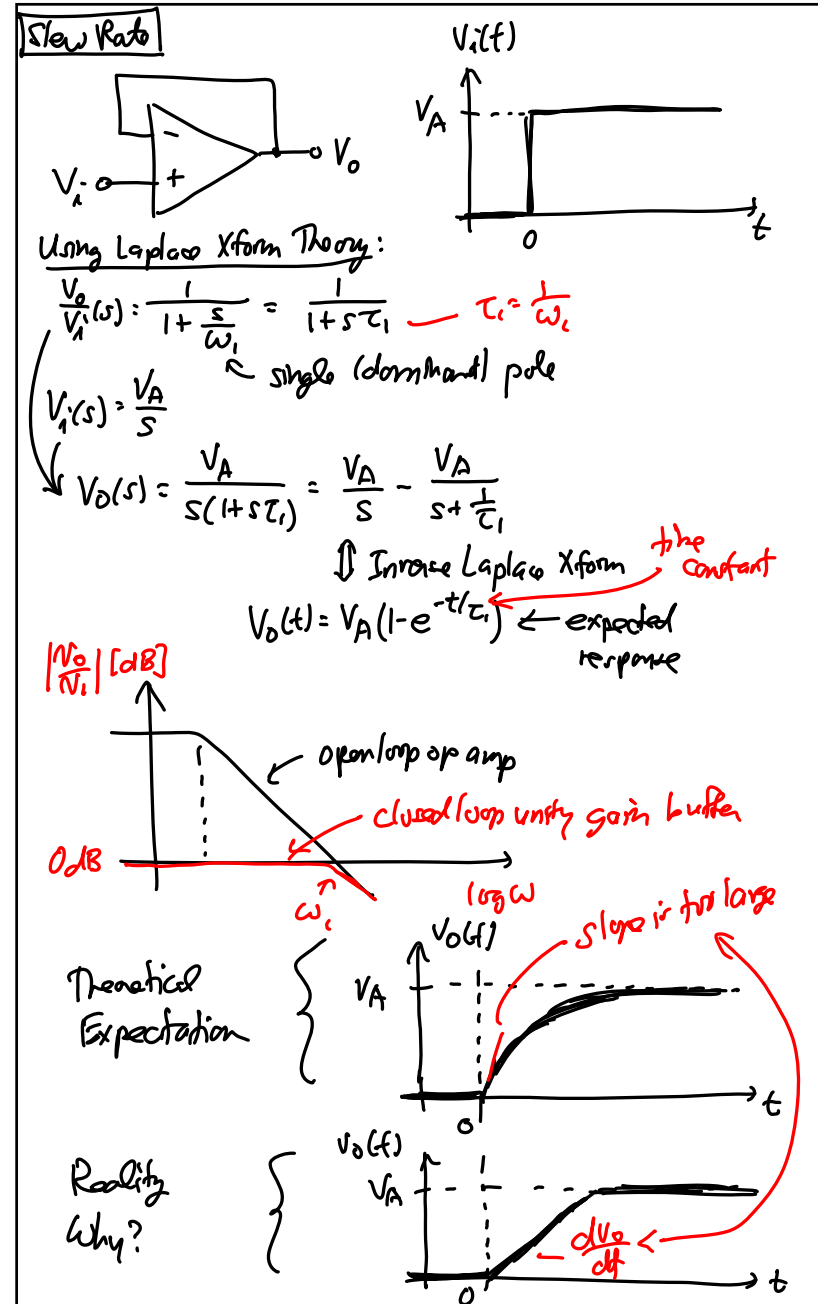
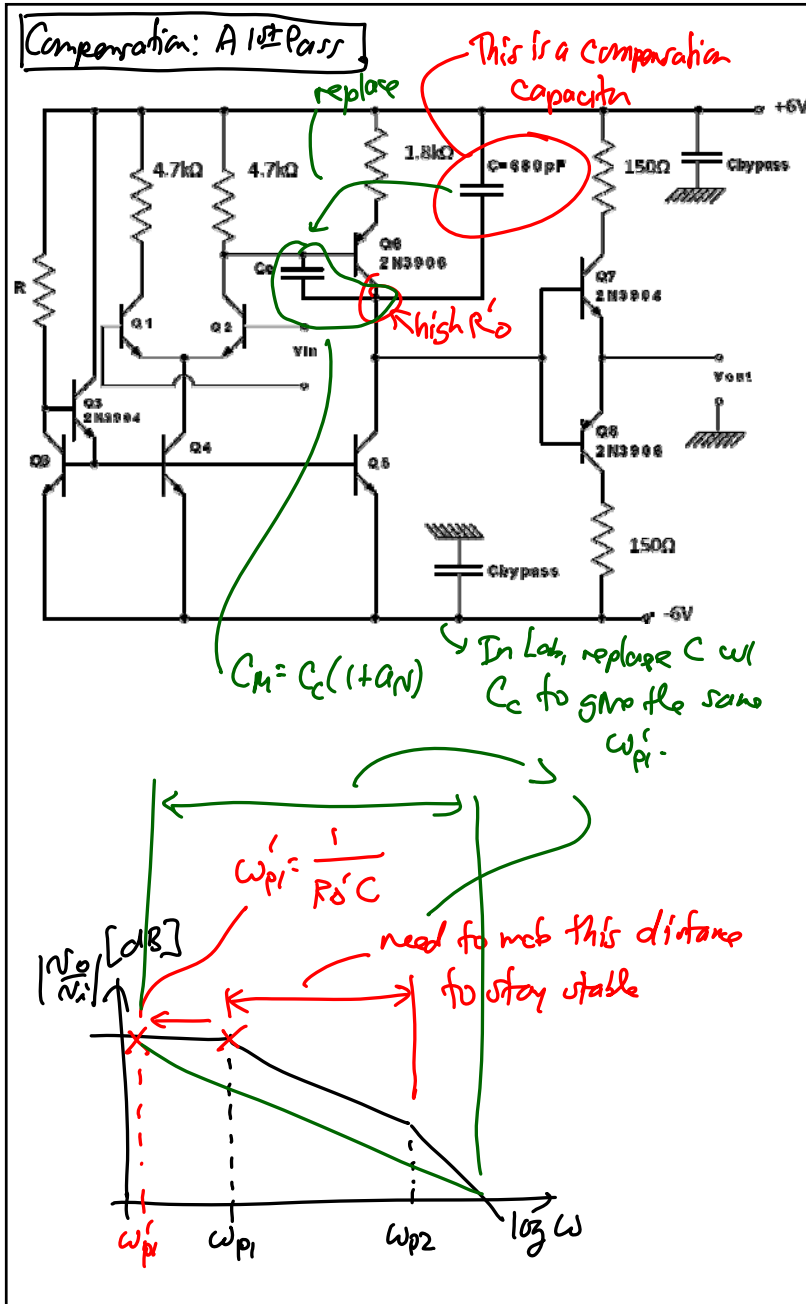
- You analyze this in Lab#2.
- Usually, the resistively-loaded diff. pair is replaced w/ an active current mirror load for more gain.
- R_{E6} raises the input R of Q_6 (of the 2nd gain stage), plus helps w/ biasing.
- Same comment as (3) for the output stage.
- Output stage needed when driving a resistive load

often the case for bipolar

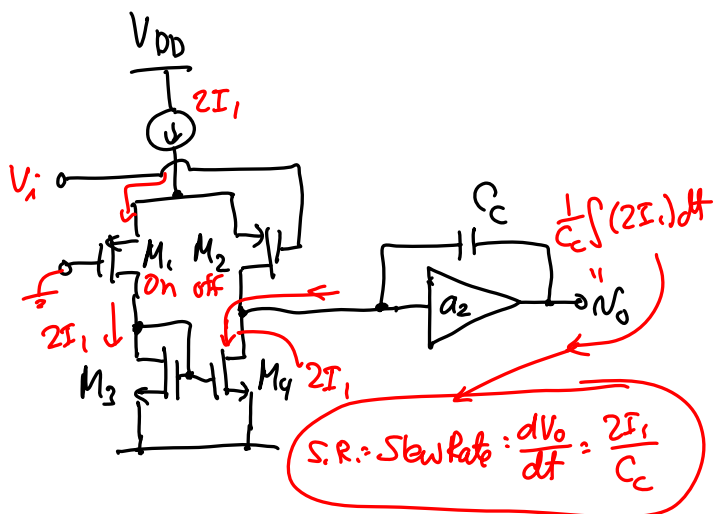
not often the case for MOS, where a capacitive load C_L is often more relevant → MOS op amps often don't need output stages!

How do we use Op Amps?

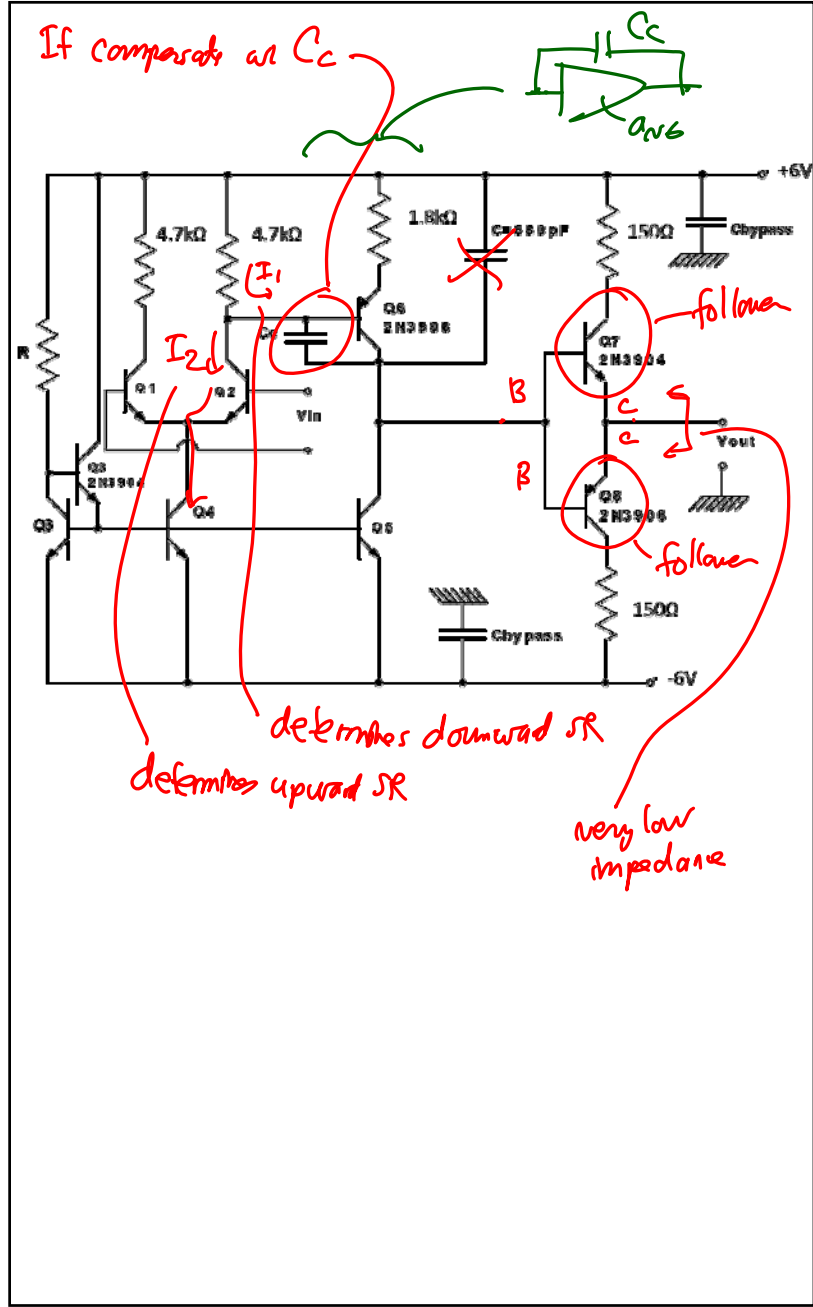
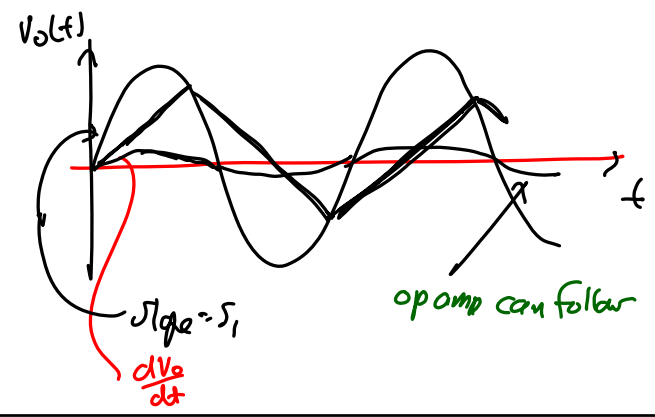




Reasons 1st or 2nd stage of op amp cannot source enough current to mimic the slope (or speed) of a fast rising input signal



Ex. If apply a very fast (i.e., high freq., large amplitude) sinusoid:



Output Stages

- Class A (Emitter or Source Follower)
- Class B
- Class AB (we'll do this one later)

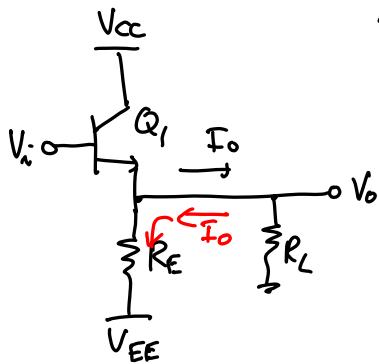
Purpose: Drive loads

- ① Deliver power w/ small distortion.
- ② Minimize output impedance \rightarrow so that the amplifier gain is insensitive to the load.

Desirable Attributes:

- ① High R_{in} ; Low R_{out} .
- ② Low quiescent power.
- ③ Minimal effect on the amplifier freq. response.
- ④ Should be able to handle large input/output swings. (i.e., V_i may be $> V_T$, invalidating small-signal approximations)

Emitter Follower (Class A)



Two main cases:

- ① $I_o > 0$: I_o comes fr/ Q_1
 \Rightarrow adequate I_o can be supplied so long as Q_1 stays in forward active

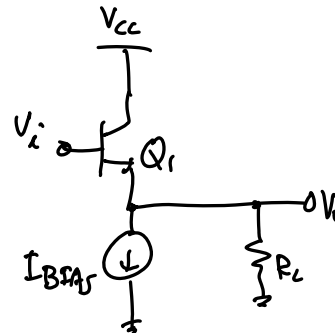
② $I_o < 0$: (i.e., $V_o < -1$)

I_o must be sunk to V_{EE} through R_E

\downarrow
 $I_o = \frac{V_o - V_{EE}}{R_E} \rightarrow |I_o|$ gets smaller as $V_o \downarrow$

Problem!

Solution: Replace R_E w/ a current source.



Now can source I_o through Q_1 for $V_o > 0$.

\downarrow
 And can sink $I_o = I_{BIAS}$, when I_o stays $= I_{BIAS}$ as V_o changes.

Actual Implementation:

$R_i = r_{\pi} + (\beta + 1)(r_{o1} || r_{o2} || R_L)$
 $R_o = \frac{1}{g_m}$

$KVL: V_i = V_o + V_{be1}$
 In general: $V_{be1} \neq \text{const} = V_T \ln\left(\frac{I_{C1}}{I_{S1}}\right)$
 (for Q in FBR)

$I_{C1} = I_o + I_{Q2} = I_{Q1} + \frac{V_o}{R_L}$
 $\therefore V_i = V_o + V_T \ln\left(\frac{I_{Q1} + \frac{V_o}{R_L}}{I_{S1}}\right)$

Two Cases:

Case 0: $R_L = \text{large}$

$\rightarrow I_o$ not Δ 'ing much to deliver large excursions in V_o
 $\hookrightarrow I_{C1}$ not Δ 'ing much

For $V_i = \text{large}$ and (1): Q_1 must source $I_o + I_{Q2}$

$V_o = V_i - V_{be1}$

@ some pt., Q_1 will saturate as $V_o \uparrow$
 \downarrow
 defines V_{omax}

$V_{omax} = V_{cc} - V_{CE(sat)}$
 \downarrow
 and $V_i = V_{cc} - V_{CE(sat)} + V_{be1}$