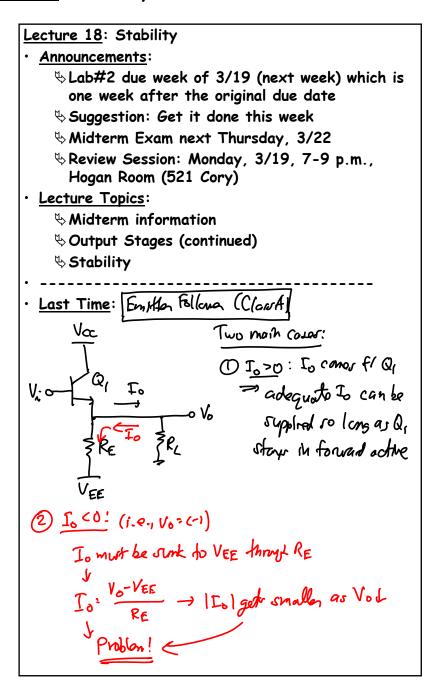
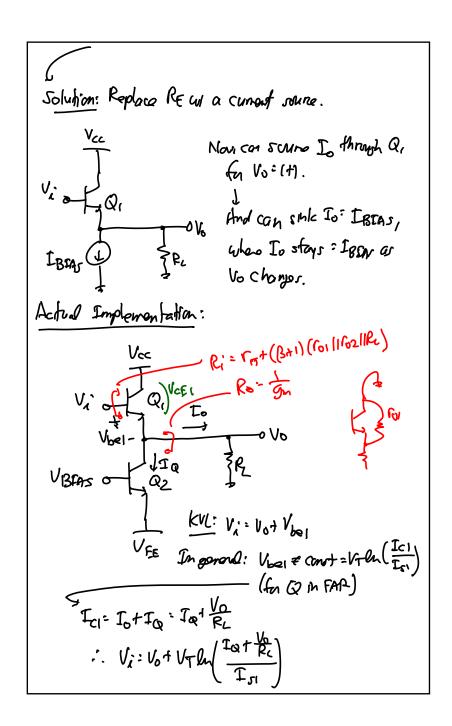
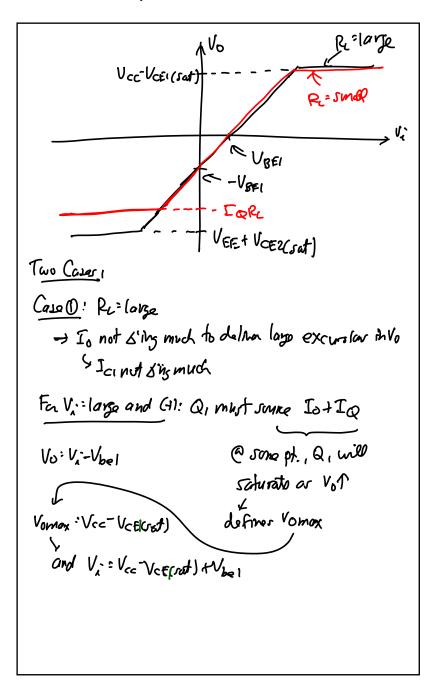
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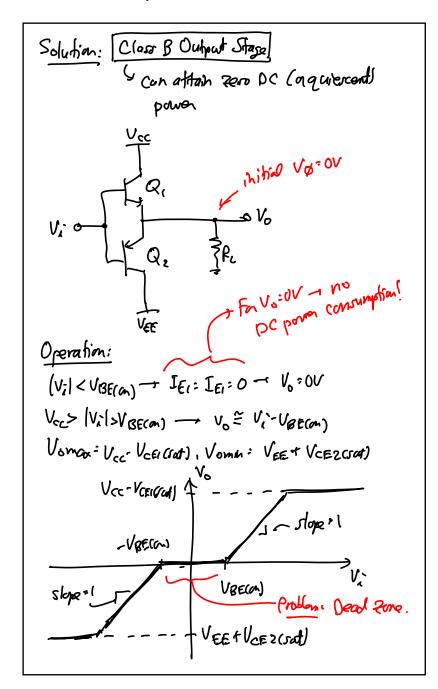


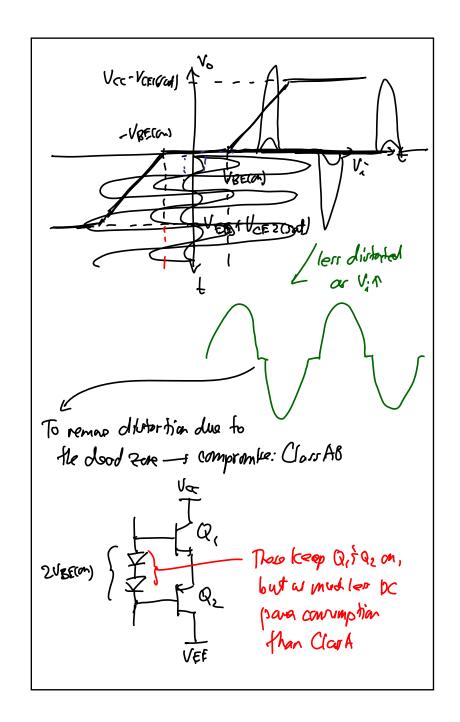


Lecture 18w: Stability



For Vi-large and (): Vo follow Vi until Qz Saturates Vomin "VEE + VCEZ(SWT) 5.2V V. : Vo + VBEI : VEE+ VCEZ(SWT) +VBEI Case 2: Re: small - thur, To can be lorge! For V: (4) and logo: Q, can some as much current as readed until it scalardos for it fros) For Vi=(-) and large: Vo: + ToRL - min. Vo= - IaR = Q cut off (Ic1:0) → Vo clongs @ -IQR Efurta docrows in Vi - no Din Vo fix by molding Iq longe Sproken: too much power consumption PQ: (Vac-VEE) IQ - DC pour consumption! VEE: (-) W Clook, if you want a large output swift w/ Ri2small, You must consume power!





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