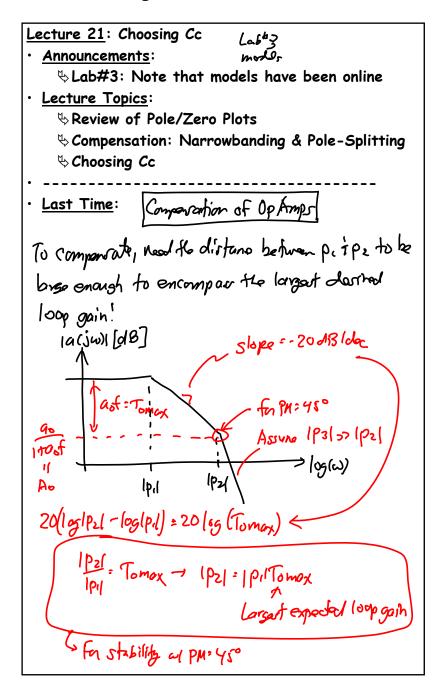
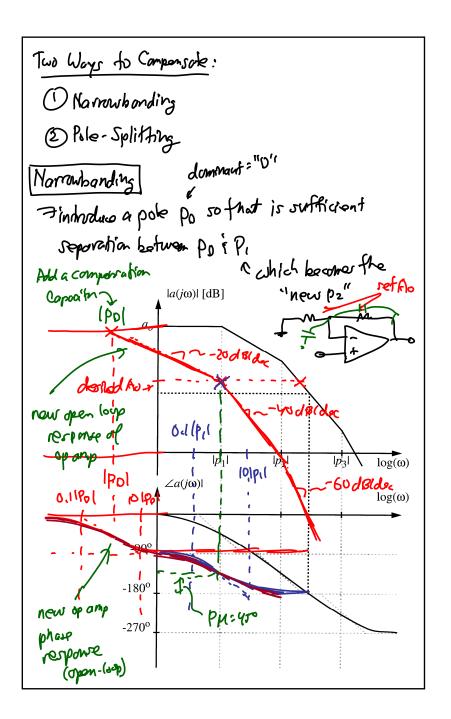
EE 140: Analog Integrated Circuits

Lecture 21w: Choosing Cc

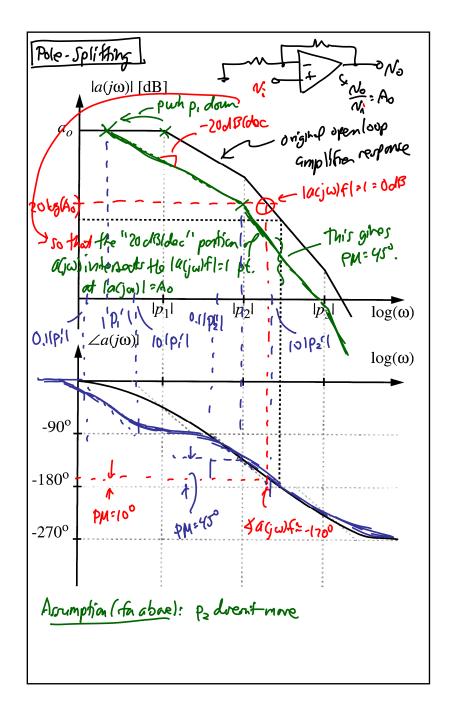




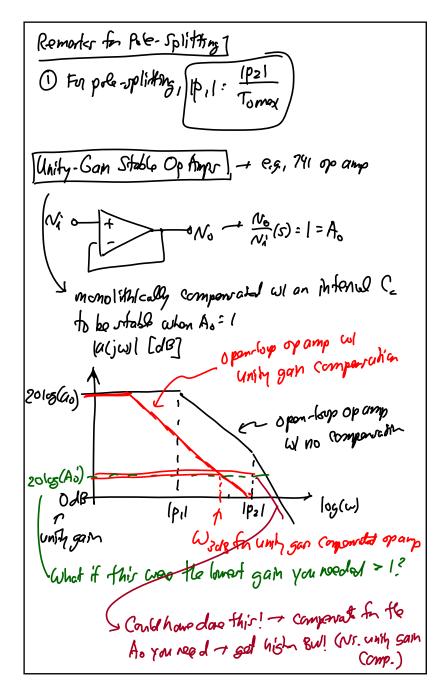
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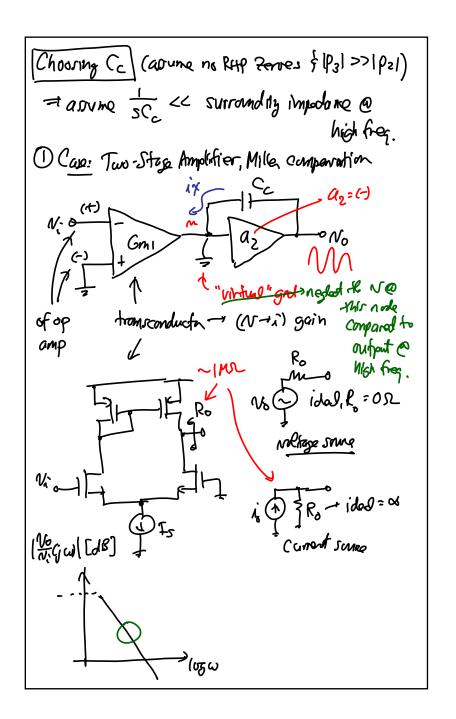
Lecture 21w: Choosing Cc

Remarks on Narrow banding 1 1) Assumption: PI,P2,P3 dail more when Po is Minduod (often not true, but their mound isn't that big) 2) Summarize choose PD such that IT(jw) = OdB=1@p, (which becomes the "new 2" most dominant pole") 4 this gives PM=450 (for 1p21 > 1p1) i [p31 >> 1p2] 3 Why do this? Wouldn't it be mud better to just more the original IPI (i.e., pole-split) & Do it when you have no other choice, e.g., when you have a pockaged opening & hore access only to a few termonals, not the optimum componsation node. [PD] = [PI] | maximum expected/neodal 4 loop gain Problemi O often, IPDI << IPI : f-3de BW of the or our will be very much 2 Woodlop = Ipil which in that large Solution: Pole-Splitting more ipil down t either leep Ipel still u mare Ipel up simultaneously after doing this: 1 W-3dB = 1P11 (2) Wchondlop = 1P21



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