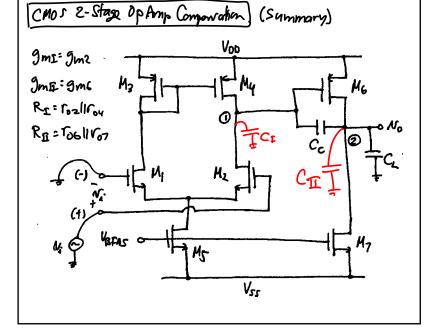
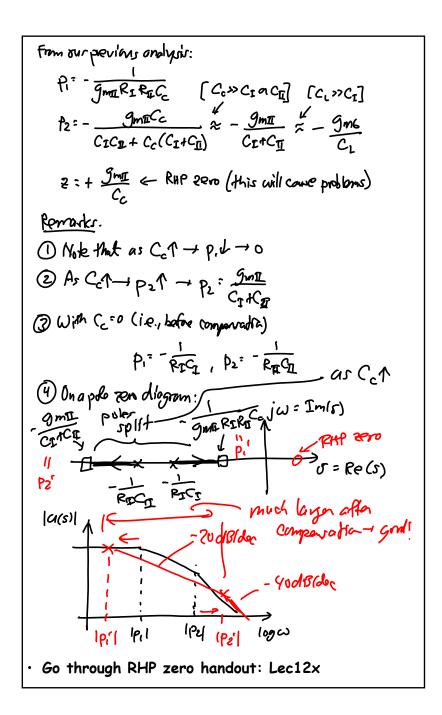
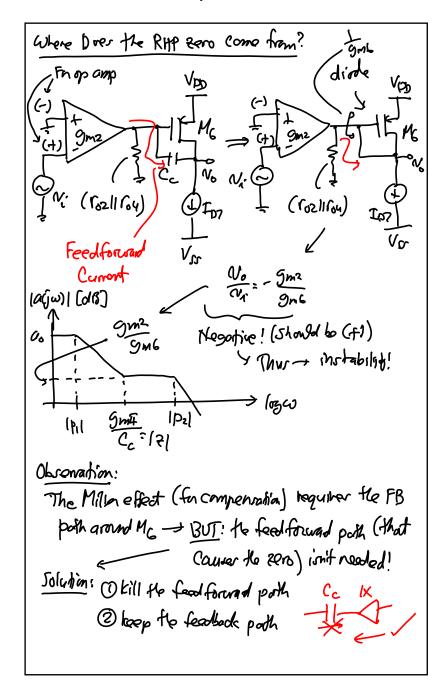


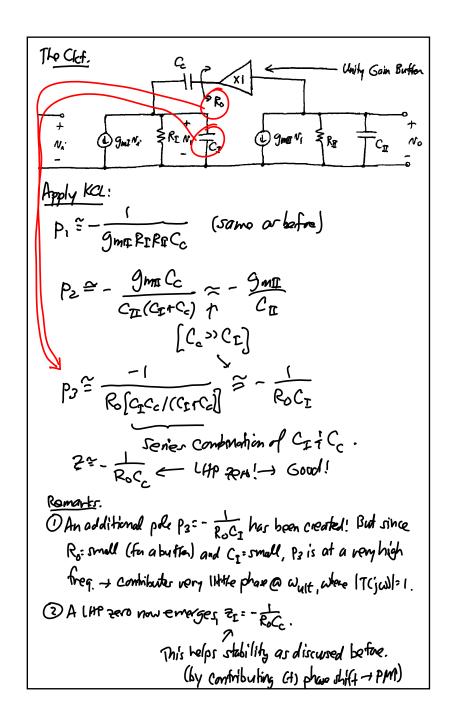
- · Announcements:
- · Design Project Checkpoint:
 - ♦ Due Monday, April 23, 11:59 p.m.
 - Send to your TA a spice file for your op amp design that simulates correctly, i.e., that reaches a stable bias point where all transistors are saturated (or linear if an MOS resistor)
 - ♦ It doesn't need to meet the project specs, but
 it should simulate correctly
- · Lecture Topics:
 - SRHP Zero
 - Shulling the RHP Zero
- -----
- Last Time:



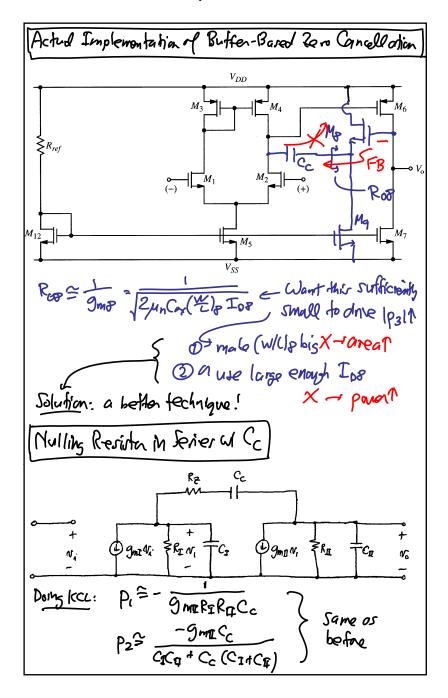


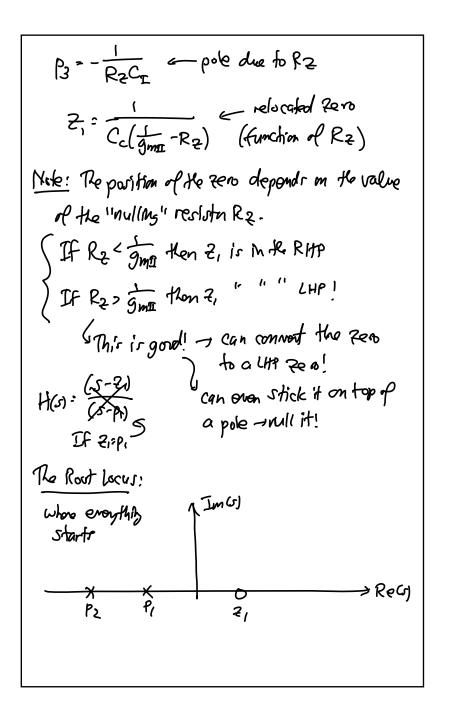
<u>EE 140</u>: Analog Integrated Circuits <u>Lecture 23w</u>: Practical Compensation





<u>EE 140</u>: Analog Integrated Circuits <u>Lecture 23w</u>: Practical Compensation





<u>EE 140</u>: Analog Integrated Circuits Lecture 23w: Practical Compensation

