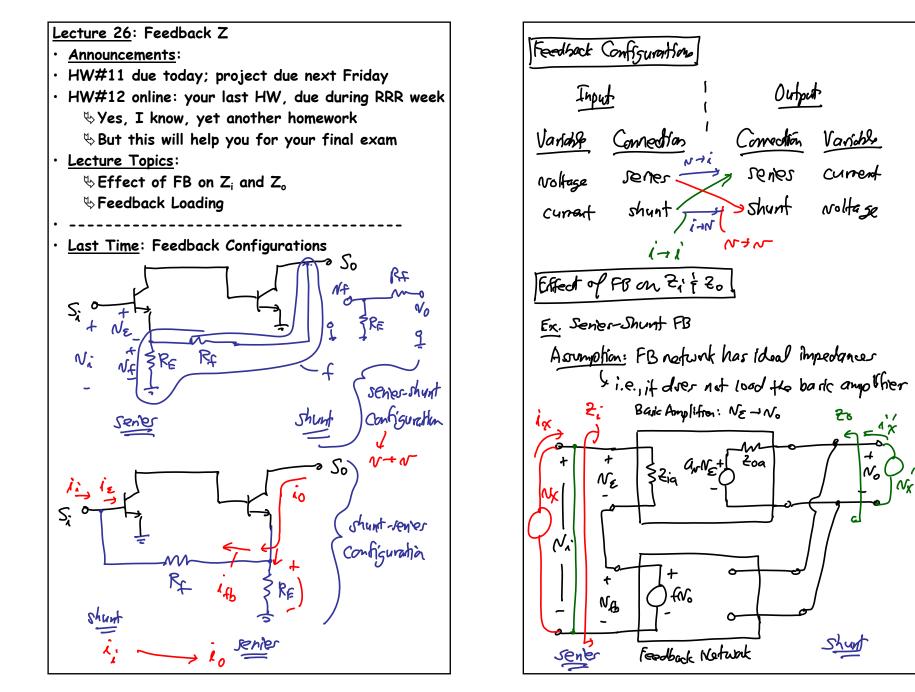
<u>EE 140</u>: Analog Integrated Circuits <u>Lecture 26w</u>: Feedback Z



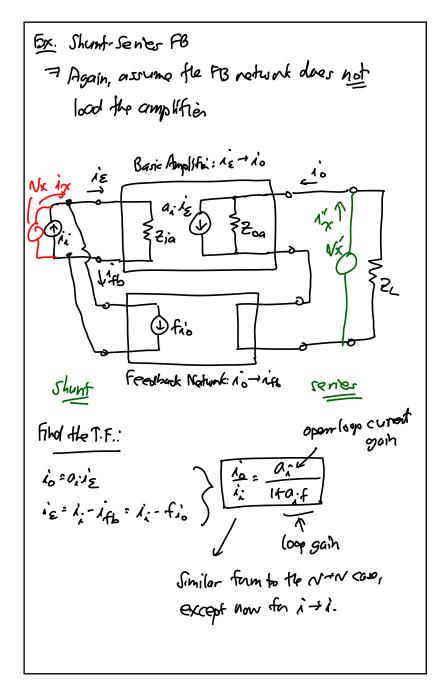
$$\frac{Filed \#e T.F. -}{N_{\Xi} = W_{1} - N_{Fb}} \xrightarrow{N_{0}: a_{N}(W_{1} - f_{N_{0}})} = \frac{W_{0} = \frac{a_{N}}{N_{1} = 1 + 6_{N} + f}}{N_{E} = f_{N_{0}}} \xrightarrow{N_{E}} = \frac{W_{X}}{N_{K}} = \frac{a_{N}}{N_{K} = 1 + 6_{N} + f}}{(ar expected)}$$

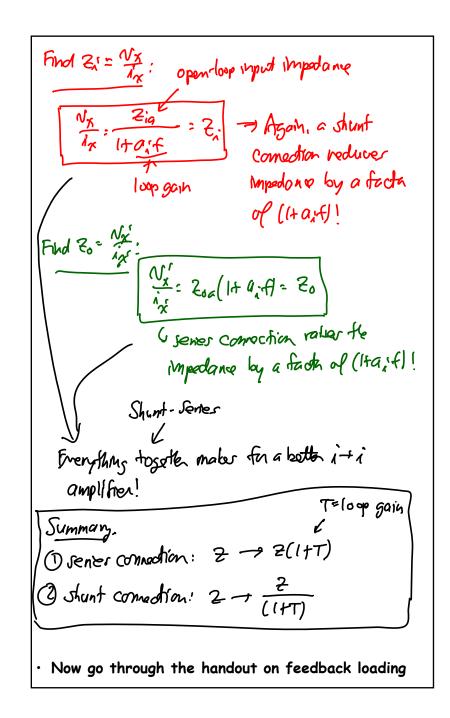
$$\frac{Filed Z_{1} = \frac{N_{X}}{n_{X}}}{n_{X}} = \frac{N_{X}}{n_{X}} =$$

Find
$$Z_0 = \frac{V_x'}{i_x'}$$
: (we imput shorted)
 $V_{\Sigma} + N_{Fb} = N_{\Sigma} + for i = 0 - 1$ $N_{\Sigma}^* - for x'$
 $i_X : \frac{U_X' - a_V N_{\Sigma}}{2o_A} = \frac{N_X' - a_V f N_X'}{2u_A}$
 u
 $U_X' := \underbrace{\frac{2u_A}{1 + a_V f}}_{i_X} : 2u_A$
 $i_X' := \underbrace{\frac{2u_A}{1 + a_V f}}_{i_X f} : 2v_A$
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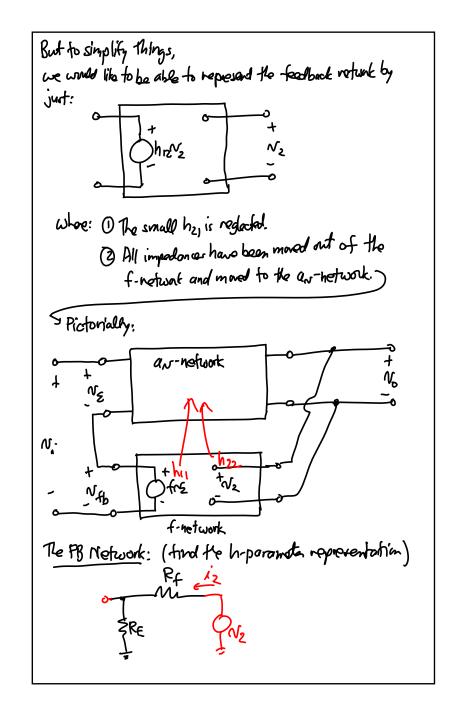
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Determine the Pis Loading of an Amplific Example. Non-Inventing Amplier , Short NO -co No Λ NE N <u>ΛΛΛ</u> Rr This is the FB network. Open sener (noltage shunt (volter) classed loop gath Objective: Use Ao= <u>un</u> to get Ao. T A .. In such to we this equation, we must know (i) an = gain of the amplifie (ii) f ≥ gain of the feedback (also, called the feedbook factu) In general: NAD ~*∿* ≡ + N₁ = ^N& Rf Phair $h_{2} h_{2} h_{3} h_{3}$ U hiz ve h-parchana notwork



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