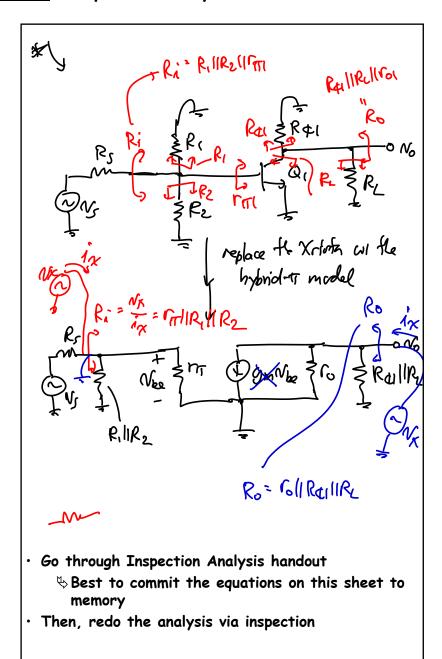
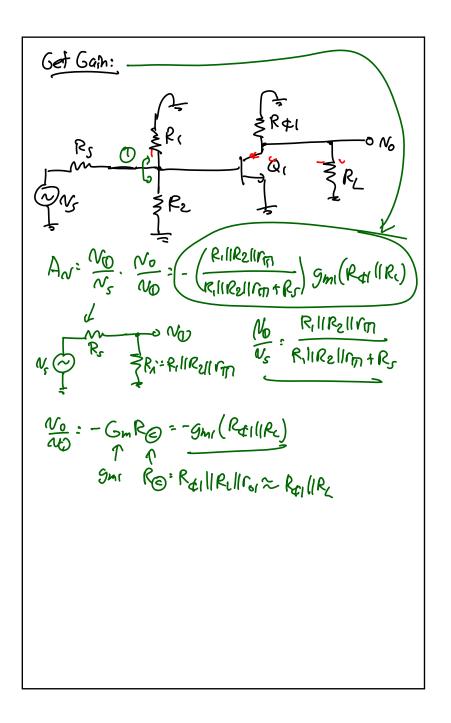
EE 140: Analog Integrated Circuits Lecture 4w: Inspection Analysis

Lecture 4: Inspection Analysis Announcements: Inspection formula sheet handed out in class ♥ It's also online Some issues with HW#1 ♦ Problem 1: junction area = 1e-5 cm² \$Problem 4: some missing info for the MOS transistor ♦ A fix will be posted Lecture Topics: \$ Procedure for Small Signal Analysis \$1-Tx Amplifier Examples Procedure for Small-Signal Knalyn's (a quick run Ex. Discusto Common Emith Ckt. Raj Cai=large

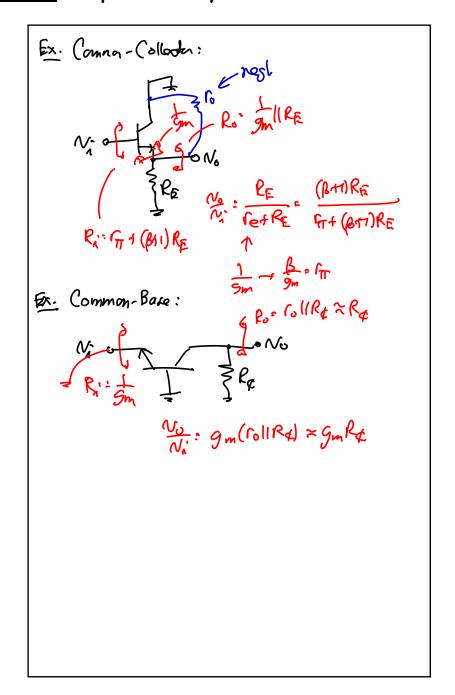
Procedure: 1) find the DC grenoting pt. - get wetaser f currents at all node fall branchos, respectively 2 Defermine the small-signal (s. s.) poremeters for devicer in the signal path (e.g., gm, rin ...) (3) Convert the full cht. to the S.S. cht. => Zero out to do sources DC voltage source - short => Short out loss capacitas model (hybridat) (4a) If needed, replace the Xsista aritis Is. Class. -> this should NOT be needed often => when is it needed? - generally, in Cases where flow is feedback! (46) Analyze by inspection based on prin 5.5. analysis exposeries! I this should be 97% of the fire. hybrid-T.

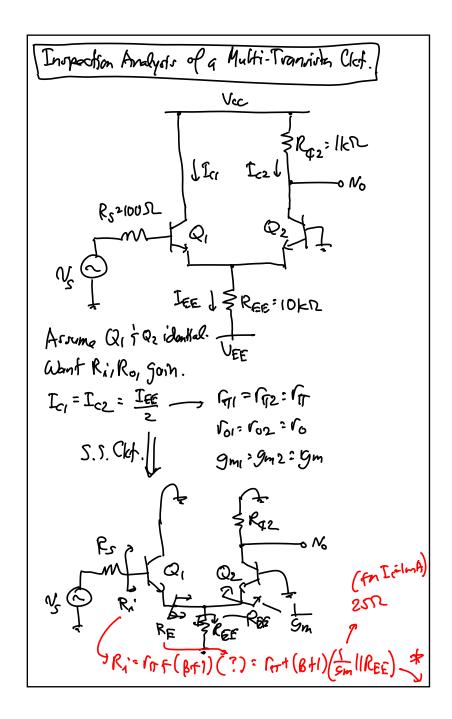
EE 140: Analog Integrated Circuits Lecture 4w: Inspection Analysis





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Pri: Firt (Bf1) fm => Rr=2Fir