

Midterm 1 in class  
 Lab 2 due Tues  
 Lab 3 - part 1 tomorrow  
 HW 4 Due Wed

Friday 2/16

for last time: 4 different biases  
 4 wildly different  $g_m, A_v, v_{ov}$   
 what if PMOS is input?  
 $g_m, v_o, A_v, v_{ov}, v_a$ ? no chap.

P.S.  
 $1.4 \rightarrow \frac{2V}{10} = 0.2V$   
 $0.6 \rightarrow \frac{5}{10} = 0.5V$   
 $100ff$

$C_{in} = C_{gs} + C_{gd}$

in saturation, quadratic model

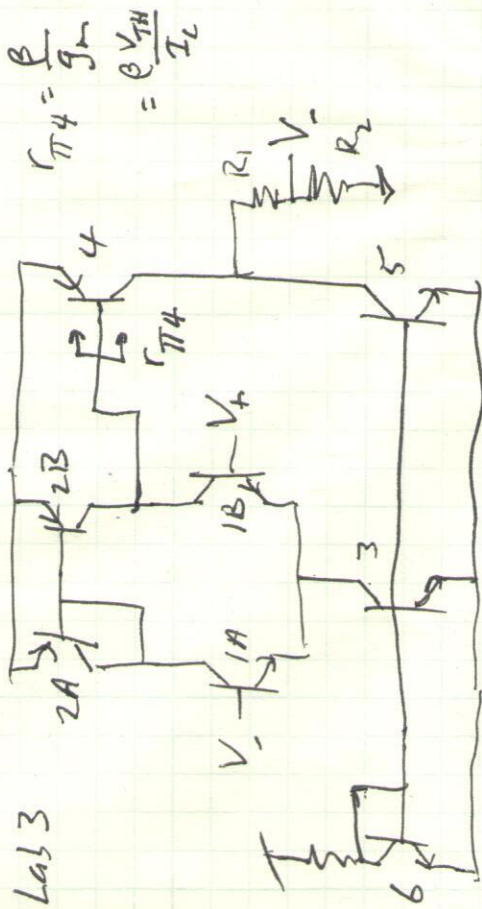
$$C_{gs} = \frac{2}{3} W L C_{ox} + W C_{oe}$$

$$= \frac{2}{3} (5\mu)(0.5\mu) \left(\frac{5ff}{0.5\mu}\right) + (5\mu\mu)(0.5ff/\mu\mu)$$

$$= 8ff + 2.5ff \approx 11ff$$

$C_{gd} = W C_{oe}' = 2.5ff$

Lab 3



$r_{\pi 4} = \frac{\beta}{g_m}$   
 $= \frac{\beta V_{TH}}{I_L}$

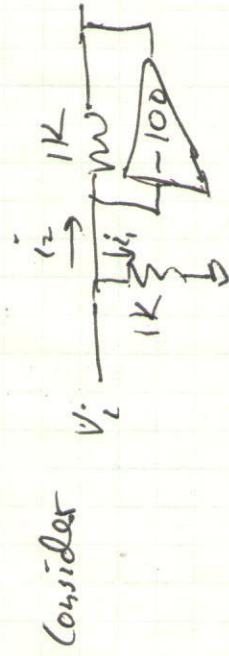
Diode connected  
 $I_C = \frac{V_{CC} - V_D}{R}$

$A_v = g_{m1} R_{o1}$   
 $R_{o1} = r_{o2B} || C_{o1B} || r_{\pi 4}$

$A_{v2} = -g_{m4} R_{o2}$   
 $R_{o2} = C_{o4} || r_{o4} || (R_1 + R_2)$

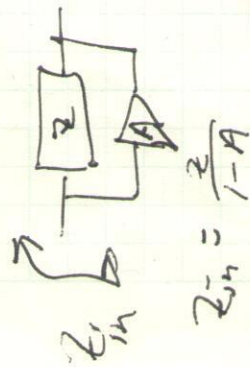


intrinsic  $\rightarrow$  can't make device work w/o it  
 extrinsic  $C_{oe}'$  parasitic overlap/fringe field.  
 $C_{in} = \text{sum? NO. Miller}$



Say  $V_i = 1mV \sin \omega t$   $i_1 = 1\mu A \sin \omega t$   
 $i_2 = 10 \mu A \sin \omega t$ !

negative gain increases voltage amount  
 while passing is recover it, by (1-A)  
 => increases current by 1-A  
 => decreases output impedance by 1-A

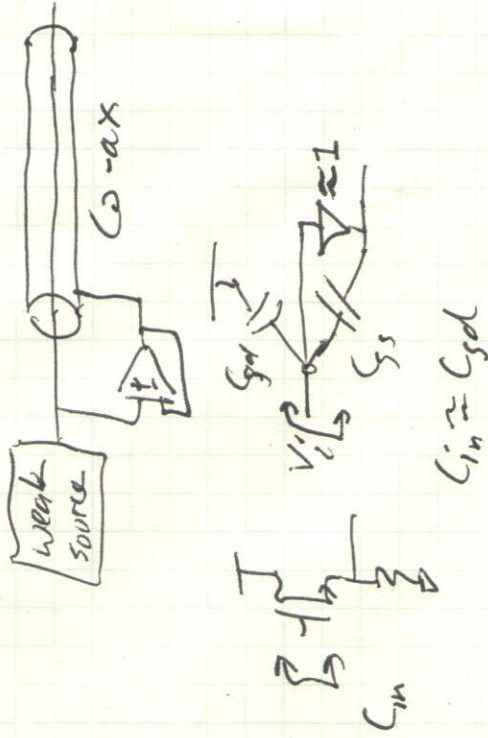


for capacitors, since  $|Z| = \frac{1}{\omega C}$   
 makes C look bigger  
 by 1-A

for CS, A is negative, and usually large.  
 e.g. when  $V_{ov}$  was 100mV in our design  
 $A = -100$  so  $C_{in} = C_{gs} + (1-A)C_{gd}$

$C_{in} = 18 \text{ fF} + (1 - (-100)) (2.5 \text{ fF})$   
 $= 260 \text{ fF}$  dominated by  $C_{gd}$   
 if you need to reduce  $t_{in}$ , what to do?  
 reduce W while keeping  $g_m$  constant

A can be positive



$$I_m = \left\{ \begin{array}{l} \frac{2 I_D}{V_{ov}} \\ \mu C_{ox} \frac{W}{L} V_{ov} \\ \sqrt{2 \mu C_{ox} \frac{W}{L} I_D} \end{array} \right.$$

eqn. 2.18-20

- decrease W 2x  $\downarrow$   $g_m$  const
  - => increase  $V_{ov}$  2x (hurts swing)
  - => increase  $I_D$  2x (more power)
  - => decrease  $\tau_0$  2x
  - => decrease  $A_{vo}$  2x may not be OK
- $\omega_{p2}$  w. unchanged



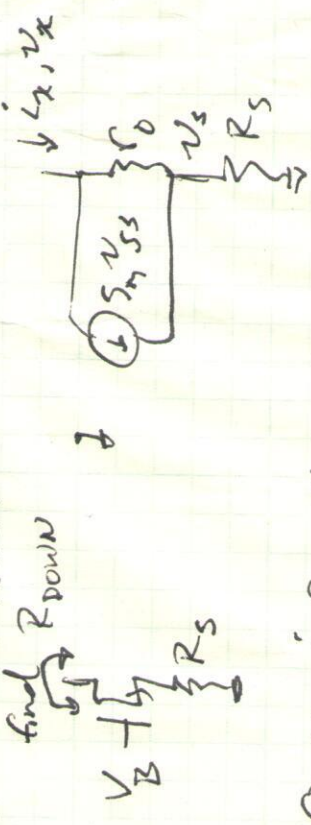
double $\frac{w_c}{2}$	double $\frac{w_c}{2}$	double $\frac{w_c}{2}$	double $\frac{w_c}{2}$
$g_m$	$r_o$	$I_D$	$A_v$
$w_p$	$w_p$	$w_p$	$w_p$
$C_{in}$	$C_{in}$	$C_{in}$	$C_{in}$
2 copies of same amp.			

Need high gain, but low input cap  
 $V_{OV} \uparrow$   
 $L \uparrow$

Need big  $w_p$  and high gain  
 $V_{OV} \uparrow$   
 $L \rightarrow$

Need new topologies  
 more gain: multiple stages or cascode.

More 105 review:



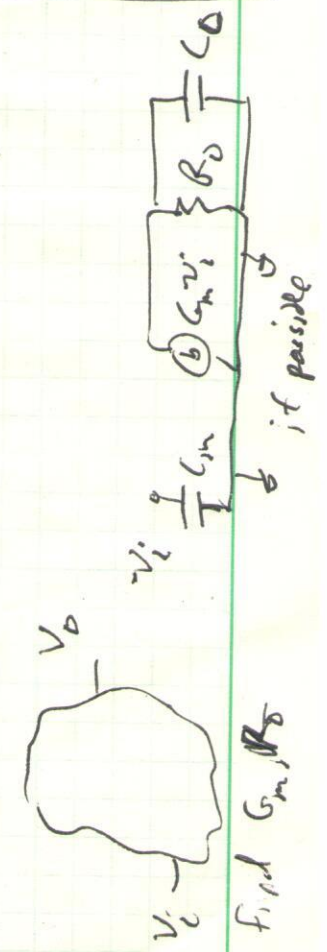
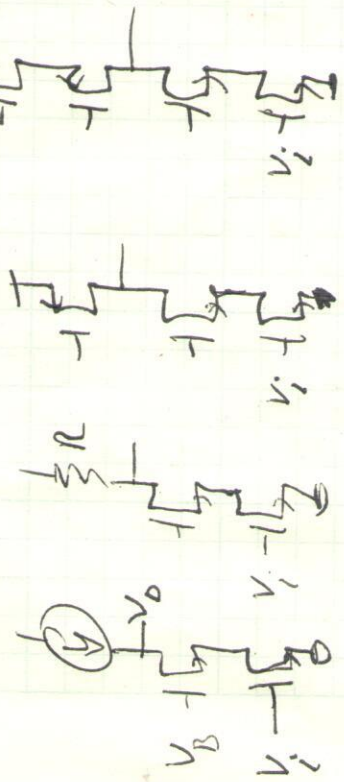
- ①  $v_S = i_x R_S, v_{SS} = -v_S$
- ② KCL @  $v_x$

$$g_m(-v_S) + \frac{1}{r_o}(v_x - v_S) = i_x$$

$$\frac{1}{r_o}v_x = (g_m R_S + \frac{1}{r_o} R_S + 1)i_x$$

$$R_{down} = \frac{v_x}{i_x} = r_o \left( 1 + (g_m + \frac{1}{r_o}) R_S \right) = r_o \left( 1 + \frac{g_m R_S}{1 + \frac{1}{g_m r_o}} \right)$$

Cascode



Find  $G_m, R_o$  if possible