

Lecture 9: Lithography III / Oxidation I

• Announcements:

↳ HW#4 online

↳ Module 2 on Oxidation online

• Lecture Topics:

↳ Masks & alignment

↳ Lambda design rules

↳ Four main components of lithography

— Radiation source

— Mask

— Photoresist

— Exposure system

↳ Resolution

↳ Linewidth control

↳ Alignment accuracy

↳ Oxidation

↳ Oxidation Theory

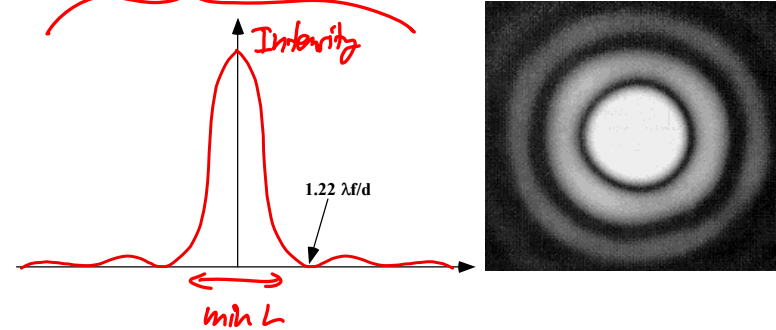
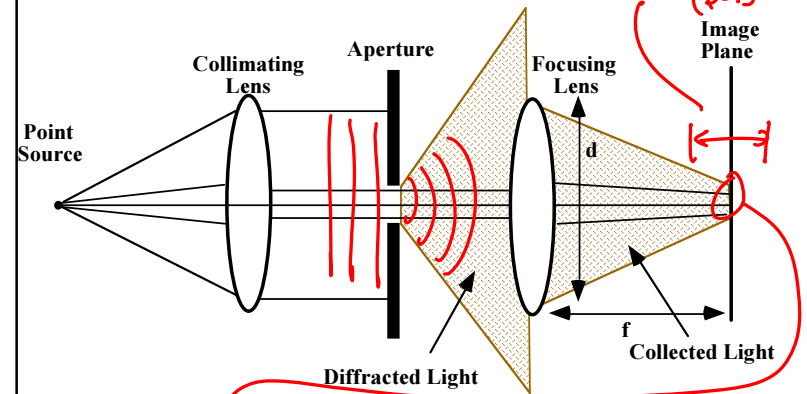
↳ Oxidation Graphs

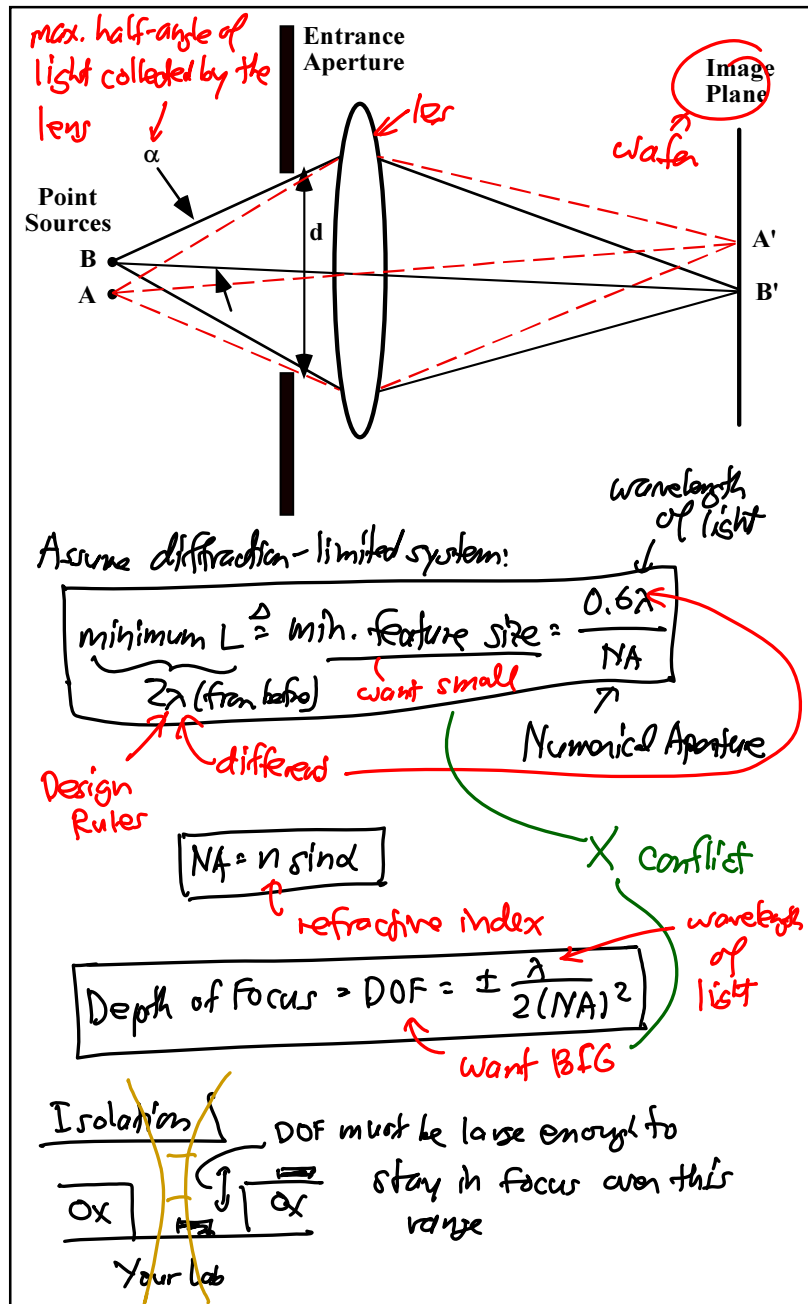
↳ Dopant Redistribution During Oxidation

• Last Time:

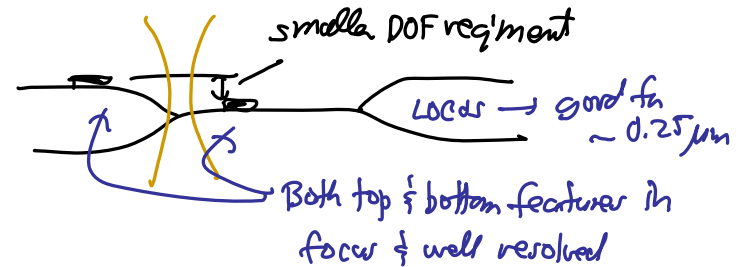
over

Resolution (for projection lithography)

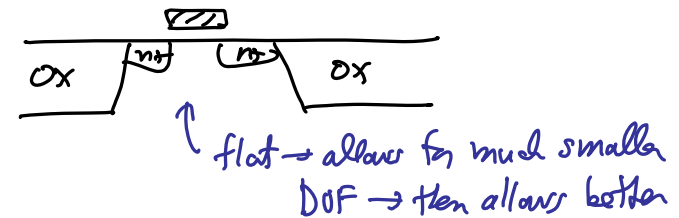




More advanced than your lab \rightarrow LOCOS



Laser CMOS: (at least more modern)

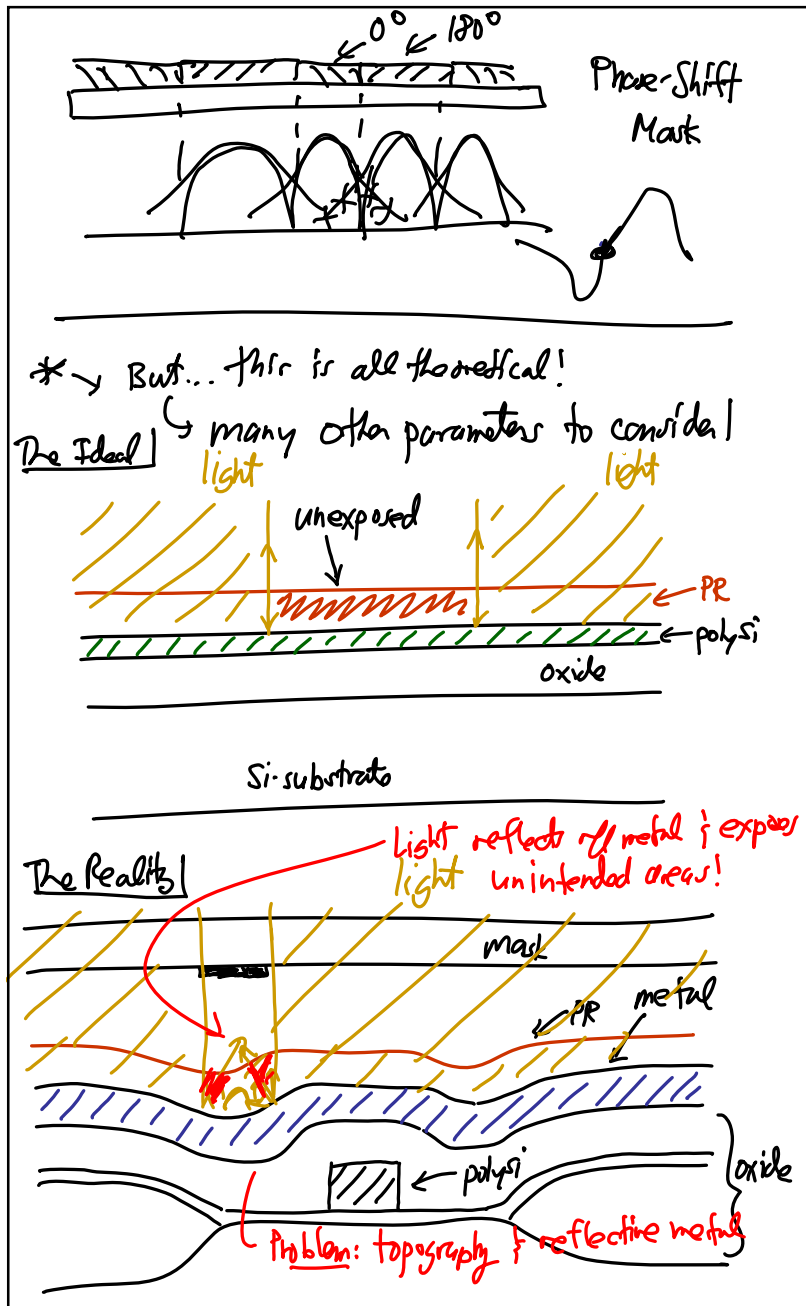


Ex. $\lambda = 436 \text{ nm}$ (g-line) $\left. \begin{array}{l} \text{min. } L \\ NA = 0.3 \end{array} \right\} \rightarrow \text{min } L = 0.9 \mu m$
DOF = $2.7 \mu m$

Ex. $\lambda = 200 \text{ nm}$ $\left. \begin{array}{l} \text{min. } L \\ NA = 0.5 \end{array} \right\} \Rightarrow \text{min } L = 0.27 \mu m = 270 \text{ nm}$
DOF = $0.4 \mu m = 400 \text{ nm}$

Aside: Phase-Shifting mask

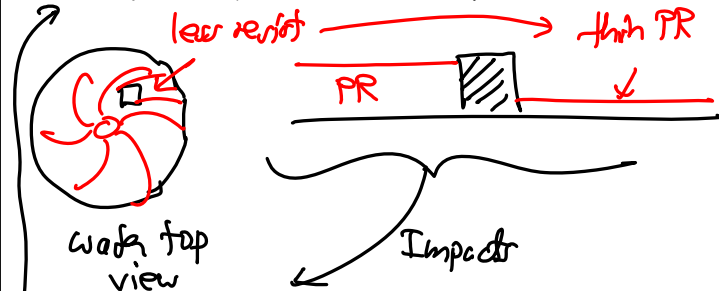




Lithewidth Control

⇒ depends on:

- ① Optical System
- ② Resist Thickness Variation



- ③ Exposure & Development Time
- ④ Reflectivity of the Substrate — also influences
- ⑤ Topography! — Active Area Mark

Ex. Say the theoretical min. $L = 0.9 \mu\text{m}$

What is the practical min. L ?

↳ depends the mark level:


↳ the later the mark, the more the topography! → worse min. L

e.g., gate mark → min. $L \sim 1 \mu\text{m}$

metal mark → min. $L \sim 1.5 \mu\text{m}$

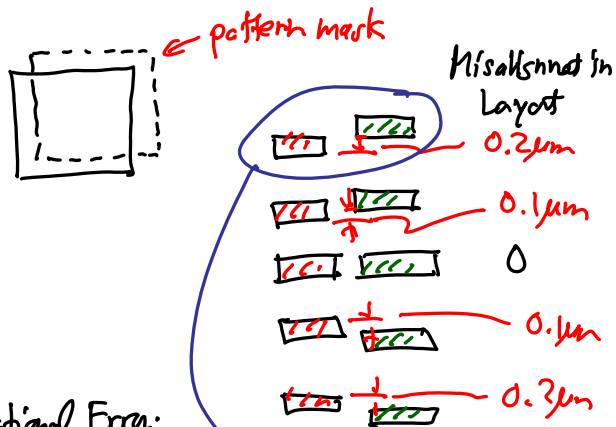
↳ What can we do about this?

Some tricks:

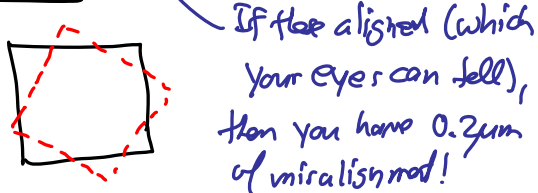
- ① Anti-reflection coating
- ② Multi-level PR (for small topography)

- ③ Dye PR → add a dye to the PR that absorbs reflected light (@ the needed λ).

Alignment Accuracy

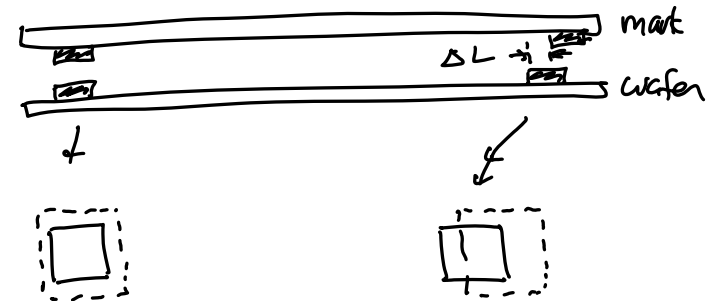
① Translational Errors:



② Rotational Error:



③ Run-in/Run-out: when one part of the wafer is aligned, but another part is not.

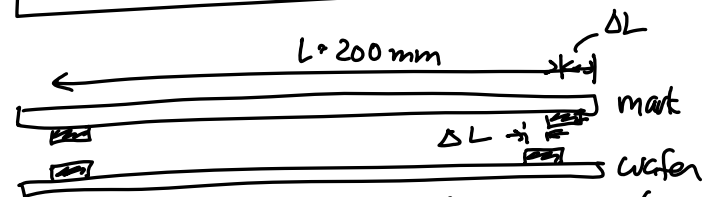


Ex. Case: Contact Lithography

Define: Thermal Expansion Coefficient

$$\alpha = \text{thermal expansion coefficient}$$

$$= \frac{\Delta L/L}{\Delta T} = \frac{1}{L} \frac{\Delta L}{\Delta T} = \frac{1}{L} \frac{\partial L}{\partial T}$$



$$(\alpha_{\text{mark}} - \alpha_{\text{Si}}) = \alpha = 10 \times 10^{-6} / ^\circ\text{C} \quad \mu = 10^{-6}$$

ppm → "parts per million"

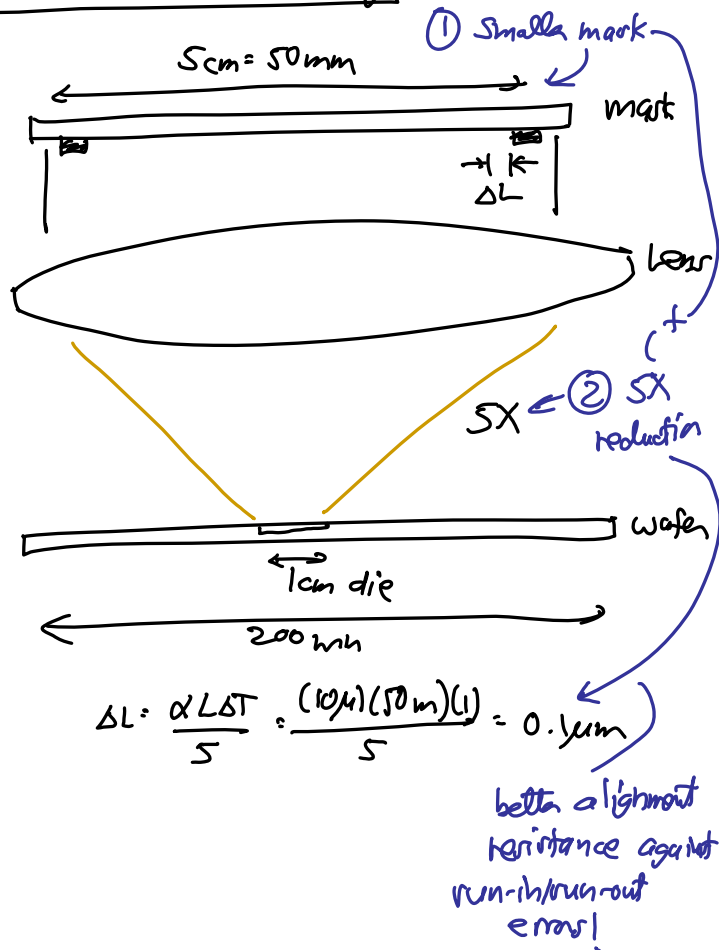
$$\Delta T = 1^\circ\text{C}$$

$$\alpha = \frac{1}{L} \frac{\Delta L}{\Delta T} \rightarrow \Delta L = \alpha L \Delta T = (10 \mu)(200 \text{ m})(1)$$

$$\rightarrow \Delta L = 2 \mu\text{m} \quad 10^{-3}$$

⇒ this is where projection stepper lithography really shines!

Projection Stepper Lithography

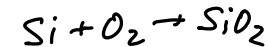


Thermal Oxidation of Silicon

⇒ achieved by heating the Si wafer to a high temperature ($\sim 900^\circ\text{C} - 1200^\circ\text{C}$) in an atmosphere containing pure O_2 or water vapor

⇒ enabling reactions:

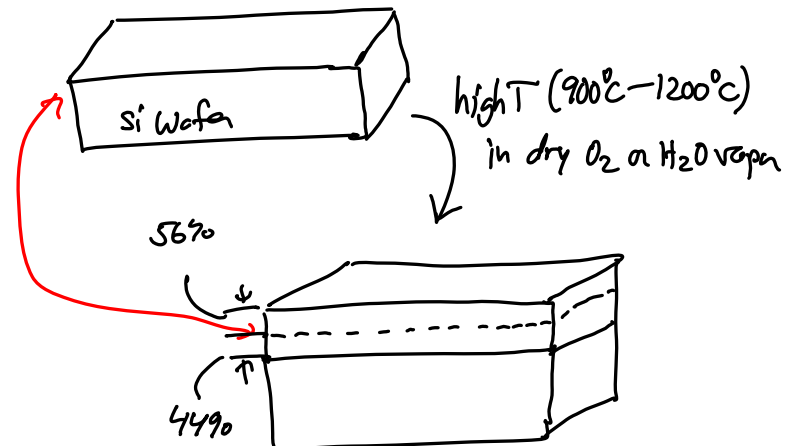
For dry O_2 :



For Water Vapor:

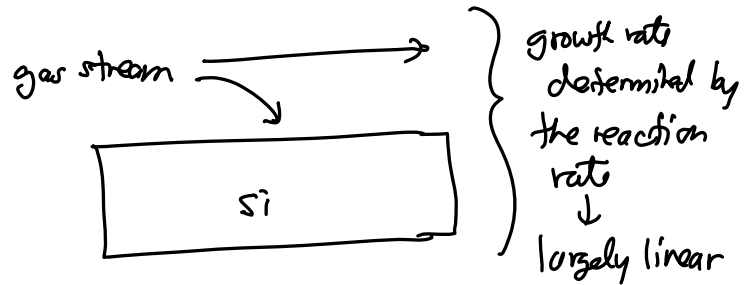


Schematically:



Modeling: Deal-Grove or "Linear-Parabolic" Model

① Initially: (no oxide on the surface)



② As oxide builds up

