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# **EE 143**

## **Microfabrication Technology**

### **Fall 2014**


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**Berkeley, CA 94720**

**Lecture Module 4: Etching**

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# **Etching**

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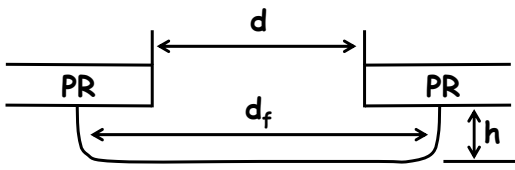


## Etching Basics

- Removal of material over designated areas of the wafer
- Two important metrics:
  1. Anisotropy
  2. Selectivity


1. Anisotropy -

a) Isotropic Etching (most wet etches)



If 100% isotropic:  $d_f = d + 2h$   
 Define:  $B = d_f - d$   
 If  $B = 2h \Rightarrow$  isotropic

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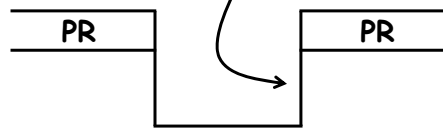
## Etching Basics (cont.)

b) Partially Isotropic:  $B < 2h$   
 (most dry etches, e.g., plasma etching)

Degree of Anisotropy: (definition)

$$A_f = 1 - \frac{B}{2h} = 0 \quad \text{if 100\% isotropic}$$

$0 < A_f \leq 1 \leftarrow$  anisotropic



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**Etching Basics (cont.)**

2. Selectivity -

Only poly-Si etched (no etching of PR or SiO<sub>2</sub>)

Perfect selectivity

PR partially etched

SiO<sub>2</sub> partially etched after some overetch of the polysilicon

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**Etching Basics (cont.)**

Why overetch?

$\sqrt{2}d = 1.4d = 0.56\mu\text{m}$  → Thicker spots due to topography!

1μ

0.4μm = d

Poly-Si → conformal if deposited by LPCVD

45°


10nm Gate oxide

0.4μm

Thus, must overetch at least 40%:  
40% overetch → (0.4)(0.4) = 0.16 μm poly = ??? oxide

Depends on the selectivity of poly-Si over the oxide

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## Etching Basics (cont.)

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**Define selectivity of A over B:**

$$S_{ab} = \frac{E.R._a}{E.R._b}$$

← Etch rate of A  
← Etch rate of B

Selectivity of A over B

**e.g., wet poly etch ( $\text{HNO}_3 + \text{NH}_4 + \text{H}_2\text{O}$ )**

$$S_{\text{poly}/\text{SiO}_2} = \frac{15}{1} \quad (\text{very good selectivity})$$

$S_{\text{poly}/\text{PR}} =$  Very high (but PR can still peel off after soaking for > 30 min., so beware)


**e.g., polysilicon dry etch:**

Regular RIE

$$S_{\text{poly}/\text{SiO}_2} = \frac{5-7}{1} \quad (\text{but depends on type of etcher})$$

ECR: 30:1  
Bosch: 100:1 (or better)

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## Etching Basics (cont.)

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**If  $S_{\text{poly}/\text{SiO}_2} = \frac{8}{1} \Rightarrow$  40% overetch removes**

$$\frac{0.16}{8} = 20 \text{ nm of oxide!}$$

$\Rightarrow$  This will etch all poly over the thin oxide, etch thru the 10nm of oxide, then start etching into the silicon substrate  $\rightarrow$  needless to say, this is bad!

**with better selectivity:**

**e.g.,  $S_{\text{poly}/\text{SiO}_2} = \frac{30}{1}$**

**(Can attain with high density Cl plasma ECR etch!)**

**40% overetch removes  $\frac{0.16}{30} = 5.3 \text{ nm}$  (better)**

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## Wet Etching

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## Wet Etching

- **Wet etching:** dip wafer into liquid solution to etch the desired film  
 ↳ Generally isotropic, thus, inadequate for defining features  $< 3\mu\text{m}$ -wide
- **General Mechanism -**

Film to be etched →

PR    PR

Si

o ← Reactant

x

o    o Reaction products

wafer

etch


Solvent bath

PR    PR

Si

1. Diffusion of the reactant to the film surface
2. Reaction: adsorption, reaction, desorption
3. Diffusion of reaction products from the surface


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## Wet Etching (cont.)

- There are many processes by which wet etching can occur
  - ↳ Could be as simple as dissolution of the film into the solvent solution
  - ↳ Usually, it involves one or more chemical reactions
    - Oxidation-reduction (redox) is very common:
      - (a) Form layer of oxide
      - (b) Dissolve/react away the oxide
- Advantages:
  1. High throughput process → can etch many wafers in a single bath
  2. Usually fast etch rates (compared to many dry etch processes)
  3. Usually excellent selectivity to the film of interest


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## Wet Etching Limitations

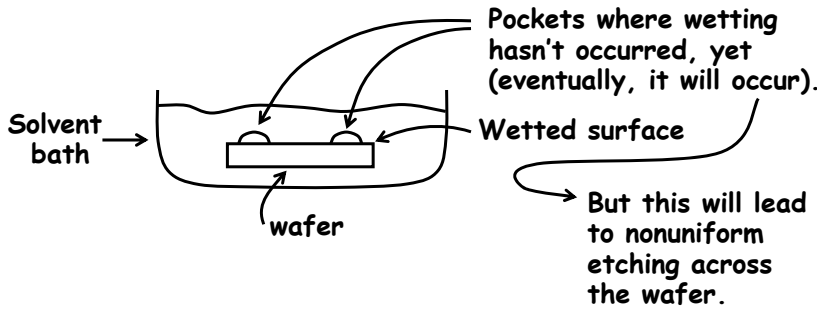
1. Isotropic
  - ↳ Limited to  $<3\mu\text{m}$  features
  - ↳ But this is also an advantage of wet etching, e.g., if used for undercutting for MEMS
2. Higher cost of etchants & DI water compared w/ dry etch gas expenses (in general, but not true vs. deep etchers)
3. Safety
  - ↳ Chemical handling is a hazard
4. Exhaust fumes and potential for explosion
  - ↳ Need to perform wet etches under hood
5. Resist adhesion problems
  - ↳ Need HMDS (but this isn't so bad)

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## Wet Etch Limitations (cont.)

6. Incomplete wetting of the surface:



For some etches (e.g., oxide etch using HF), the solution is to dip in DI water first, then into HF solution → the DI water wets the surface better

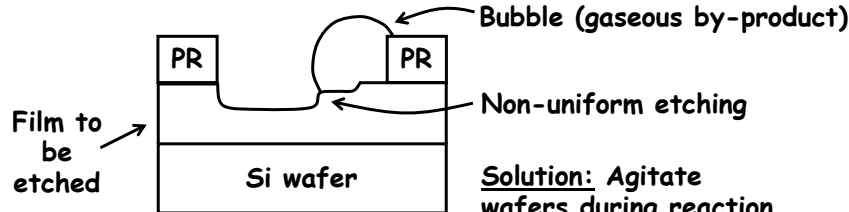
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## Wet Etch Limitations (cont.)

7. Bubble formation (as a reaction by-product)

↪ If bubbles cling to the surface → get nonuniform etching



**Solution:** Agitate wafers during reaction.

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**Some Common Wet Etch Chemistries**

**Wet Etching Silicon:**

**Common:**  $\text{Si} + \text{HNO}_3 + 6\text{HF} \rightarrow \text{H}_2\text{SiF}_6 + \text{HNO}_2 + \text{H}_2 + \text{H}_2\text{O}$

(isotropic)      (nitric acid)      (hydrofluoric acid)

(1) forms a layer of  $\text{SiO}_2$       (2) etches away the  $\text{SiO}_2$

Different mixture combinations yield different etch rates.

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**Silicon Crystal Orientation**

• Silicon has the basic diamond structure

- Two merged FCC cells offset by  $(a/4)$  in  $x$ ,  $y$ , and  $z$  axes
- From right:
  - # available bonds/cm<sup>2</sup>  $\langle 111 \rangle$
  - # available bonds/cm<sup>2</sup>  $\langle 110 \rangle$
  - # available bonds/cm<sup>2</sup>  $\langle 100 \rangle$

Increasing

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**Anisotropic Wet Etching**

**Anisotropic etches also available for single crystal Si:**

Orientation-dependent etching:  $\langle 111 \rangle$ -plane more densely packed than  $\langle 100 \rangle$ -plane

Faster E.R.      Slower E.R.

...in some solvents

One such solvent: KOH + isopropyl alcohol  
(e.g., 23.4 wt% KOH, 13.3 wt% isopropyl alcohol, 63 wt% H<sub>2</sub>O)

$\Rightarrow \text{E.R.}_{\langle 100 \rangle} = 100 \times \text{E.R.}_{\langle 111 \rangle}$

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**Anisotropic Wet Etching (cont.)**

Can get the following:

$\langle 111 \rangle$        $\langle 100 \rangle$       SiO<sub>2</sub>

54.7°      Si

(on a  $\langle 100 \rangle$  - wafer)


$\langle 110 \rangle$        $\langle 111 \rangle$       SiO<sub>2</sub>

Si

(on a  $\langle 110 \rangle$  - wafer)

$\Rightarrow$  Quite anisotropic!

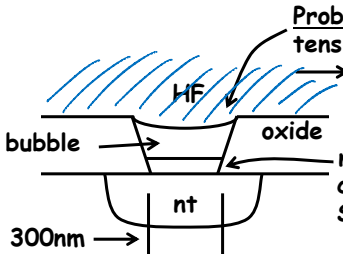
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## Wet Etching SiO<sub>2</sub>

$$\text{SiO}_2 + 6\text{HF} \rightarrow \text{H}_2 + \text{SiF}_6 + 2\text{H}_2\text{O}$$

Generally used to clear out residual oxides from contacts



Problem: Contact hole is so thin that surface tensions don't allow the HF to get into the contact  
Generally the case for VLSI circuits

bubble

oxide

native oxide  
can get this just by exposing Si to air → 1-2nm-thick


300nm

nt

**Solution:** add a surfactant (e.g., Triton X) to the BHF before the contact clear etch

1. Improves the ability of HF to wet the surface (hence, get into the contact)
2. Suppresses the formation of etch by-products, which otherwise can block further reaction if by-products get caught in the contact

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## More Wet Etch Chemistries

- Wet etching silicon nitride
  - ↗ Use hot phosphoric acid: 85% phosphoric acid @ 180°C
  - ↗ Etch rate ~ 10 nm/min (quite slow)
  - ↗ **Problem:** PR lifted during such etching
  - ↗ **Solution:** use SiO<sub>2</sub> as an etch mask (E.R. ~2.5 nm/min)
    - A hassle → dry etch processes more common than wet
- Wet etching aluminum
  - ↗ Typical etch solution composition:
 

(H<sub>2</sub>PO<sub>4</sub>)
(HNO<sub>3</sub>)
(CH<sub>3</sub>COOH)
(H<sub>2</sub>O)

(1) Forms Al<sub>2</sub>O<sub>3</sub> (aluminum oxide)  
 (2) Dissolves the Al<sub>2</sub>O<sub>3</sub>
  - ↗ **Problem:** H<sub>2</sub> gas bubbles adhere firmly to the surface → delay the etch → need a 10-50% overetch time
  - ↗ **Solution:** mechanical agitation, periodic removal of wafers from etching solution

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# Wet Etch Rates (f/ K. Williams)

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Wet-Etch Rates for Micromachining and IC Processing (Å/min)

The top etch rate was measured by the authors with fresh solutions, etc. The center and bottom values are the low and high etch rates observed by the authors and others in our lab under less carefully controlled conditions.

ETCHANT EQUIPMENT CONDITIONS	TARGET MATERIAL	MATERIAL																	
		SC Si <100>	Poly Si	Poly Si	Wet Si	Dry Si	LTO undep	PSG undep	PSG undep	SiO <sub>2</sub> undep	SiO <sub>2</sub> undep	Low- temp Si	Al 24 Si	Sput Ti	Sput Ti	Sput Ti	OC K2098	OC K2098	
Concentrated HF (49%) Wet Sink Room Temperature	Silicon oxides	-	0	-	23k 18k 23k	F	>14k	F	36k 140	140	52 30 52	42 0 42	<50	F	-	P	0	P	
10:1 HF Wet Sink Room Temperature	Silicon oxides	-	7	0	230 230	340	15k	4700	11	3	2500 2500 12k	0	11k	<70	0	0	0		
25:1 HF Wet Sink Room Temperature	Silicon oxides	-	0	0	97 95	130	W	1500	6	1	W	0	-	-	0	0	0		
5:1 HF Wet Sink Room Temperature	Silicon oxides	-	9	2	1000 900 1080	1000 1200	6800	4400 3500 4400	9	4	1400	<20 0.25 25	F	1000	0	0	0		
Phosphoric Acid (85%) Heated Bath with Reflux 160°C	Silicon nitrides	-	7	-	0.7 0.8	<1	37	34 9 24	28 19 42	19 19 42	9800	-	-	-	550	390	0		
Silicon Etchant (126 HNO <sub>3</sub> : 60 H <sub>2</sub> O : 5 NH <sub>4</sub> F) Wet Sink Room Temperature	Silicon	1500	3100 1200 8000	1000	87	W	110	4000	1700	2	3	4000	130	3000	-	0	0		
KOH (1 KOH : 2 H <sub>2</sub> O by weight) Heated Stirred Bath 80°C	<100> Silicon	14k	>10k	F	77 41 77	-	94	W	380	0	0	F	0	-	-	F	F		
Aluminum Etchant Type A (16 H <sub>2</sub> PO <sub>4</sub> : 1 HNO <sub>3</sub> : 1 HAc : 2 H <sub>2</sub> O) Heated Bath 50°C	Aluminum	-	<10	<9	0	0	0	-	<10	0	2	6600 2600 6600	-	0	-	0	0		
Titanium Etchant (20 H <sub>2</sub> O : 1 H <sub>2</sub> O <sub>2</sub> : 1 HF) Wet Sink Room Temperature	Titanium	-	12	-	120	W	W	W	2100	8	4	W	0	8800	-	0	0		
H <sub>2</sub> O <sub>2</sub> (30%) Wet Sink Room Temperature	Tungsten	-	0	0	0	0	0	0	0	0	0	<20	190 190 1000	0	60 60 150	<2	0		
Phoska (~50 H <sub>2</sub> SO <sub>4</sub> : 1 H <sub>2</sub> O <sub>2</sub> ) Heated Bath 120°C	Cleaning off metals and organics	-	0	0	0	0	0	-	0	0	0	1800	-	2400	-	F	F		
Acetone Wet Sink Room Temperature	Photoresist	-	0	0	0	0	0	-	0	0	0	0	-	0	-	>44k	>35k		

Notation: - not performed; W-not performed, but known to work (> 100 Å/min); F-not performed, but known to be fast (> 10 kÅ/min); P-across of film peeled during etch or when rinsed; A=film was visibly attacked and etched.  
Each entry is of a 4-inch wafer for the transparent films and half of the wafer for single-crystal silicon and the metals.  
Each rate will vary with temperature and prior use of solution, area of exposure of film, other materials present (e.g., photoresist), film impurities and microstructures, etc. Some variation should be expected.

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**Film Etch Chemistries**

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- For some popular films:

Material	Wet etchant	Etch rate [nm/min]	Dry etchant	Etch rate [nm/min]
Polysilicon	HNO <sub>3</sub> :H <sub>2</sub> O: NH <sub>4</sub> F	120-600	SF <sub>6</sub> + He	170-920
Silicon nitride	H <sub>3</sub> PO <sub>4</sub>	5	SF <sub>6</sub>	150-250
Silicon dioxide	HF	20-2000	CHF <sub>3</sub> + O <sub>2</sub>	50-150
Aluminum	H <sub>3</sub> PO <sub>4</sub> :HNO <sub>3</sub> : CH <sub>3</sub> COOH	660	Cl <sub>2</sub> + SiCl <sub>4</sub>	100-150
Photoresist	Acetone	>4000	O <sub>2</sub>	35-3500
Gold	KI	40	n/a	n/a

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## Dry Etching

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## Dry Etching

- Physical sputtering
- Plasma etching
- Reactive ion etching

All based upon plasma processes.

RF (also, could be  $\mu$ wave)

Develop (-) bias

Plasma (partially ionized gas composed of ions,  $e^-$ 's, and highly reactive neutral species)

E-field


wafer

Develops (+) charge to compensate for

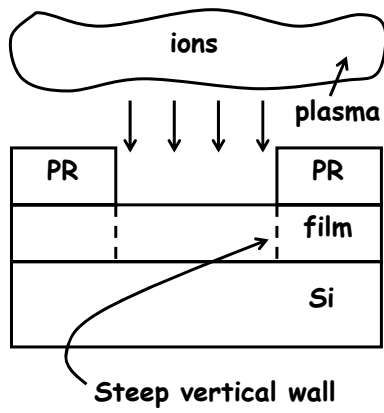
$\therefore$  (+) ions will be accelerated to the wafer

(+) ions generated by inelastic collisions with energetic  $e^-$ 's  
Get avalanche effect because more  $e^-$ 's come out as each ion is generated.

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 **Physical Sputtering (Ion Milling)**

- Bombard substrate w/ energetic ions → etching via physical momentum transfer
- Give ions energy and directionality using E-fields
- Highly directional → very anisotropic



ions

plasma


PR

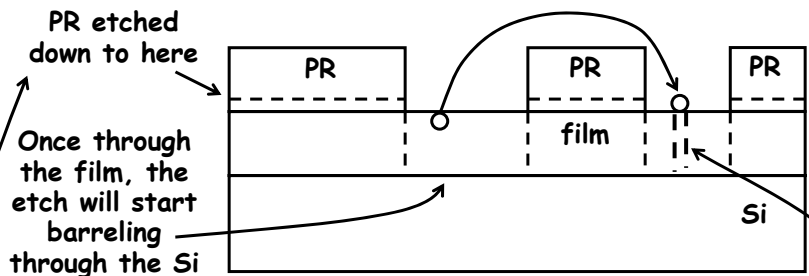
film

Si

Steep vertical wall

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 **Problems With Ion Milling**



PR etched down to here

Once through the film, the etch will start barreling through the Si

PR


film

Si

1. PR or other masking material etched at almost the same rate as the film to be etched → very poor selectivity!
2. Ejected species not inherently volatile → get redeposition → non-uniform etch → grass!

- Because of these problems, ion milling is not used often (very rare)

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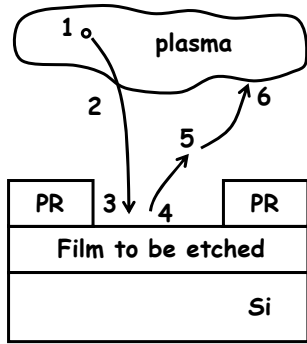


## Plasma Etching

- Plasma (gas glow discharge) creates reactive species that chemically react w/ the film in question
- Result:** much better selectivity, but get an isotropic etch


**Plasma Etching Mechanism:**

- Reactive species generated in a plasma.
- Reactive species diffuse to the surface of material to be etched.
- Species adsorbed on the surface.
- Chemical reaction.
- By-product desorbed from surface.
- Desorbed species diffuse into the bulk of the gas



← MOST IMPORTANT STEP! (determines whether plasma etching is possible or not.)

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## Ex: Polysilicon Etching w/ $\text{CF}_4$ and $\text{O}_2$

$$\text{CF}_4 \xrightarrow{\text{plasma}} \text{CF}_4^+ + \text{CF}_3^+ + \text{CF}_2^+ + \text{CF}^+ + \text{F}^+ + \text{F}^0 + \text{CF}_2^+ + \dots$$

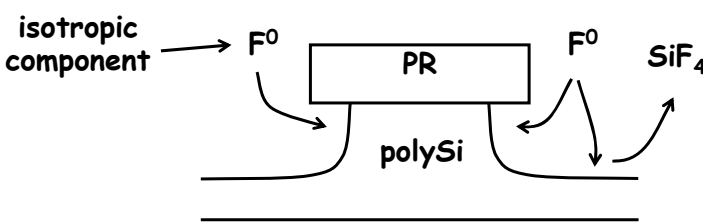
Si

Neutral radical (highly reactive!)


$$e^- + \text{CF}_4 \rightarrow \text{CF}_3 + \text{F} + e^-$$

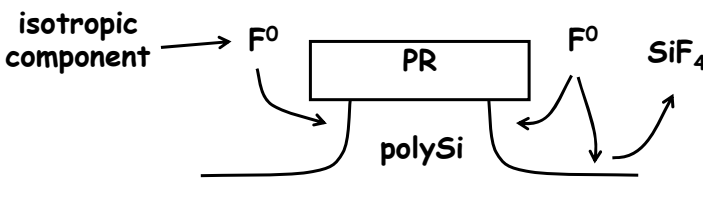
$\text{SiCF}_6, \text{SiF}_4 \leftarrow$  both volatile  $\therefore$  dry etching is possible.

- $\text{F}^0$  is the dominant reactant  $\rightarrow$  but it can't be given a direction  $\rightarrow$  thus, get isotropic etch!




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 **Ex: Polysilicon Etching w/  $\text{CF}_4$  and  $\text{O}_2$**



- **Problems:**
  1. Isotropic etching
  2. Formation of polymer because of C in  $\text{CF}_4$ 
    - ↳ **Solution:** add  $\text{O}_2$  to remove the polymer (but note that this reduces the selectivity,  $S_{\text{poly/PR}}$ )
- **Solution:**
  - ↳ Use Reactive Ion Etching (RIE)

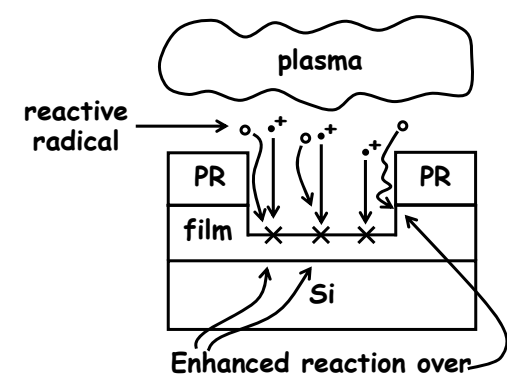
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 **Reactive Ion Etching (RIE)**

- Use ion bombardment to aid and enhance reactive etching in a particular direction
  - ↳ **Result:** directional, anisotropic etching!
- RIE is somewhat of a misnomer
  - ↳ It's not ions that react ... rather, it's still the neutral species that dominate reaction
  - ↳ Ions just enhance reaction of these neutral radicals in a specific direction
- Two principle postulated mechanisms behind RIE
  1. Surface damage mechanism
  2. Surface inhibitor mechanism

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**RIE: Surface Damage Mechanism**



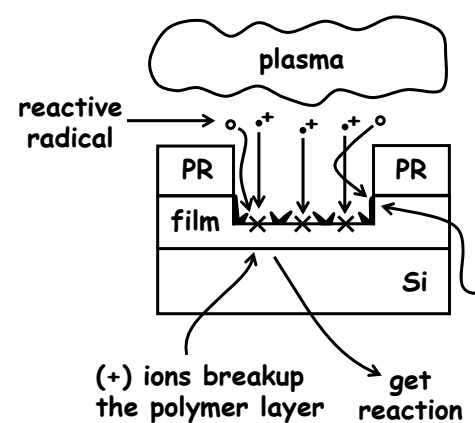
The diagram illustrates the surface damage mechanism in Reactive Ion Etching (RIE). It shows a cross-section of a substrate with a Silicon (Si) layer, a film layer, and a Photoresist (PR) layer. A plasma source is positioned above the substrate, releasing reactive radicals (represented by 'o' with a dot) and positive ions (represented by '+' with a dot). The positive ions are shown impinging on the surface of the PR layer. The reactive radicals are shown reacting with the film layer. The diagram indicates that the reaction is enhanced over the surface compared to the sidewalls.

- Relatively high energy impinging ions ( $>50$  eV) produce lattice damage at surface
- Reaction at these damaged sites is enhanced compared to reactions at undamaged areas

**Result: E.R. at surface  $\gg$  E.R. on sidewalls**

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**RIE: Surface Inhibitor Mechanism**



The diagram illustrates the surface inhibitor mechanism in Reactive Ion Etching (RIE). It shows a cross-section of a substrate with a Silicon (Si) layer, a film layer, and a Photoresist (PR) layer. A plasma source is positioned above the substrate, releasing reactive radicals (represented by 'o' with a dot) and positive ions (represented by '+' with a dot). The positive ions are shown impinging on the surface of the PR layer. The reactive radicals are shown reacting with the film layer. The diagram indicates that the reaction is enhanced over the surface compared to the sidewalls. The positive ions are shown breaking up the polymer layer on the horizontal surface, but not on the sidewalls.


- Non-volatile polymer layers are a product of reaction
- They are removed by high energy directional ions on the horizontal surface, but not removed from sidewalls

(+) ions breakup the polymer layer    get reaction    no reaction

**Result: E.R. @ surface  $\gg$  E.R. on sidewalls**

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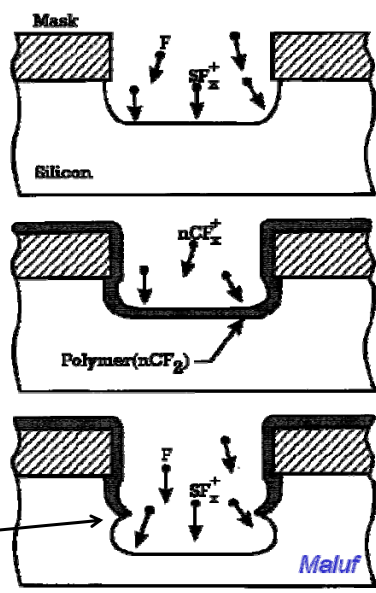





## Deep Reactive-Ion Etching (DRIE)

**The Bosch process:**

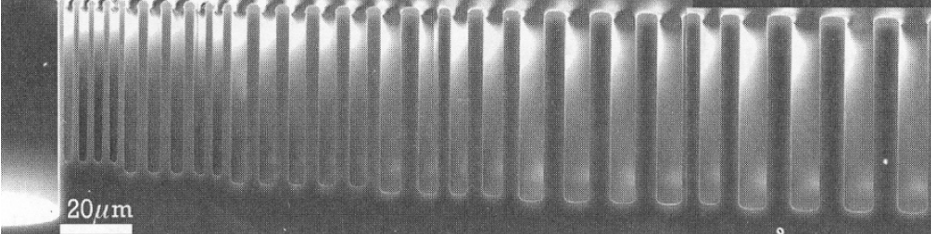
- Inductively-coupled plasma
- Etch Rate: 1.5-4  $\mu\text{m}/\text{min}$
- Two main cycles in the etch:
  - Etch cycle** (5-15 s):  $\text{SF}_6$  ( $\text{SF}_x^+$ ) etches Si
  - Deposition cycle** (5-15 s):  $\text{C}_4\text{F}_8$  deposits fluorocarbon protective polymer  $(\text{CF}_2^-)_n$
- Etch mask selectivity:
  - $\text{SiO}_2 \sim 200:1$
  - Photoresist  $\sim 100:1$
- Issue:** finite sidewall roughness
  - scalloping  $< 50 \text{ nm}$
- Sidewall angle:  $90^\circ \pm 2^\circ$



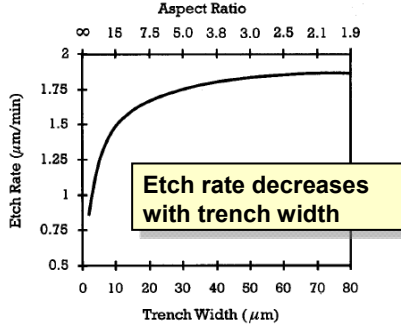
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## DRIE Issues: Etch Rate Variance



- Etch rate is diffusion-limited and drops for narrow trenches
  - Adjust mask layout to eliminate large disparities
  - Adjust process parameters (slow down the etch rate to that governed by the slowest feature)



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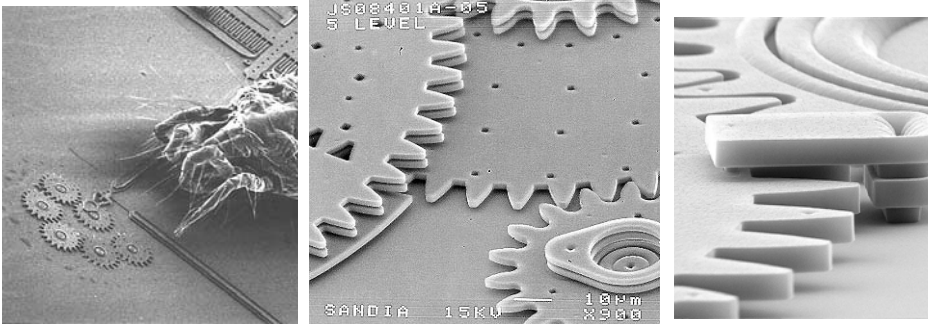
## Chemical Mechanical Polishing (CMP)

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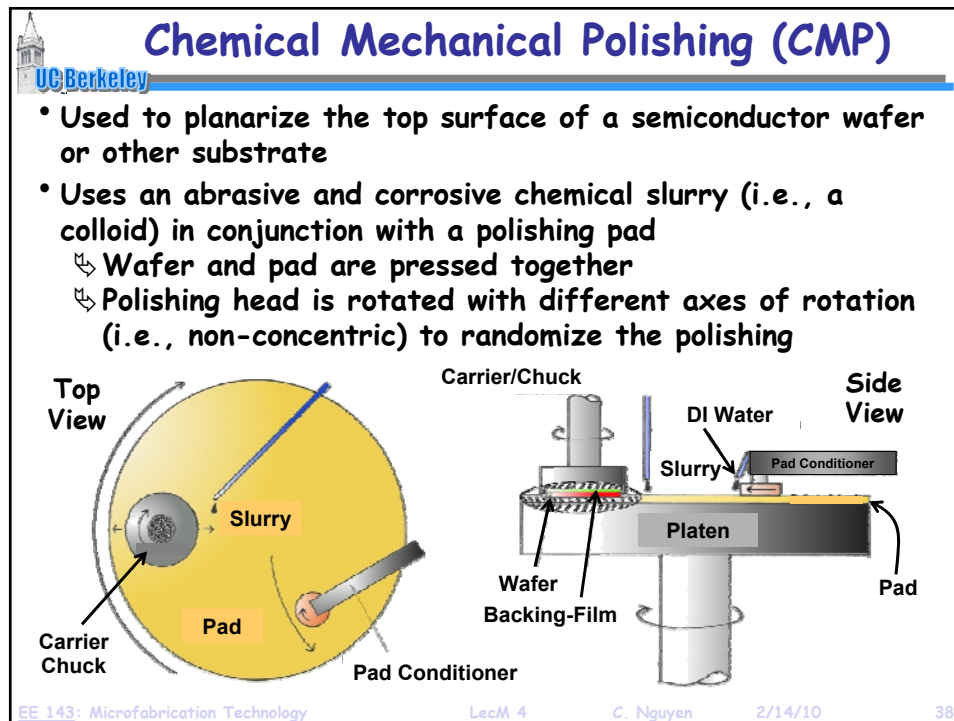
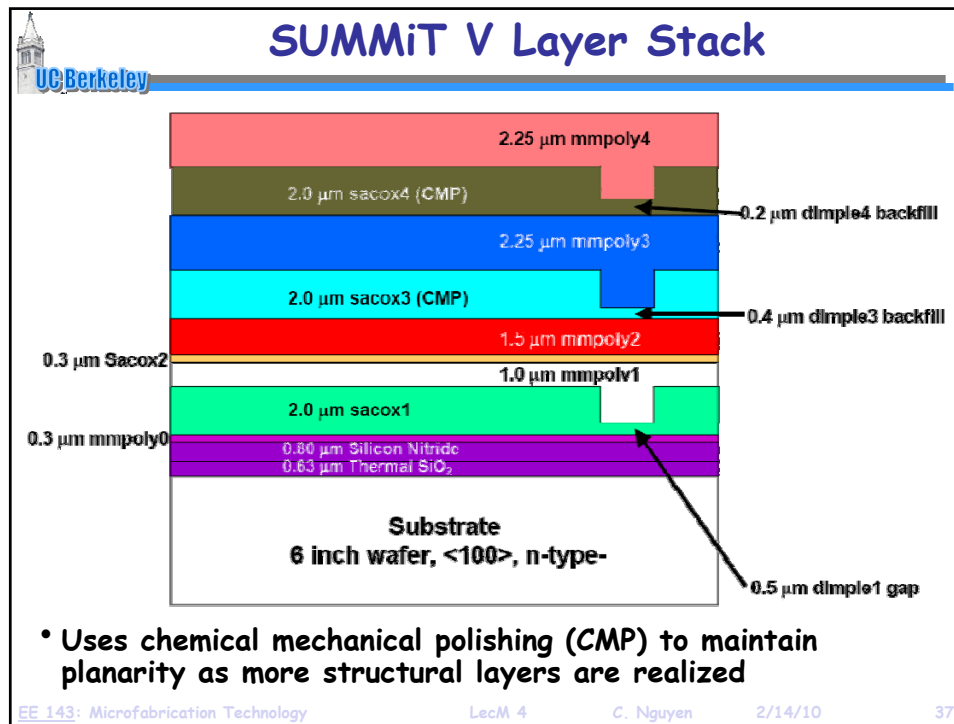
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## Sandia's SUMMiT V

- **SUMMiT V**: "Sandia Ultra-planar Multi-level MEMS Technology 5" fabrication process
  - ↗ Five-layer polysilicon surface micromachining process
  - ↗ One electrical interconnect layer & 4 mechanical layers
  - ↗ Uses chemical mechanical polishing (CMP) to maintain planarity as more structural layers are realized
  - ↗ 14 masks



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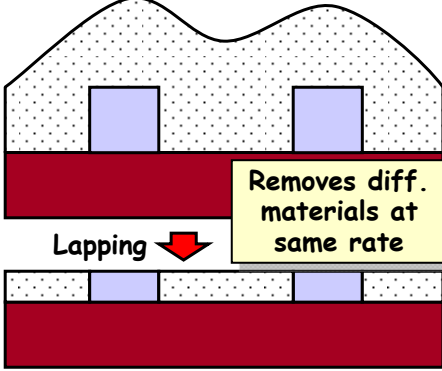




## CMP: Not the Same as Lapping

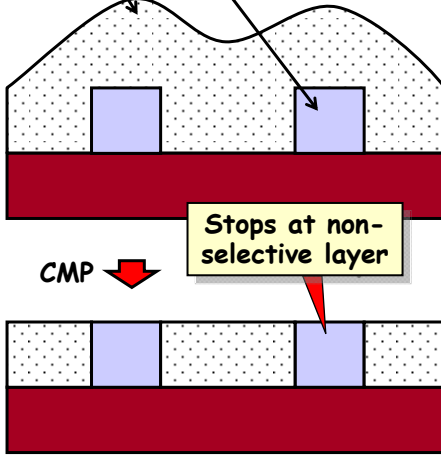
### Lapping

- Lapping is merely the removal of material to flatten a surface without selectivity
- Everything is removed at approximately the same rate




### Chemical Mechanical Polishing

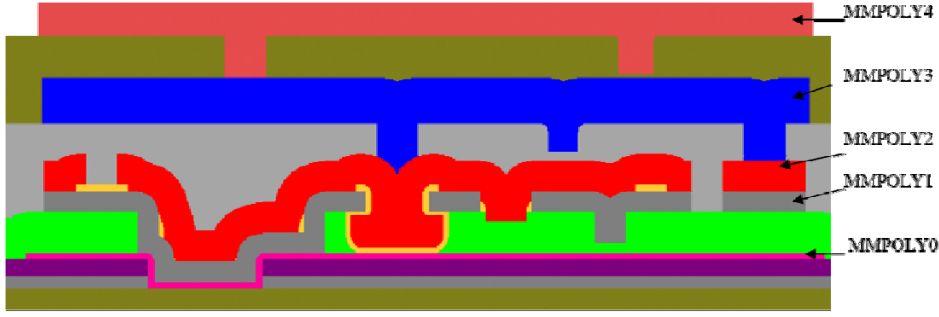
- CMP is selective to certain films, and not selective to others



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## Actual SUMMiT Cross-Section



- No CMP until after the first three polySi layers
- 1  $\mu\text{m}$  mmpoly1 and 1.5  $\mu\text{m}$  mmpoly2 can be combined to form a 2.5  $\mu\text{m}$  polysilicon film
- Refer to the SUMMiT V manual (one of your handouts) for more detailed information on masks and layout instructions

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