

Week 6 Student Checklist
Created: Shong Yin, Spring 2006

Student checklist

- Review the overall Process Flow. This can be found at the lab webpage:
http://www-inst.eecs.berkeley.edu/~ee143/sp06/lab/fabrication_process_flow.pdf
- Read the detailed Process Flow for week 4 available on the lab web site at:
<http://www-inst.eecs.berkeley.edu/~ee143/sp06/lab/NMOS-process-flow.pdf>
Week 6: Gate Lithography. The process steps for this week will be very similar to those of Week 3: Active Area Lithography. Think about what we're trying to do, and the reason for the patterning.
- Read the Vernier Tutorial under *Laboratory References* on the lab web-site:
Read the manual for Four Point Probe available on the lab web-site at:
<http://www-inst.eecs.berkeley.edu/~ee143/fa05/lab/vernier.pdf>
Know how to read the misalignment as this will be an important metric to record this week.
- Print out and complete the Week 6 Quiz from the lab web page, before coming to your lab session.