





Welcome to EECS 16A!

Designing Information Devices and Systems I



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Module 2 Lecture 11 Op-amp circuit analysis (Note 19)

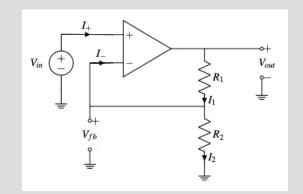


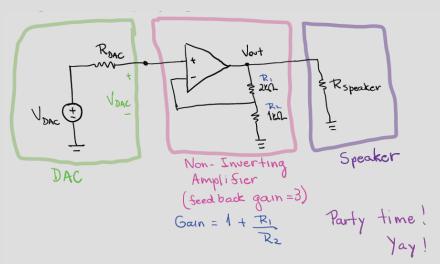
Last Lecture...

Toolbox

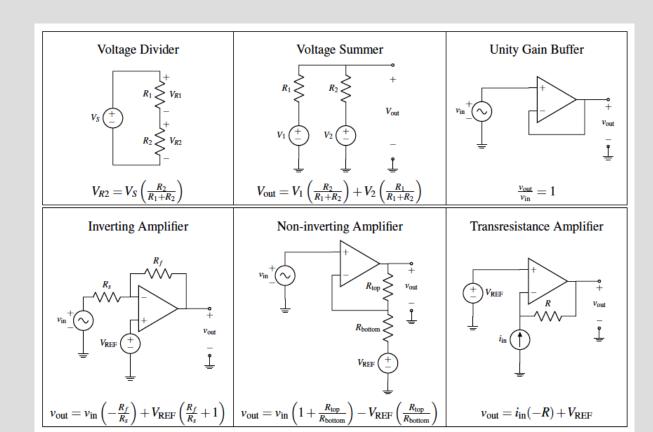
- Resistors
- Capacitors
- Open-circuits
- Voltage Dividers/Summers
- Op-Amps
- Thevenin and Norton Equivalence
- KCL/KVL
- Element Definitions
- DAC
- Negative Feedback
- Op-Amp in Negative Feedback
- "Golden Rules" for Op-Amps

$$A_{V} = \frac{\sqrt{\omega_{V}}}{\sqrt{V_{IN}}}$$





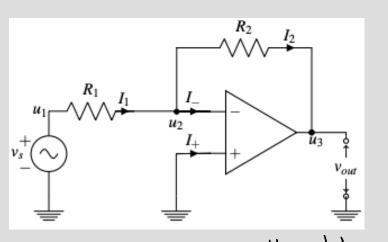
Today

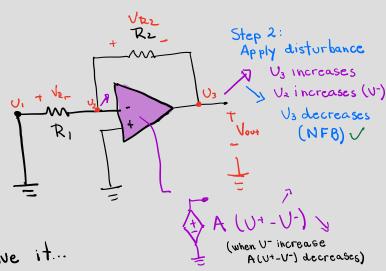


Checking for Negative Feedback (Determing the polarity of NFB)

Step 1 – Zero out all independent sources : replacing voltage sources with wires and current sources with open circuits as in superposition

- Step 2 Wiggle the output and check the loop to check how the feedback loop responds to a change.
 - if the error signal decreases, the output must also decrease. The circuit is in negative feedback
 - if the error signal increases, the output must also increase. The circuit is in positive feedback





$$V_{1n} = V_{1} = V_{1}$$

$$V_{2} = V_{2}$$

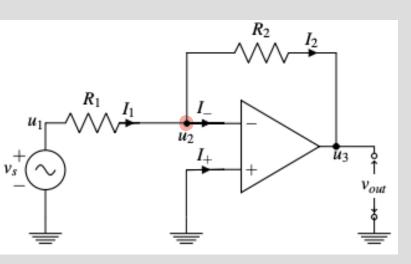
$$V_{2} = V_{3}$$

$$V_{3} = V_{4}$$

$$V_{4} =$$

VR2 = V3 - V2 = V3 = Vox

A faster way...

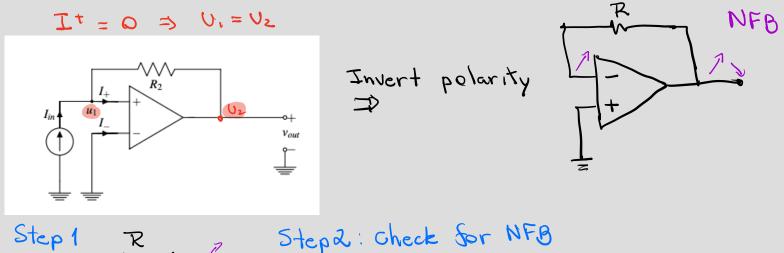


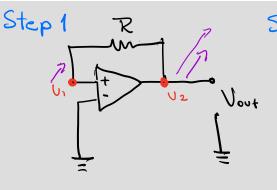
GRZ:
$$U^{\dagger} = U^{-}$$
 $U_{2} = U^{-}$
 $U^{\dagger} = 0 \Rightarrow U_{2} = 0$

GR1+ KCh $(I_{1} = I_{2} + I^{-})$
 $U_{2}^{\prime} - U_{1} = U_{3} - V_{2}^{\prime} + I^{\prime}$
 $V_{1}^{\prime} - V_{1}^{\prime} = V_{3}^{\prime} + I^{\prime}$
 $V_{2}^{\prime} - V_{1}^{\prime} = V_{3}^{\prime} + I^{\prime}$

$$=-\frac{R_2}{R}$$

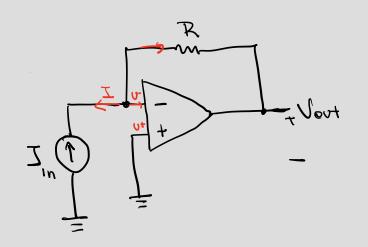
Example circuit 2 (trans-resistance amplifier)





Increase output +
+ Moves up
output increases

X Not in NFB



 $V+=V^{-}$ $V^{+}=0 \rightarrow V^{-}=0$

$$GZ # Z$$

$$GRZ - Vout + (- Im) + Z = 0$$

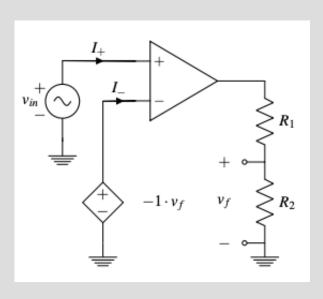
$$- \frac{Vout}{R} = Im$$

Vout = - R

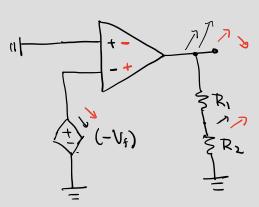
Vout = - In R

The input is current; output is Voltage: we use this model in the lab sor photo sensors.

Example circuit 3 -



Check NFB:



Voltage Divider
$$V_{\xi} = \frac{R_2}{R_1 + R_2} \cdot V_{\text{out}}$$

$$V_{in} = -\frac{R_2}{R_1 + R_2} V_{out} \rightarrow \frac{V_{in}}{V_{out}} = -\frac{R_2}{R_1 + R_2}$$

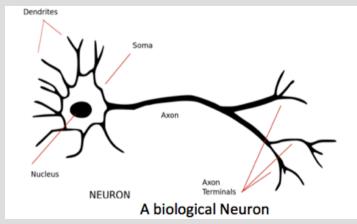
$$Av = \frac{V_{out}}{V_{in}} = -\frac{R_1 + R_2}{R_2} = -\left(1 + \frac{R_1}{R_2}\right)$$

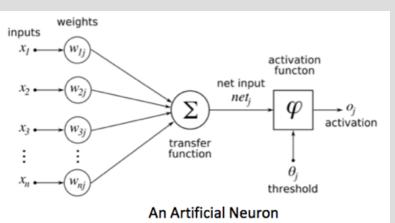
Artificial Neuron

(Energy Efficient Neural Networks) - Yes we can!

- Neurons in our brain are interconnected.
- The output of a single-neuron is dependent on inputs from several other neurons.
- This idea is represented with vector-vector multiplication the output is a linear combination of several inputs.
- An artificial neuron circuit must perform addition and multiplication.

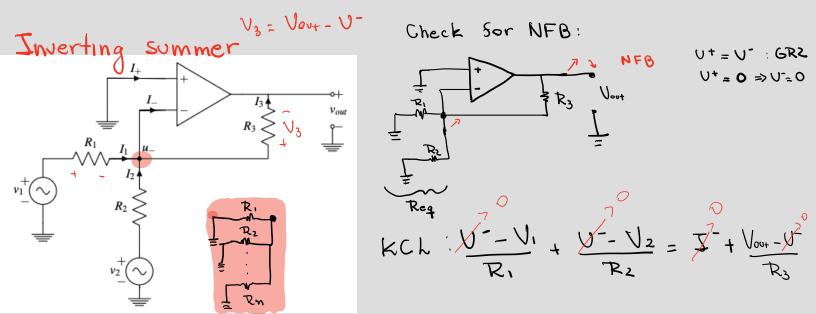
$$\begin{bmatrix} a_1 & a_2 \end{bmatrix} \cdot \begin{bmatrix} V_1 \\ V_2 \end{bmatrix} = a_1 V_1 + a_2 V_2$$





Artificial Neuron

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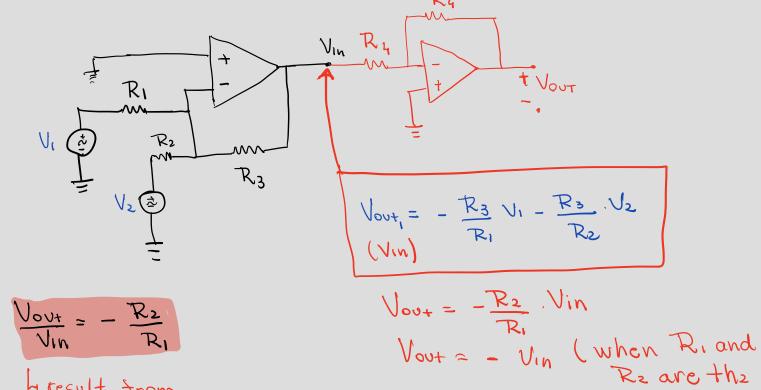


$$-\frac{V_1}{R_1} - \frac{V_2}{R_2} = \frac{V_{004}}{R_3}$$

$$V_{004} = \frac{R_3}{R_1} \cdot V_1 + \left(\frac{R_3}{R_2} \cdot V_2\right) + \frac{R_3}{R_2} \cdot V_2$$

only negative weights and on on one make an and as positive?

Add another inverting amplifier circuit.



Same)

h result from Inverting amplifier

Unity Gain Buffer

LA Allows us to isolate Design

loading

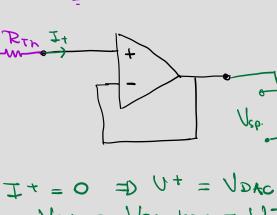
$$U^{+} = V_{in}$$

U+ = U-

Vin = VouT

GRZ





VDAC = Vspeaker

=> Ut=V-Vout = Uspeaker = U-

