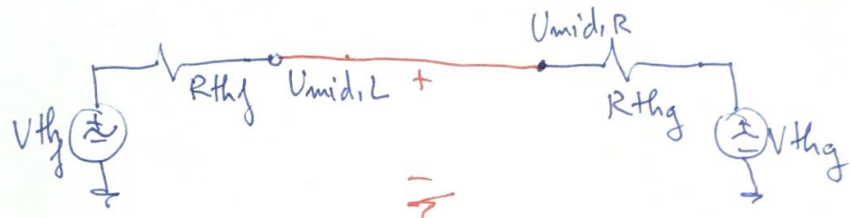


Today:
Note 20

- * Composing blocks (continued)
- * Design procedure
- * Design examples

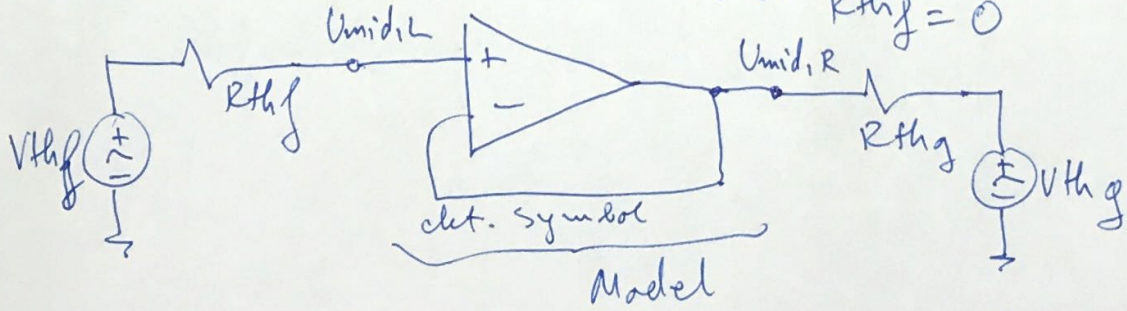


$U_{mid,L o.c} = V_{thf} \neq U_{mid,L} = \frac{R_{thg}}{R_{thf} + R_{thg}} \cdot V_{thf} + \frac{R_{thf}}{R_{thf} + R_{thg}} \cdot V_{thg}$
in general

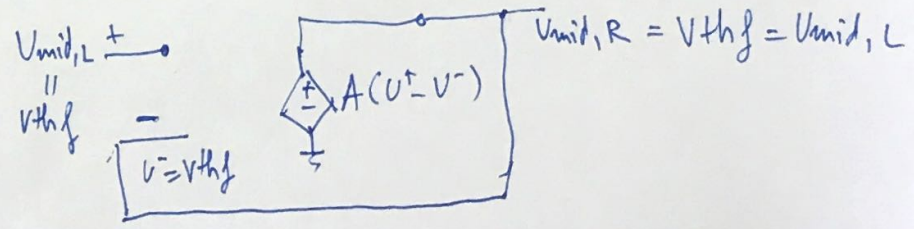
Ideal isolation:

From perspective of block $f \Rightarrow$ see an open-circuit
 i.e. $R_{thg} \equiv o.c$
 (or $R_{thf} = 0 \Rightarrow$ drive output with voltage source)

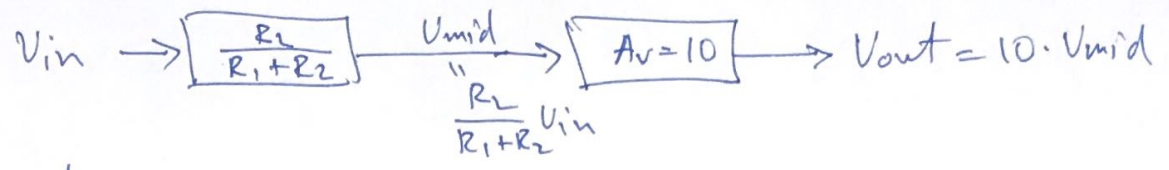
From perspective of block $g \Rightarrow$ see a voltage source
 i.e. $R_{thf} = 0$



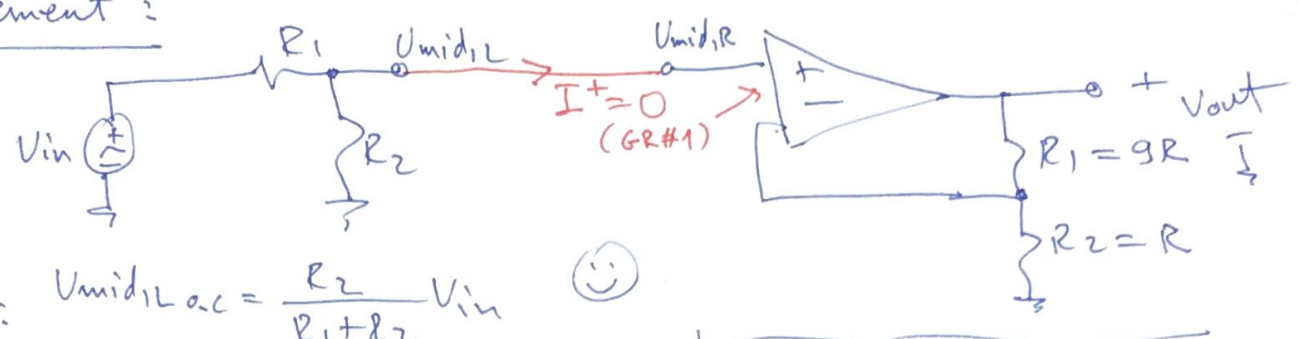
goal: $U_{mid,L} = U_{mid,R}$



Q2 Example 1: Want this:



Implement:

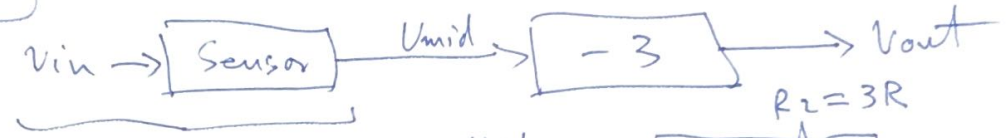


Verify: $U_{mid,L} \text{ o.c.} = \frac{R_2}{R_1 + R_2} V_{in}$

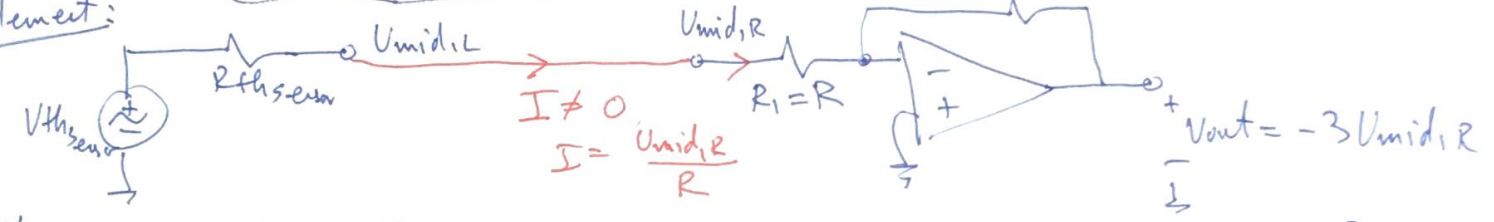
$U_{mid,L} = U_{mid,R}$
b.c. $R_{th} \equiv \text{o.c.}$

$A_v = \frac{V_{out}}{U_{mid,R}} = 10$

Example 2:



Implement:



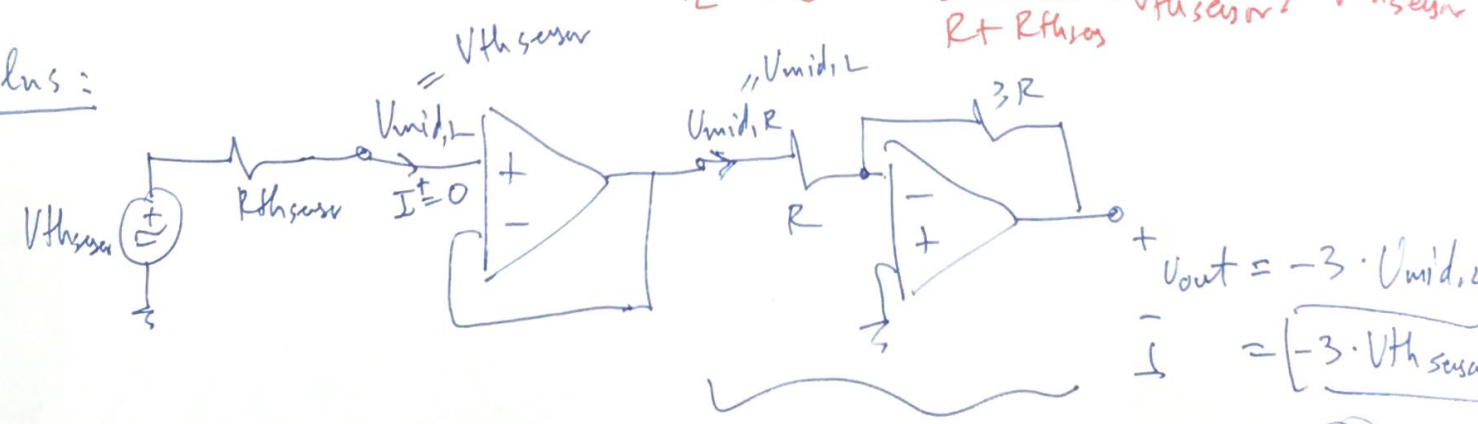
Verify: $U_{mid,L} = V_{th,sens}$

$U_{mid,L} = V_{th,sens} - R_{th,sens} \cdot \frac{U_{mid,L}}{R}$

$V_{out} = -\frac{R_2}{R_1} \cdot U_{in}$

$U_{mid,L} = \frac{R_{th} R}{R + R_{th,sens}} - V_{th,sens} \neq V_{th,sens}$

solns:



$V_{out} = -3 \cdot U_{mid,L}$
 $= -3 \cdot V_{th,sens}$



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Design procedure:

step 1:
(specification)

Concretely (re)state your goal for the design.
(most often from a word specification)

step 2:
(strategy)

Describe (often as a block diagram) the strategy to achieve this goal.

↳ often review what you can measure vs. what you wanted to know.

↳ what is the relationship between the two (e.g. touch / no-touch)



step 3:
(Implementation)

Implement the components within the strategy

↳ Remind yourself of the blocks you know that can provide the wanted function (or be adapted to provide this function)

↳ Think about how to modify / adapt the blocks (iteration #1000)

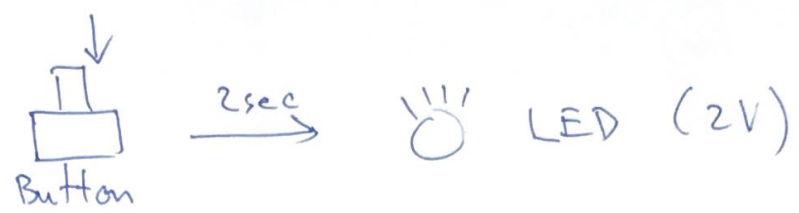
step 4:
(Verification/analysis)

Does the implementation in step 3 satisfy the specification in step 1?

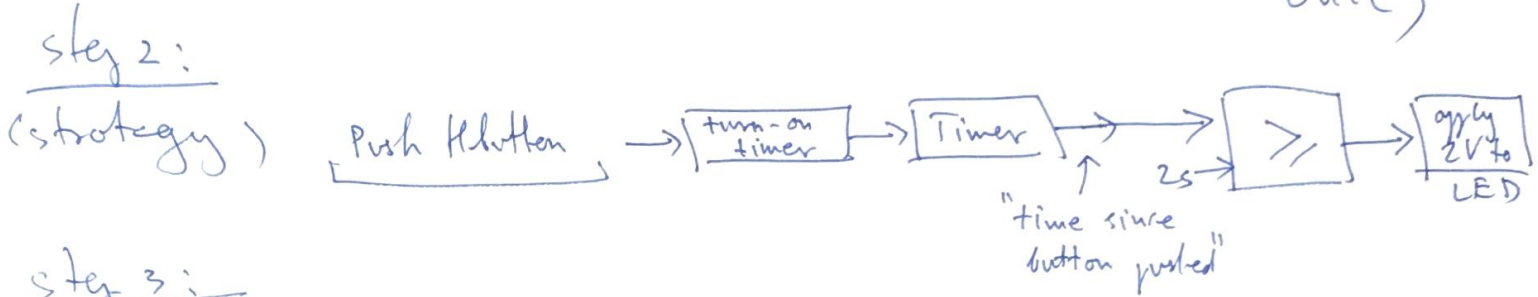
↳ check for block to block connections.

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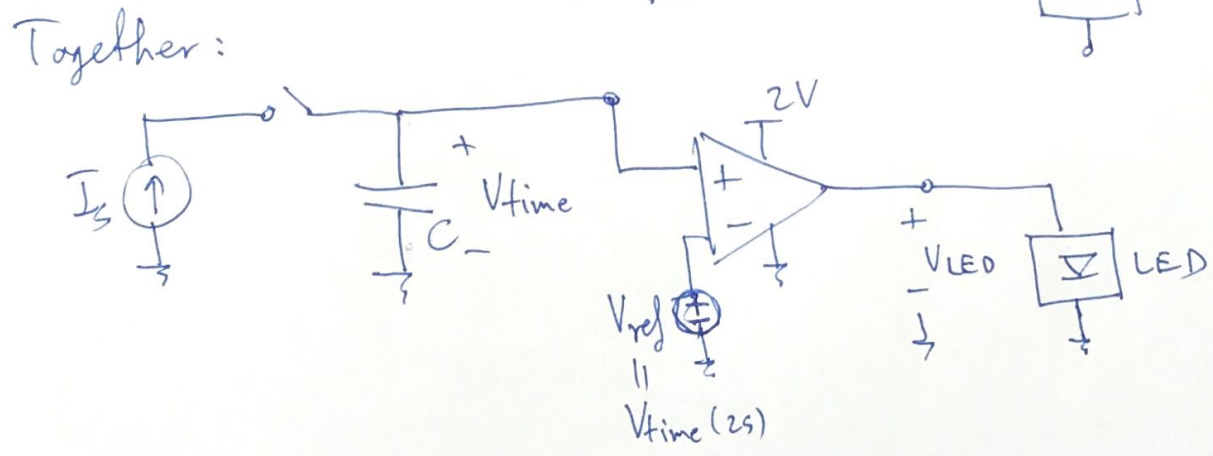
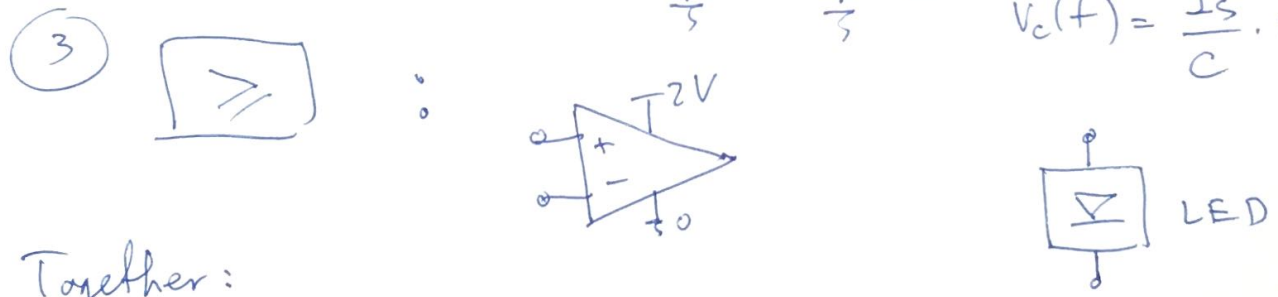
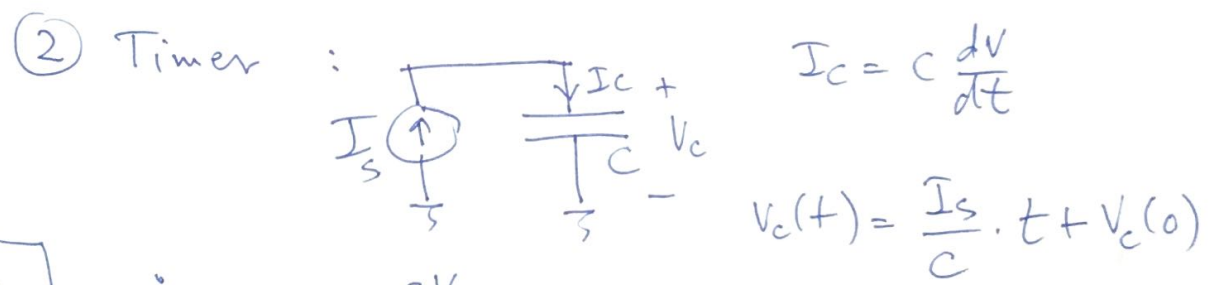
Example design #1: ("Countdown timer")



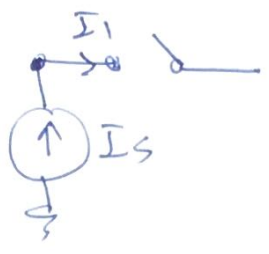
step 1: (spec) Build a circuit that after a button is pressed measures 2s and then applies 2V across an LED. (I assume you can press the button only once)



step 3: (implementation) ① Turn-on a timer: switch



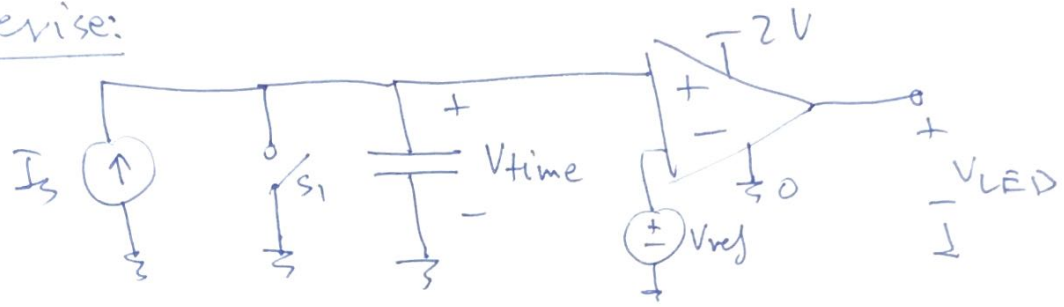
Q5 step 4:
(Verify)



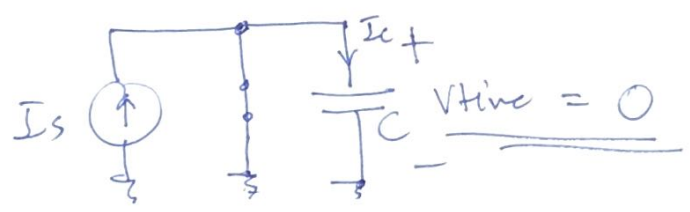
$I_1 = 0$ (switch off)
 $I_1 = I_s$ (switch on)

KCL: $I_s = I_1$ (always)

revise:

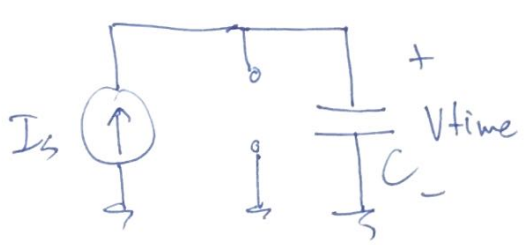


Before the button is pushed $S_1 = \text{on}$ wire



$I_c = C \frac{dV_{time}}{dt} = 0$

when push the button $S_1 = \text{off}$ o.c.
 @ $t = t_0$



$V_{time}(t_0) = 0$

$V_{time}(t) = \frac{I_s}{C} \cdot (t - t_0) + V_{time}(t_0)$

$V_{time}(t) = \frac{I_s}{C} \cdot (t - t_0)$

$V_{time}\left(\underset{\uparrow}{t}\right) = \frac{I_s}{C} \cdot 2s = V_{ref}$
 $t = t_0 + 2s$

Q6

