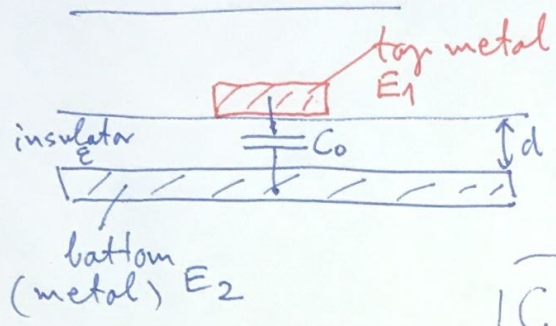


Lecture 7 - Module 2

Today:
Note 17

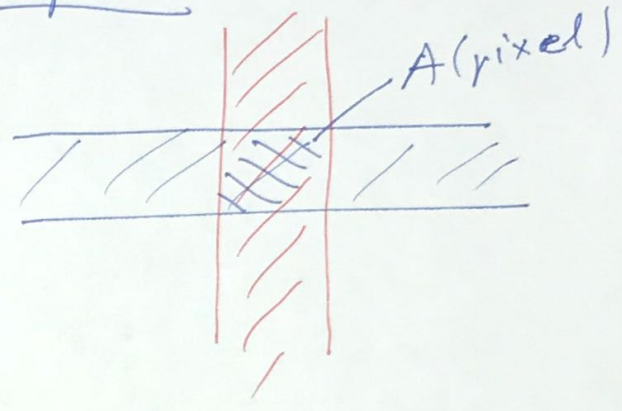
- * 2D touchscreen (capacitive)
- * sensing capacitor
- * capacitive measurement circuit
- * comparator (op-amp)

Side view:

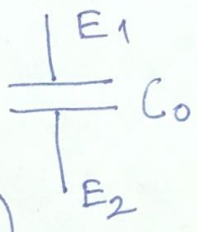


$$C_0 = \epsilon \frac{A}{d}$$

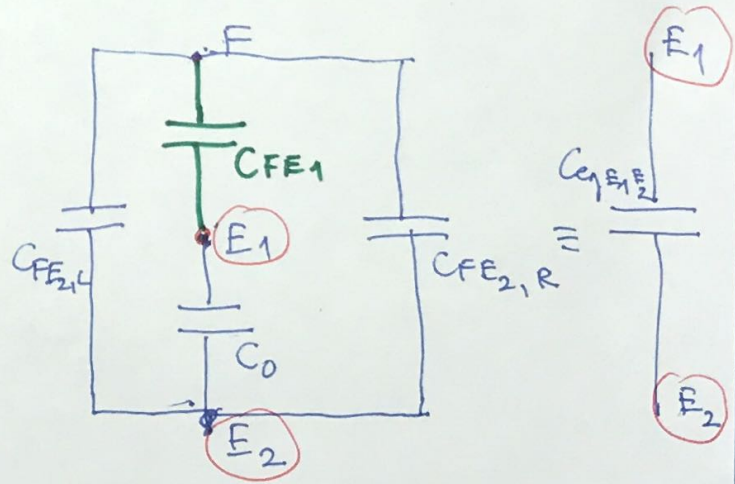
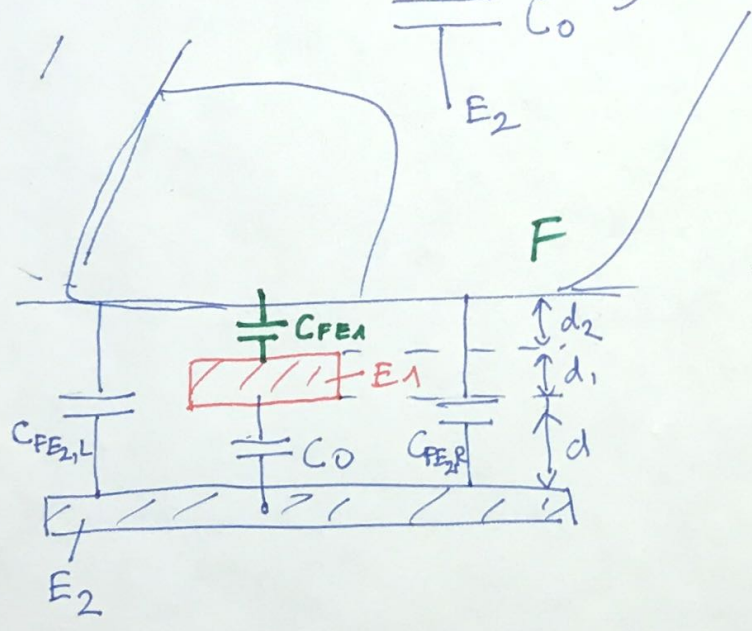
Top view:



Model w/o touch:

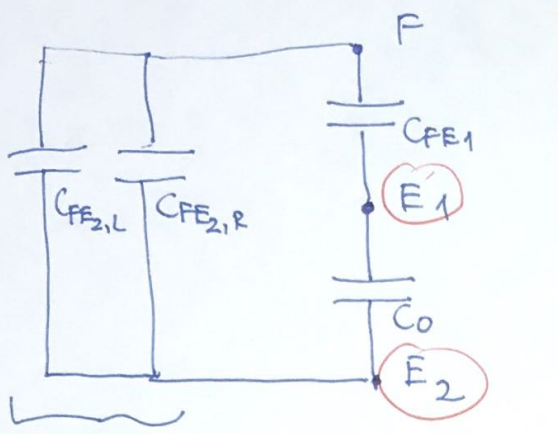


Model with touch:

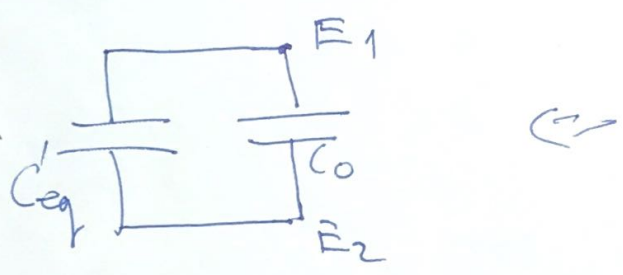
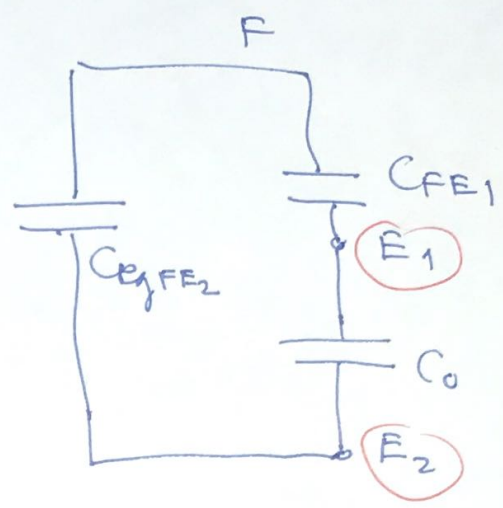


$$C_{eq\ E_1 E_2} = C_0 + C_{FEA} \parallel (C_{FE2,L} + C_{FE2,R})$$

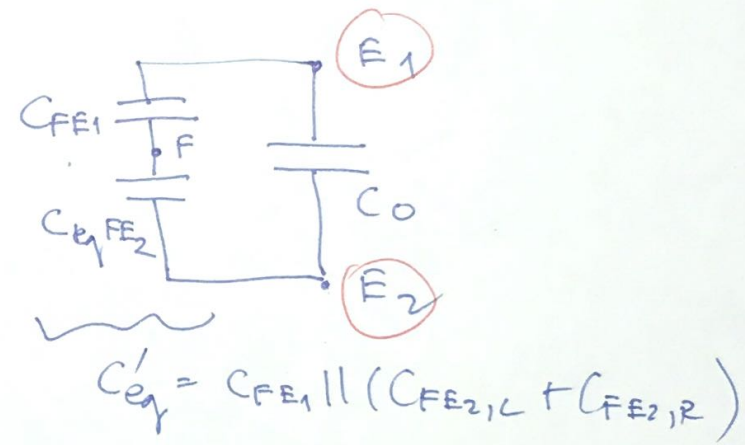
Q2



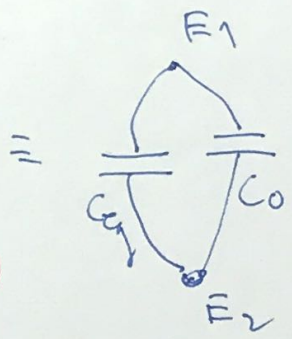
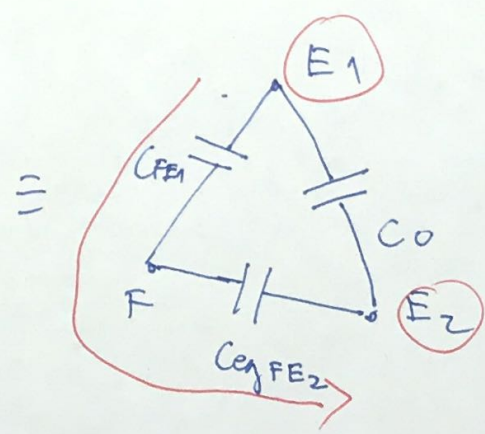
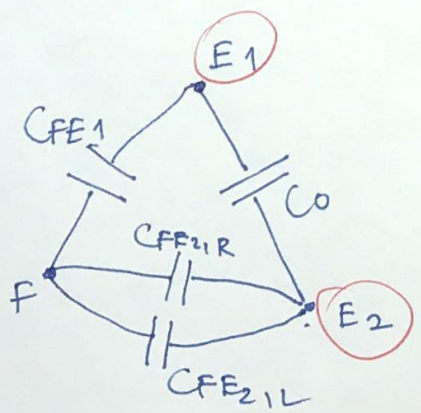
$$C_{eq,FE2} = C_{FE2,L} + C_{FE2,R}$$



$$C_{eq,E1E2} = C_{eq}' + C_0$$



$$C_{eq}' = C_{FE1} \parallel (C_{FE2,L} + C_{FE2,R})$$



Touch:

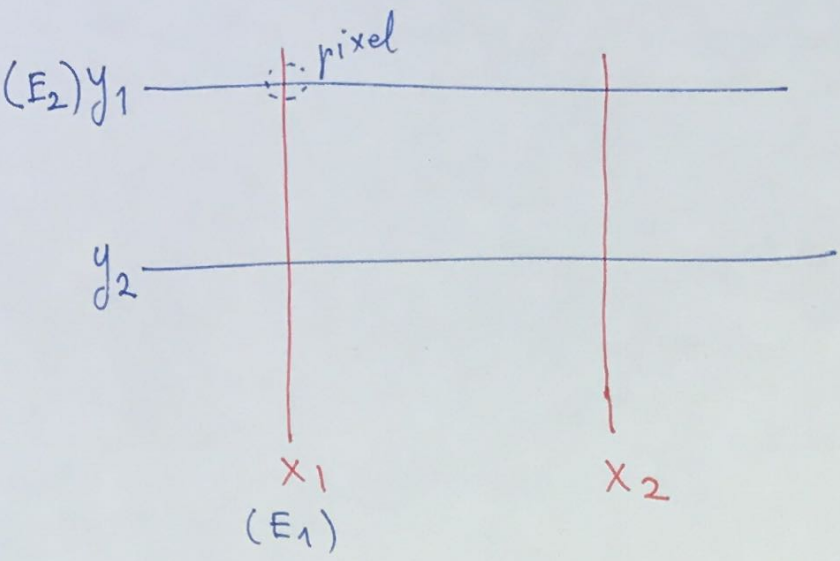
$$C_{eq,E1E2} = C_0 + \Delta C$$

$$\parallel$$

$$C_{FE1} \parallel C_{FE2}$$

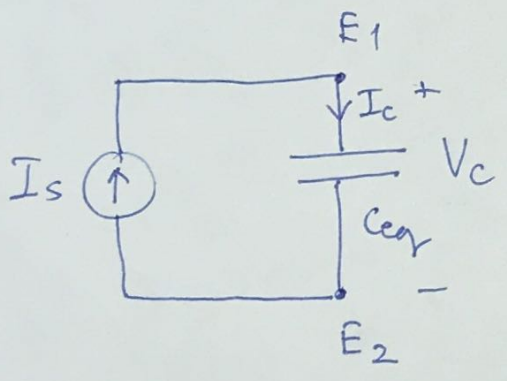
$$> 0$$

(L3) 2D view:



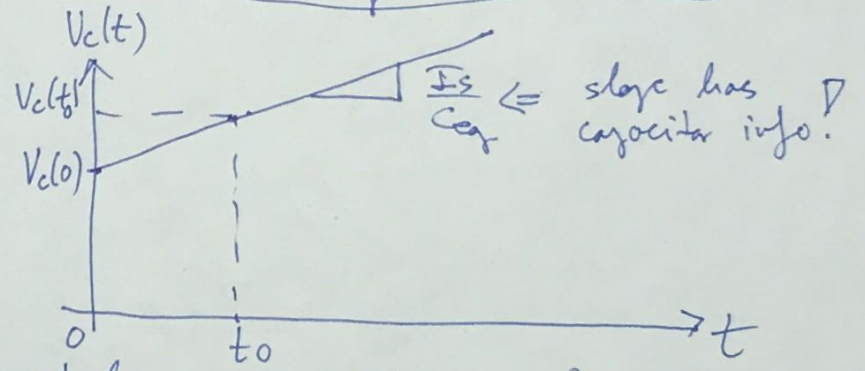
How do we measure the capacitance? }
 turn into voltage or current

Attempt 1: Want to convert capacitance value to voltage.



$$I_c = C_{eq} \cdot \frac{dV_c}{dt}$$

$$V_c(t) = \frac{I_s \cdot t}{C_{eq}} + V_c(0)$$

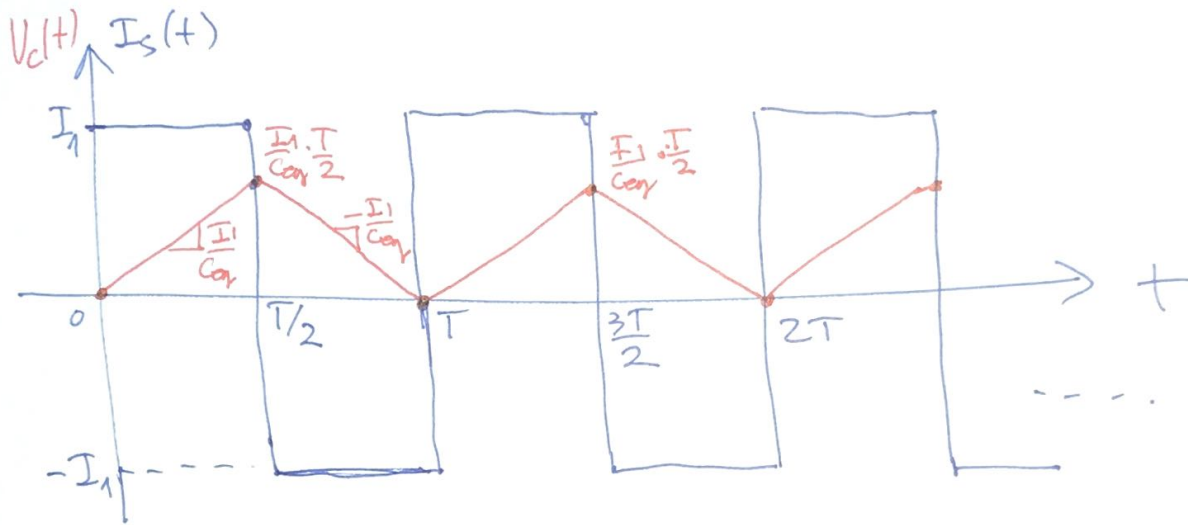
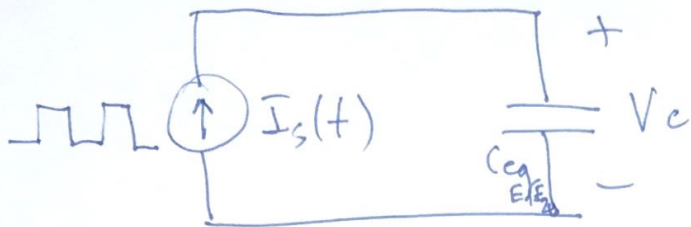


$V_c(t)$ grows indefinitely \Rightarrow problem for potential implementation

In general, for constant I_s from t_0 to t

$$V_c(t) = \frac{I_s}{C_{eq}} \cdot (t - t_0) + V_c(t_0)$$

Q4 Attempt #2: Make current source periodic to limit $V_c(t)$



$$t \in [0, \frac{T}{2}] : I_s(t) = I_1 \Rightarrow V_c(t) = \frac{I_1}{C_{eq}} \cdot t + V_c(0)$$

$$t \in [\frac{T}{2}, T] : I_s(t) = -I_1 \Rightarrow V_c(t) = -\frac{I_1}{C_{eq}} \cdot (t - \frac{T}{2}) + V_c(\frac{T}{2})$$

$$V_c(\frac{T}{2}) = \frac{I_1 \cdot T}{C_{eq} \cdot 2}$$

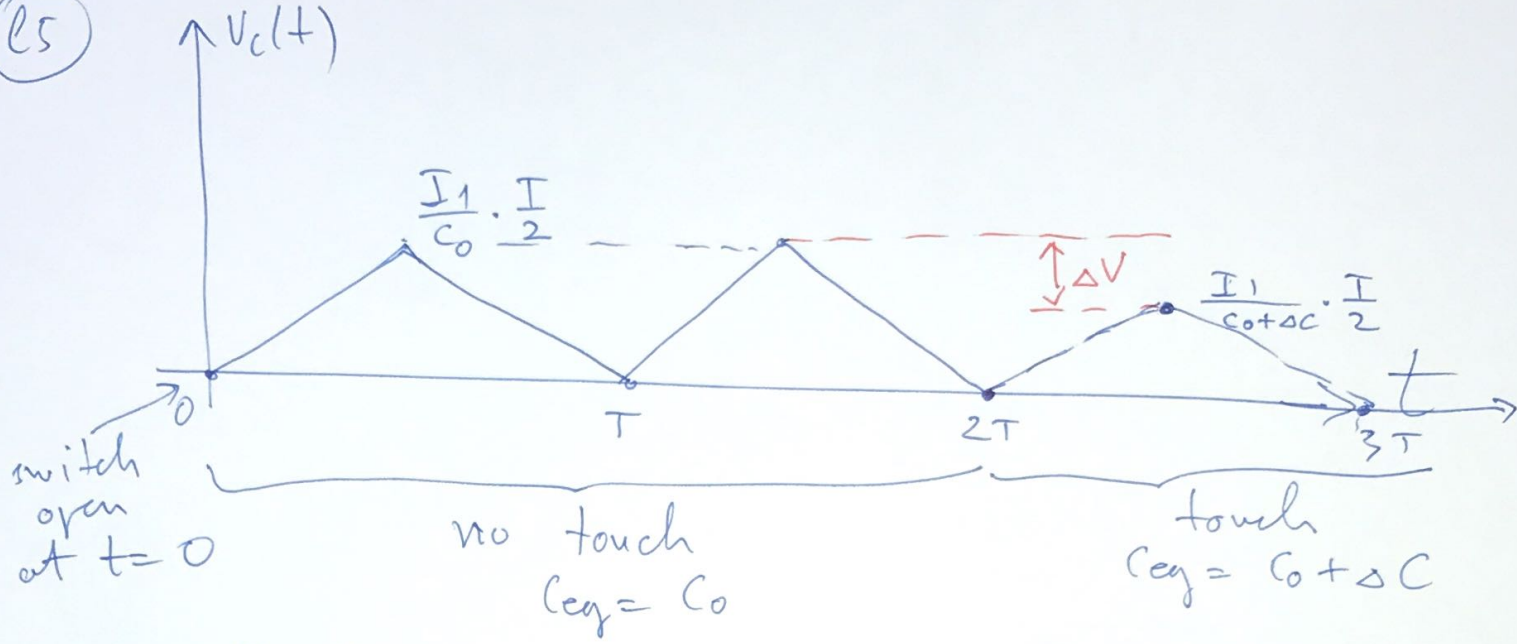
max voltage

$$V_c(t) = -\frac{I_1}{C_{eq}} \cdot t + \frac{I_1 \cdot T}{C_{eq} \cdot 2} + \frac{I_1 \cdot T}{C_{eq} \cdot 2}$$

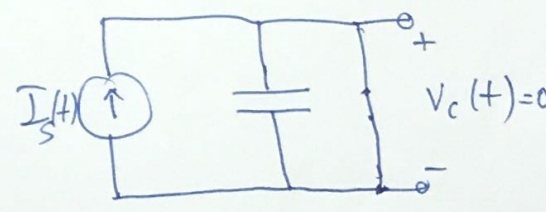
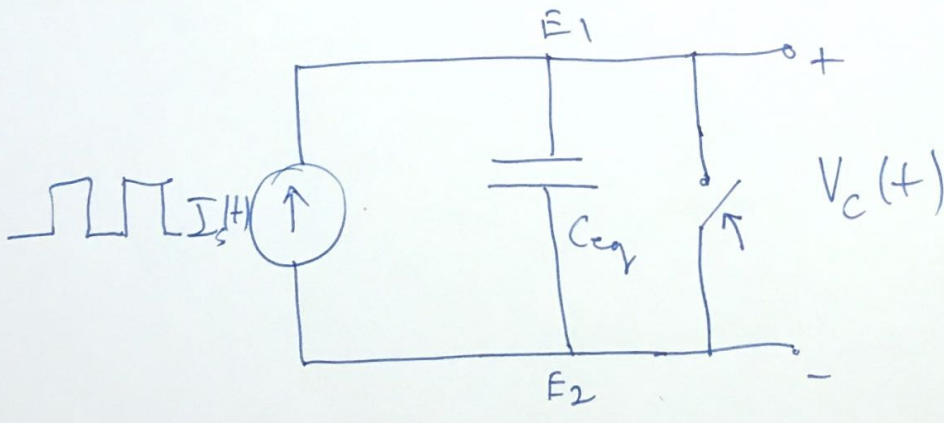
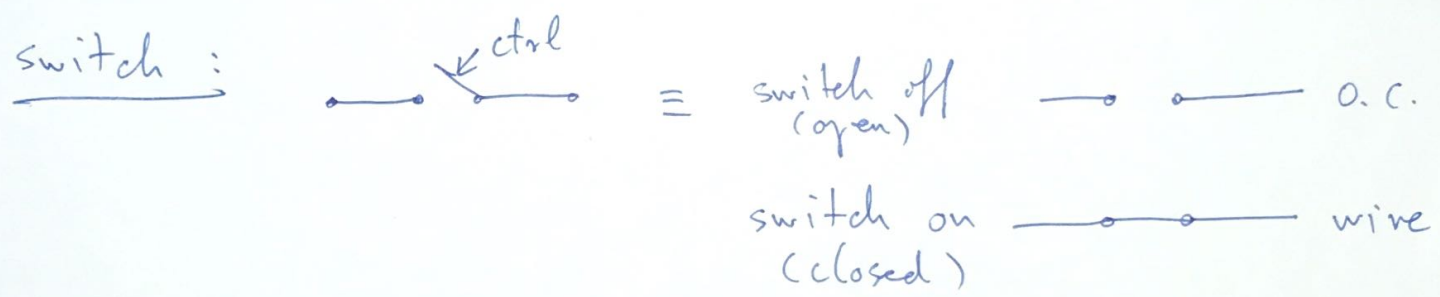
$$V_c(T) = 0$$

$$\frac{I_1 \cdot T}{C_{eq}}$$

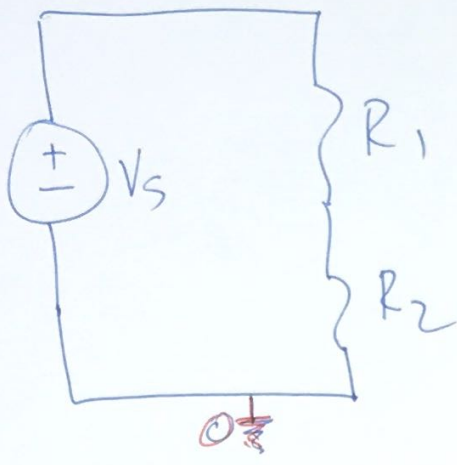
(25)



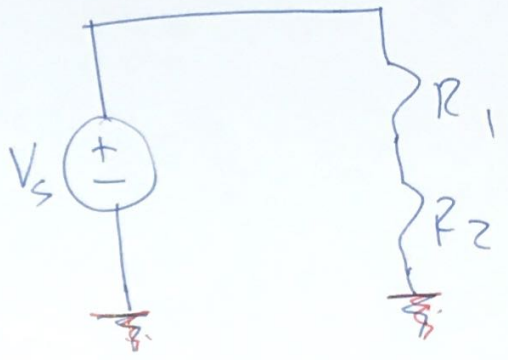
small change:
$$\Delta V = \left(\frac{I_1}{C_0} - \frac{I_1}{C_0 + \Delta C} \right) \cdot \frac{T}{2}$$



Q6



\equiv

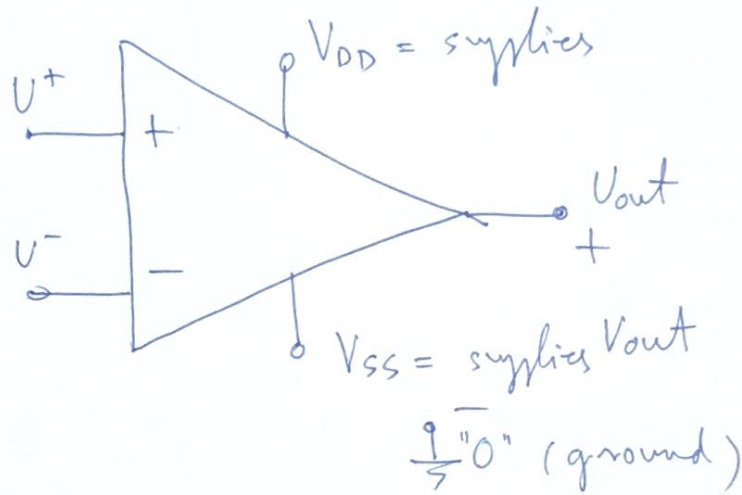


(7)

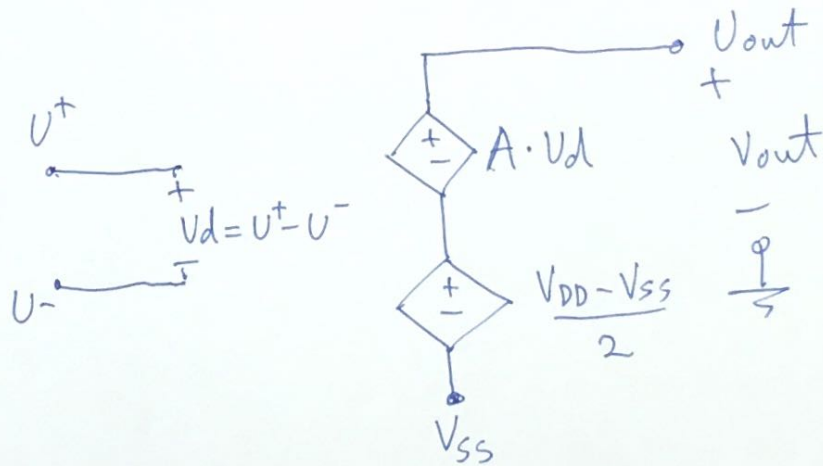
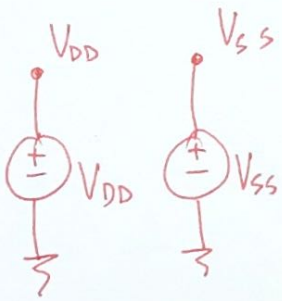
New element: Op-amp

(can be used as a comparator - compare two potentials and determine which one is larger)

Symbol:



Model:



In ideal op-amp $A \rightarrow \infty$

$$KVL: V_{out} = \begin{cases} V_{DD} & , V^* > V_{DD} \\ V_{SS} + \frac{V_{DD} - V_{SS}}{2} + A \cdot U_d & , \text{when } V_{SS} < V^* < V_{DD} \\ V_{SS} & , V^* < V_{SS} \end{cases}$$