

# Welcome to EECS 16A!

## Designing Information Devices and Systems I

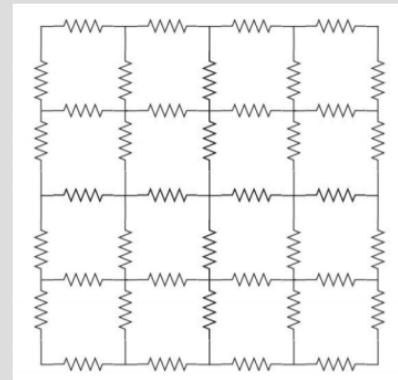
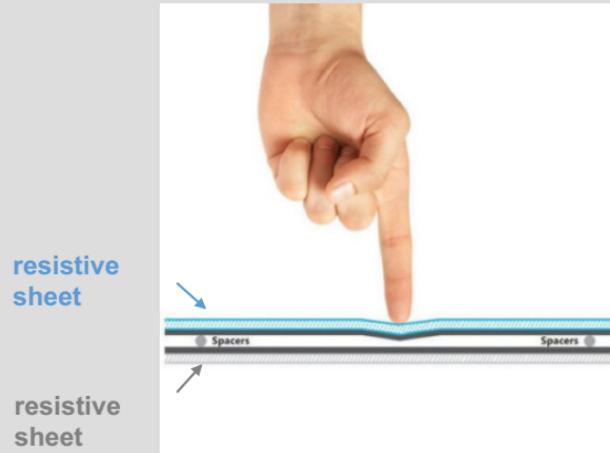


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Fall 2021

Module 2  
Lecture 7  
Capacitors  
(Note 16)



# Now that we understand 2D resistive touchscreen, let's change it!



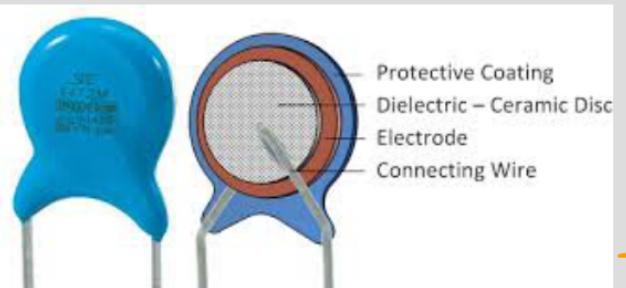
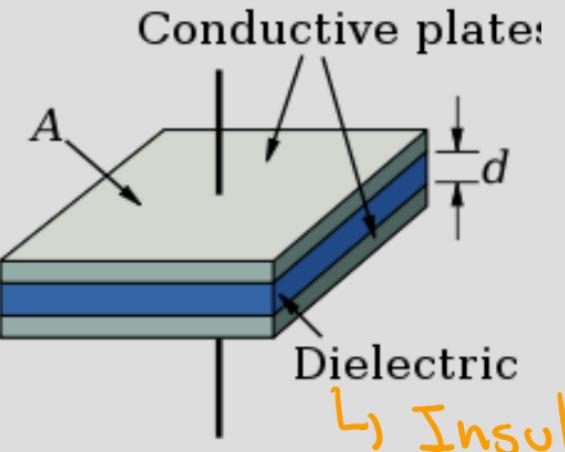
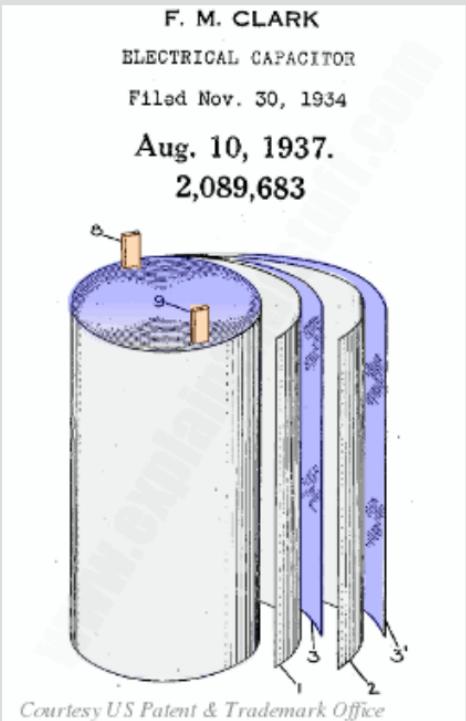
Circuit model for  
each resistive sheet  
is a grid of resistors

real-world touchscreens are usually capacitive, not resistive:

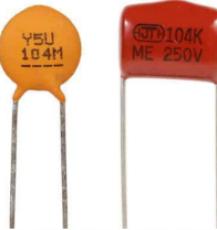
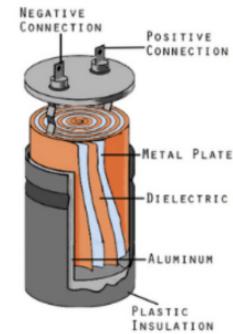
- don't need to be flexible
- multi-touch is easier
- more sensitive
- increased contrast on screen

# Now, Capacitors!

- Charge storage device (like a ‘bucket’ for charge)

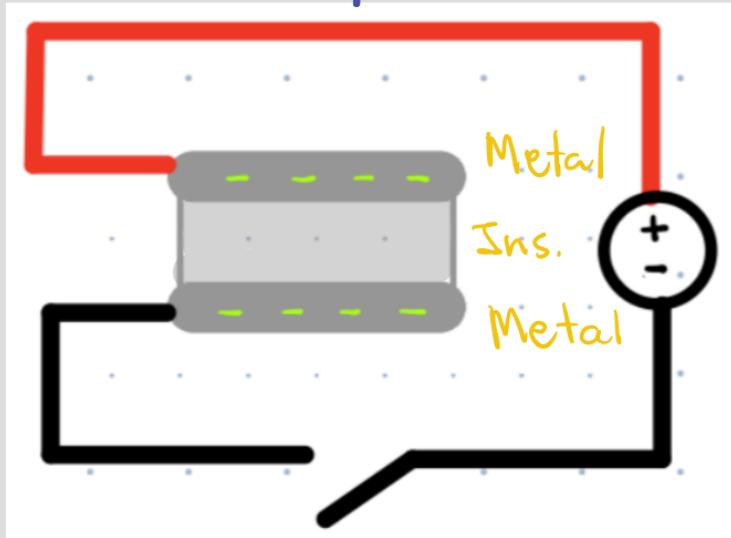


↳ Higher Energy is needed to move charge.



# The Physics of a Capacitor

\* Energy is needed to move charge.



$e^-$

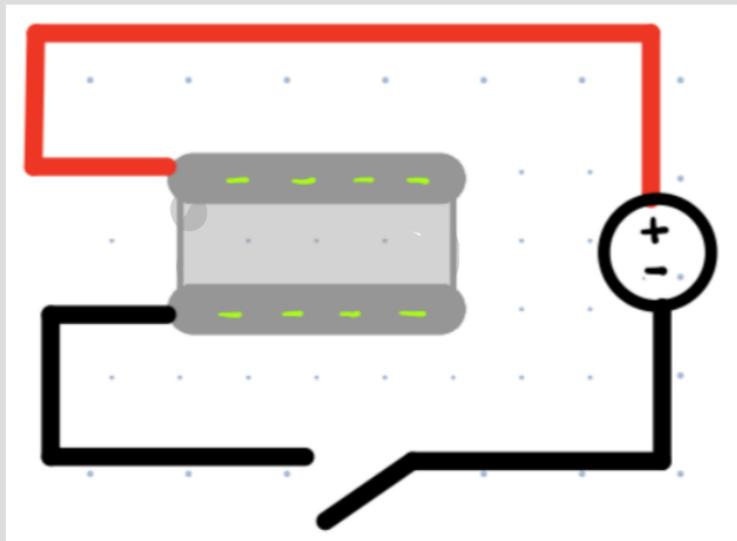
→ No current across  
the capacitor plates

→ Voltage Source  
provides Energy  
needed for flow  
of charges ( $e^-$ )

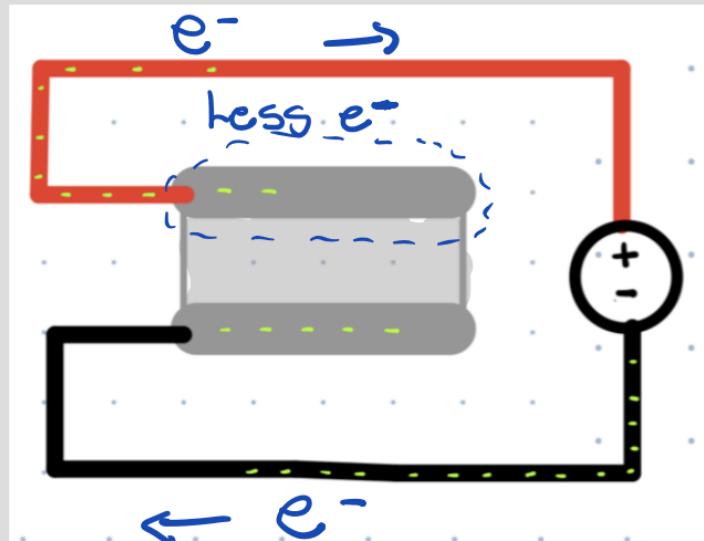
# The Physics of a Capacitor

→ Once the switch is ON e<sup>-</sup> flow!

$t_0$



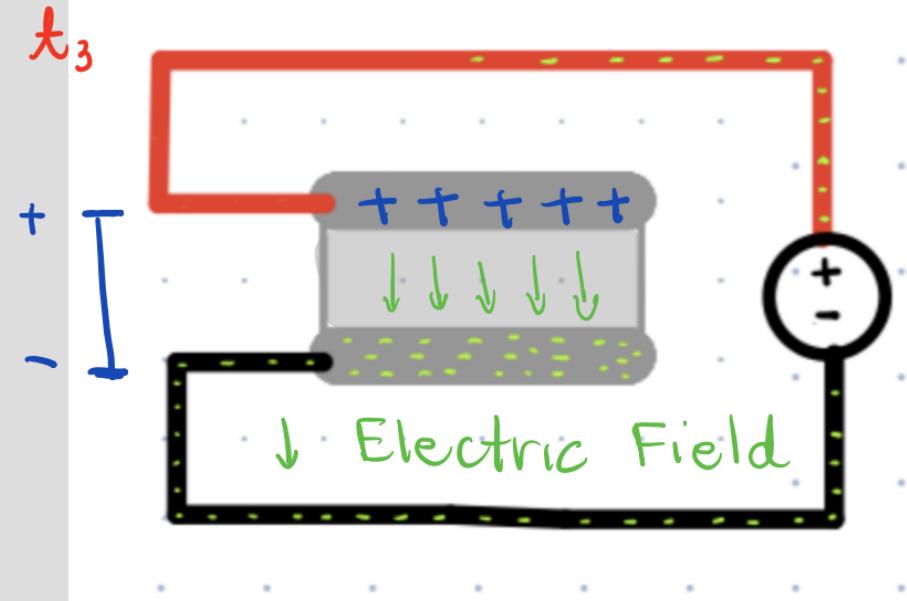
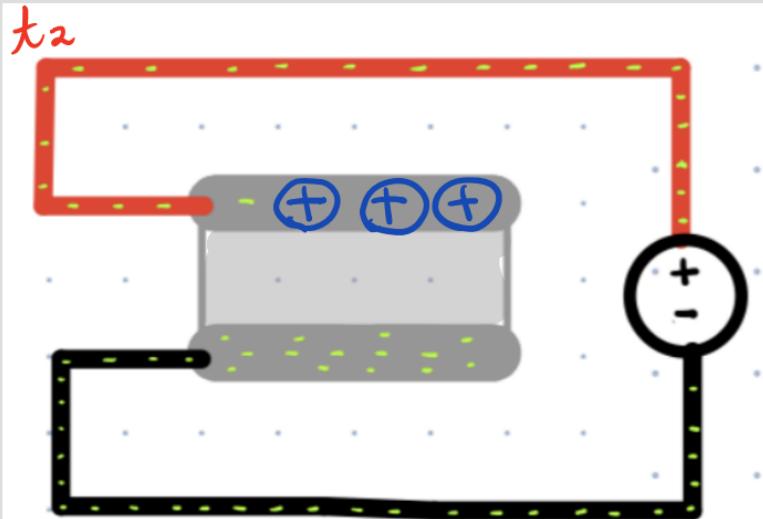
$t_1$



# The Physics of a Capacitor

lack of electrons means holes!

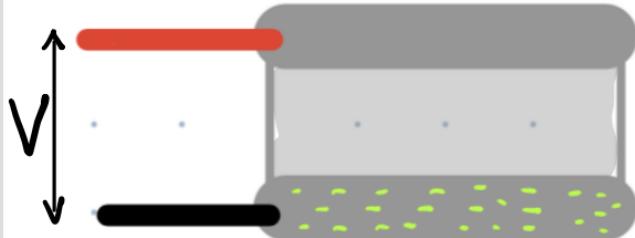
$h^+$



Potential difference  
between the two  
plates!  $V$

# The Physics of a Capacitor

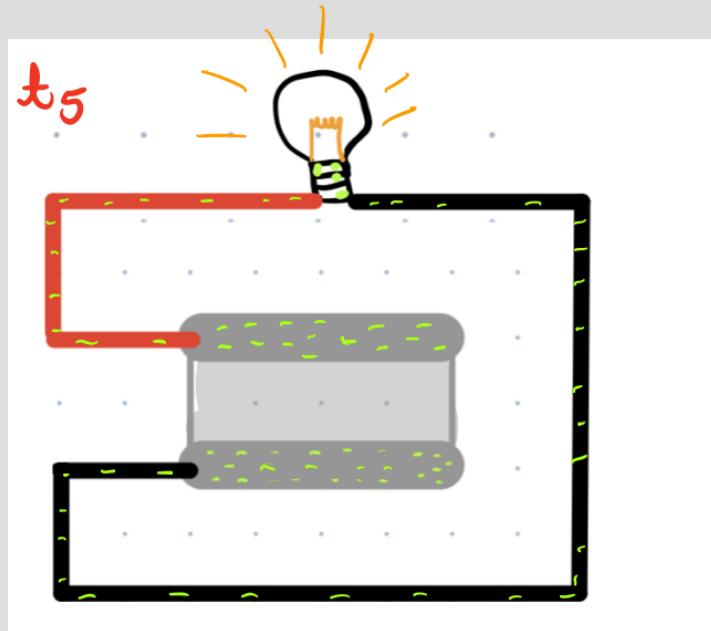
$t_4$  Independent Energy Source



Charges are stored!

Every Capacitor can  
be charged up to a  
fixed Voltage.

<https://www.youtube.com/watch?v=X4EUwTwZ110>



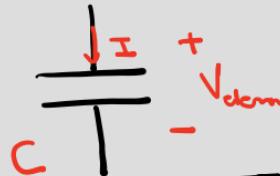
The capacitor will charge a "load" until the charges on the plate are equalized. ( $\text{No change}$ )  
 $\text{in } V$

Charge storage device (like a ‘bucket’ for charge)

Holds electric charge when we apply a voltage across it, and gives up the stored charge to the circuit when voltage removed

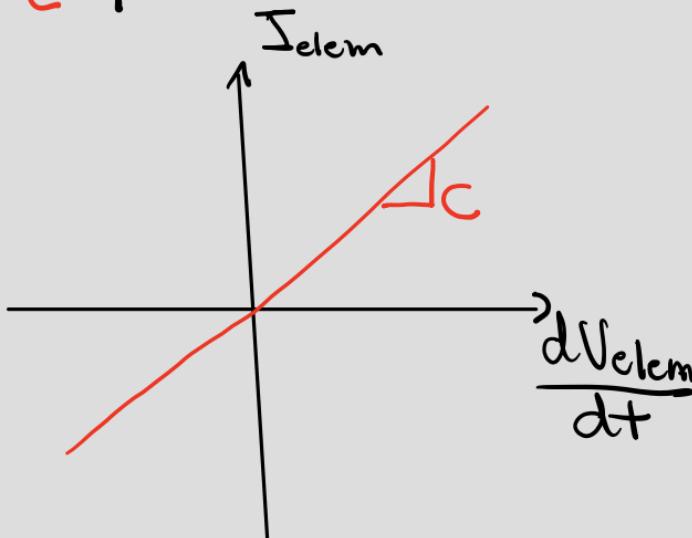
# Circuit Model: IV relationship

Capacitor Symbol



$$Q_{elecm} = C \cdot V_{elecm}$$

[C] [F] [V]  
(Farad)



We know :  $I_{elecm} = \frac{d Q_{elecm}}{dt}$

$$I_{elecm} = \frac{d}{dt} C \cdot V_{elecm}$$

$C = \text{constant over time}$

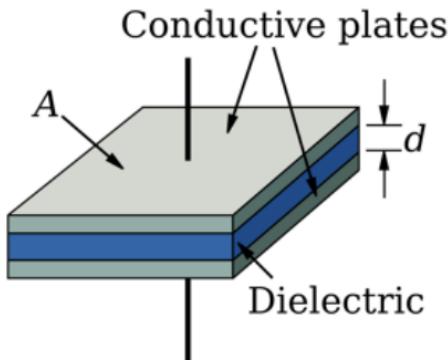
$$I_{elecm} = C \cdot \frac{d V_{elecm}}{dt}$$

↳ Can use the same 7-step analysis.

# Capacitance

$$C = \epsilon \frac{A}{d}$$

$$[F] = [E] \left[ \frac{m^2}{m} \right]$$



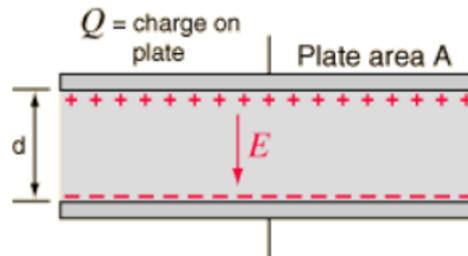
Depends on:

- Materials :  $\epsilon$  permittivity

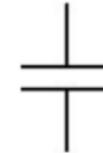
$$\epsilon_0 = 8.85 \cdot 10^{-12} \text{ F/m}$$

$$\epsilon = \epsilon_0 \epsilon_r$$

- Geometry of Conductors



Symbol:



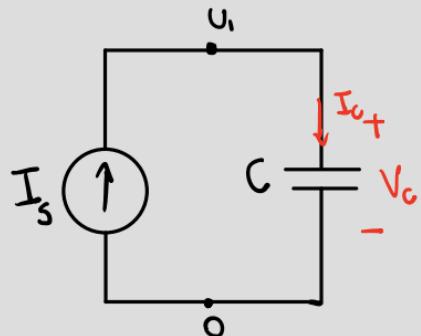
Capacitance:

C

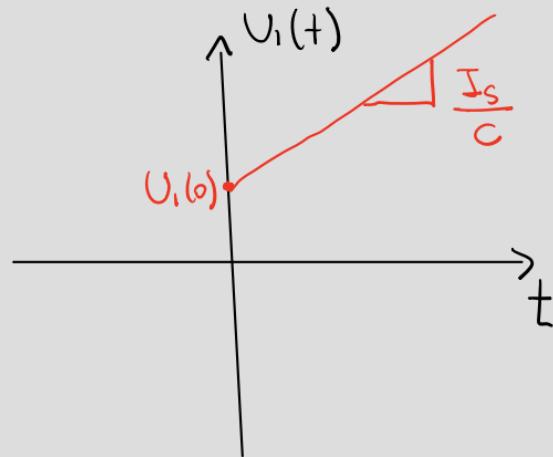
Units: Farads [F]

IV equation:  $I = C \cdot \frac{dV}{dt}$

# Simple Circuit 1



$$\boxed{I_s = C \frac{dU_1}{dt}} \times dt$$



$$KCL : \underline{I_s = I_c}$$

Element Def.:

$$\underline{I_c} = C \cdot \frac{dV_c}{dt}$$

Voltage Def.:

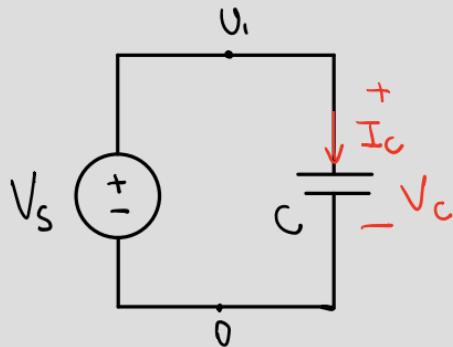
$$U_1 - 0 = V_c$$

$$I_s \cdot dt = C dU_1$$
$$\int_0^+ I_s dt = \int_{U_1(0)}^{U_1(+)} C \cdot dU_1$$

$$I_s + = C \cdot (U_1(+)-U_1(0))$$

$$U_1(+) = \frac{I_s}{C} \cdot + + U_1(0)$$

## Simple Circuit 2



$$\begin{aligned} V_1 - 0 &= V_s \\ V_1 - 0 &= V_c \end{aligned} \quad \left. \begin{array}{l} \text{Voltage Def.} \\ \text{Voltage Def.} \end{array} \right\}$$

$$V_s = V_c$$

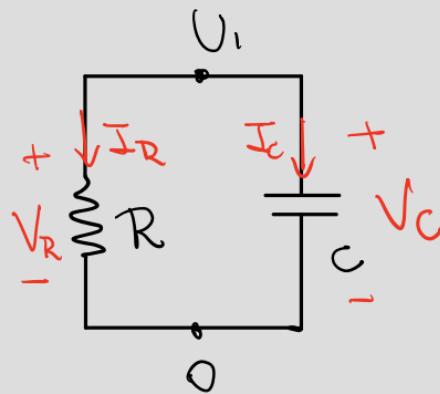
$$I_c = C \frac{dV_c}{dt} \quad (\text{capacitor Def.})$$

$$I_c = C \frac{dV_c}{dt} = C \cdot \frac{dV_s}{dt} = 0$$

Current in a capacitor is zero when  
a constant Voltage source is across it.

Hint: We like zeros... they make our lives easier!

# Simple Circuit 3



$$V_1 = ?$$

Steady State:  
means the Voltages  
Settled.

If current is zero  $\Rightarrow$  OPEN-CIRCUIT

looking for  $V_1$  value when  
 $V_C = \text{const.}$  (steady-state)

$$I_C = C \frac{dV_C}{dt} = 0$$

$$\text{KCL: } I_C + I_R = 0$$

$$I_R = 0$$

$$\text{Ohm's law: } V_R = I_R R = 0$$

$$\text{Voltage Def: } V_1 - 0 = V_R$$

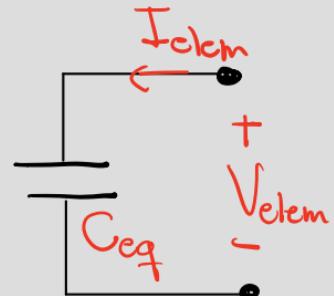
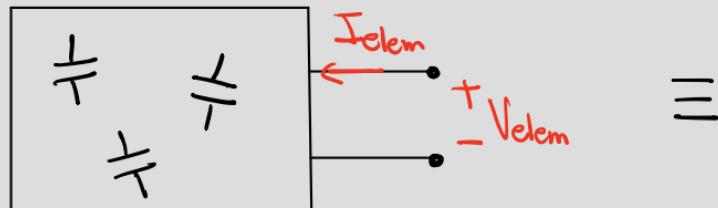
$$V_1 = 0$$

# Equivalent Circuits with Capacitors

\* Capacitor - only circuits

~~Step 1 : Find  $V_{th}$  and  $I_{no}$  no source~~

Step 2 :  $C_{eq} = \frac{I_{elem}}{\frac{dV_{elem}}{dt}}$



only if  
(match  $\frac{dV_{elem}}{dt}$ )

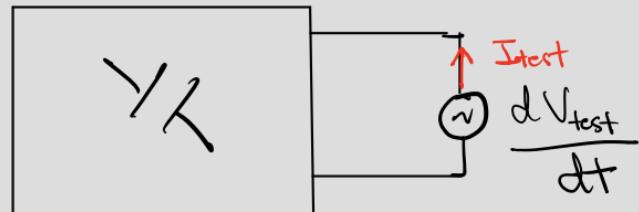
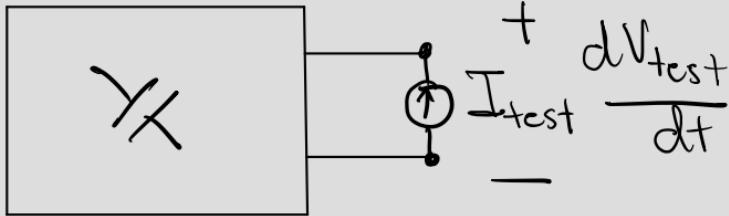
## Two Methods:

a) Apply  $I_{test}$  and measure  $\frac{dV_{test}}{dt}$

b) Apply  $\frac{dV_{test}}{dt}$  and measure  $I_{test}$

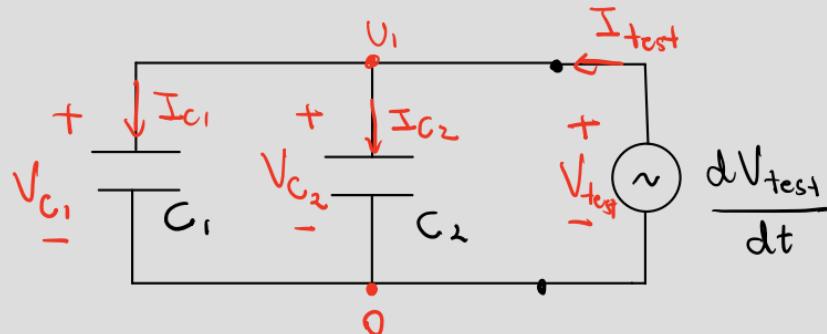
$$= C_{eq} = \frac{I_{test}}{\frac{dV_{test}}{dt}}$$

(a)



Example 1

$$V_{C_1} = U_1, V_{C_2} = U_1 \text{ and}$$
$$U_1 = V_{\text{test}}$$



$$\frac{dU_1}{dt} = \frac{dV_{\text{test}}}{dt}$$

Elem def:  $I_{C_1} = C_1 \frac{dV_1}{dt} = C_1 \frac{dU_1}{dt} = C_1 \frac{dV_{\text{test}}}{dt}$

Elem def:  $I_{C_2} = C_2 \frac{dV_2}{dt} = C_2 \frac{dU_1}{dt} = C_1 \frac{dV_{\text{test}}}{dt}$

KCL:  $I_{\text{test}} = I_{C_1} + I_{C_2} = C_1 \frac{dV_{\text{test}}}{dt} + C_2 \frac{dV_{\text{test}}}{dt}$

$$I_{\text{test}} = (C_1 + C_2) \frac{dV_{\text{test}}}{dt}$$

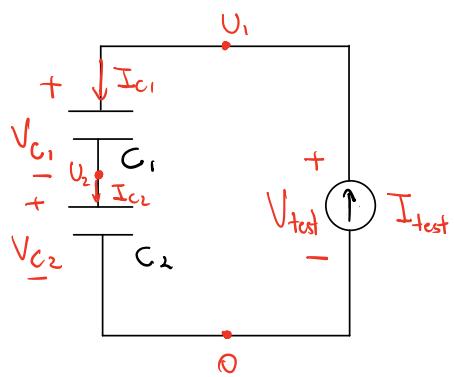
$$C_{\text{eq}} = \frac{I_{\text{test}}}{\frac{dV_{\text{test}}}{dt}} = C_1 + C_2$$



$$\equiv R_{\text{eq}} = R_1 + R_2$$

Series

Example 2 : "Capacitors in series"



$$\text{KCL} : I_{c_1} = I_{c_2} = I_{\text{test}}$$

Elements :

$$I_{c_2} = C_2 \frac{dV_{c_2}}{dt}$$

$$I_{c_1} = C_1 \frac{dV_{c_1}}{dt}$$

Voltage Def.

$$V_{c_2} = U_2 - 0$$

$$V_{c_1} = U_1 - U_2$$

$$V_{\text{test}} = U_1 - 0$$

For  $V_{c_2}$ :

$$I_{c_2} = C_2 \frac{dV_{c_2}}{dt}$$

$$I_{\text{test}} = C_2 \frac{dU_2}{dt} \equiv \frac{dU_2}{dt} = \frac{I_{\text{test}}}{C_2}$$

For  $V_{c_1}$ :

$$I_{c_1} = C_1 \frac{dV_{c_1}}{dt}$$

$$\frac{dV_1}{dt} = \frac{I_c}{C_1} = \frac{dU_1 - dU_2}{dt} = \frac{I_{\text{test}}}{C_1}$$

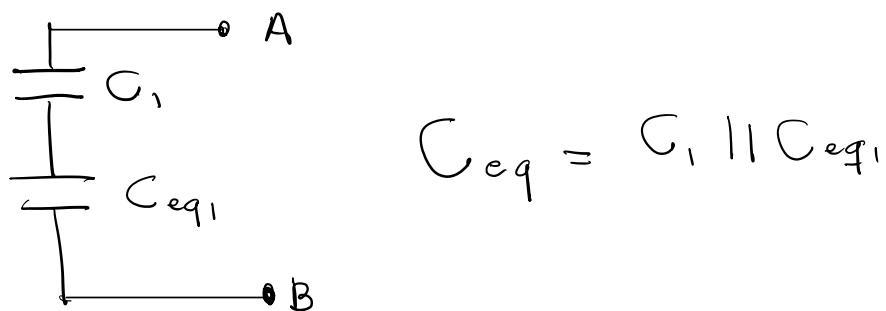
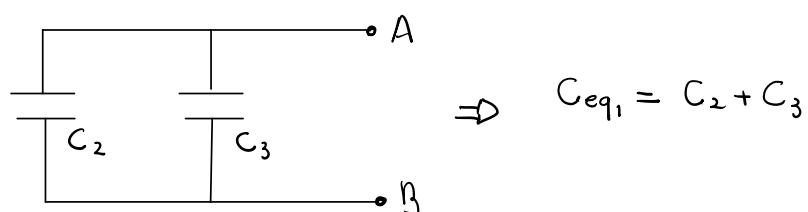
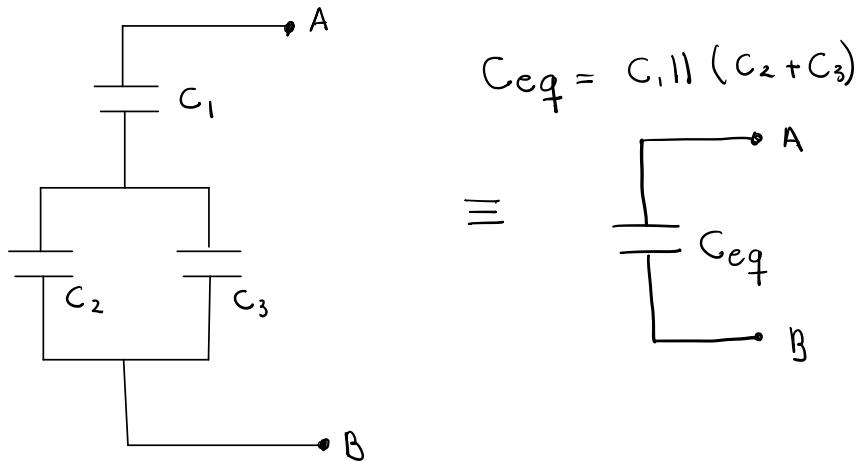
$$\frac{dU_1}{dt} = \frac{dU_2}{dt} + \frac{I_{\text{test}}}{C_1} = \frac{I_{\text{test}}}{C_2} + \frac{I_{\text{test}}}{C_1}$$

$$\frac{dU_1}{dt} = \frac{dV_{\text{test}}}{dt} = I_{\text{test}} \left( \frac{1}{C_2} + \frac{1}{C_1} \right)$$

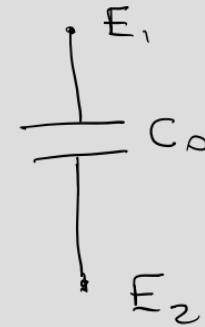
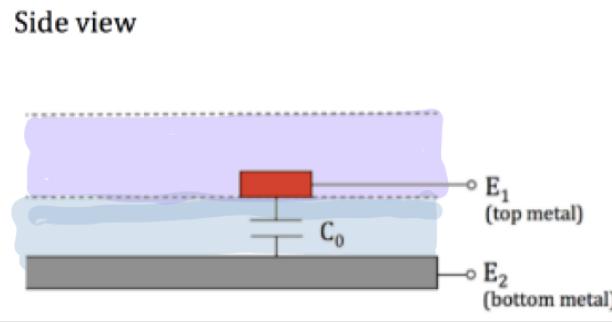
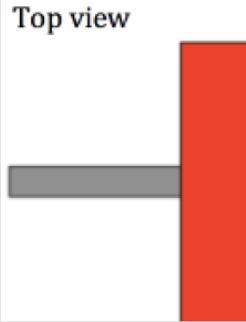
$$C_{\text{eq}} = \frac{\frac{I_{\text{test}}}{dV_{\text{test}}}}{\frac{1}{C_1} + \frac{1}{C_2}} = \frac{1}{\frac{1}{C_1} + \frac{1}{C_2}} = \frac{C_1 C_2}{C_1 + C_2} = C_1 \parallel C_2$$

$$C_{\text{eq}} = C_1 \parallel C_2 \quad (\parallel - \text{parallel mathematical operator})$$

Example 3



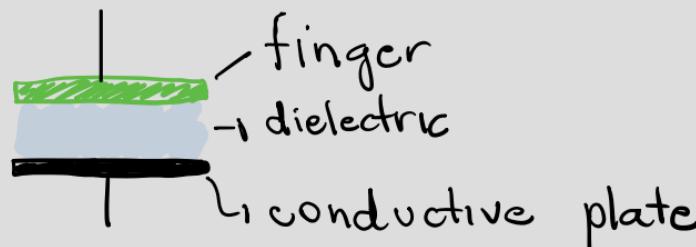
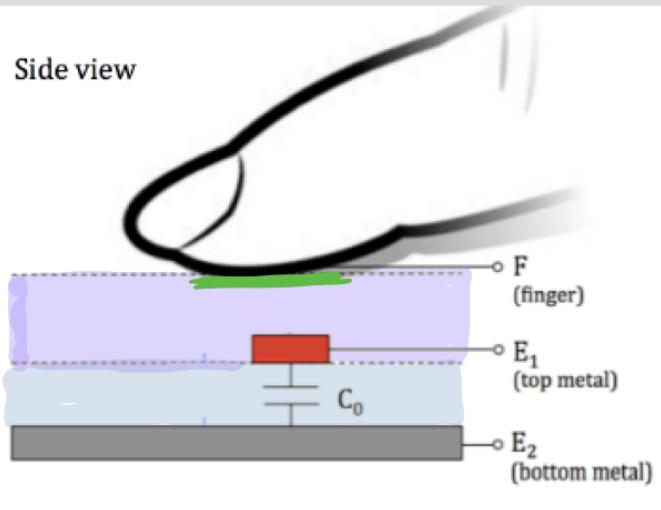
# Capacitive Touchscreen – Model without touch



$$C_0 = \epsilon \cdot \frac{A}{d}$$

# Capacitive Touchscreen – Model with touch

When there is a touch, it makes a capacitor!

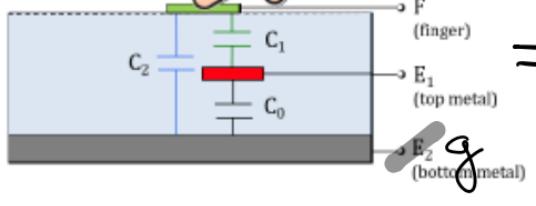


Problem: How can Voltage/Current when the finger is one of the terminals?

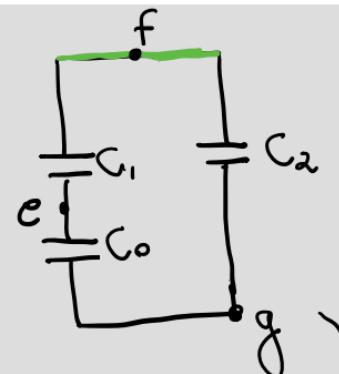
Solution: Models / Good architecture

Side view

with touch

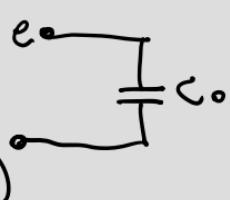


⇒ circuit model

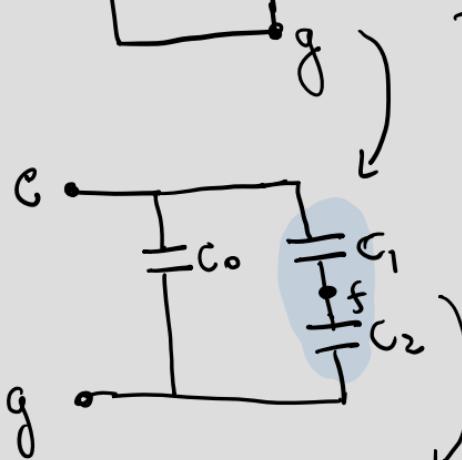
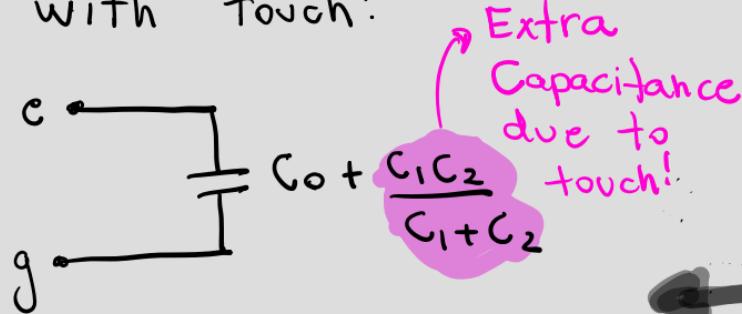


We only have access to nodes e and g, not f

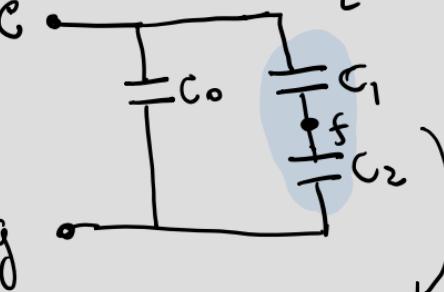
when no touch:



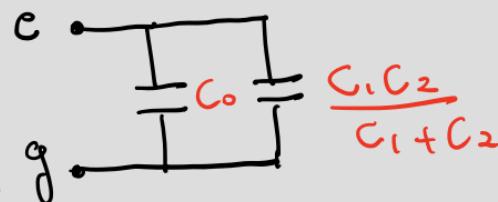
with touch:



Redraw to focus on terminals (nodes) e and g



Equivalent capacitance  
for C<sub>1</sub> in series with  
C<sub>2</sub>



⇒ Equiv. Capacitance  
for C<sub>0</sub> in parallel  
to  $\frac{C_1 C_2}{C_1 + C_2}$