





Welcome to EECS 16A!

Designing Information Devices and Systems I

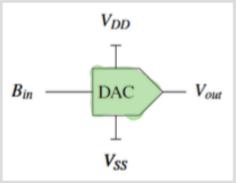


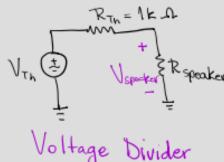
Ana Claudia Arias and Miki Lustig

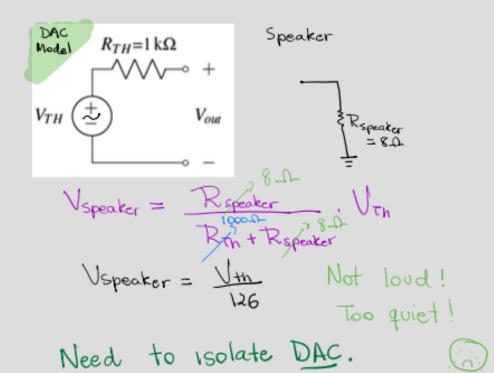
Module 2 Lecture 10 Negative Feedback (Note 18)



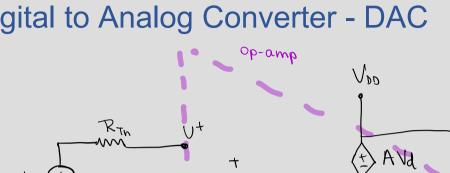
Last Class...

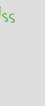


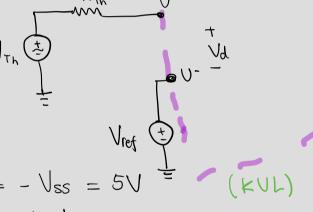


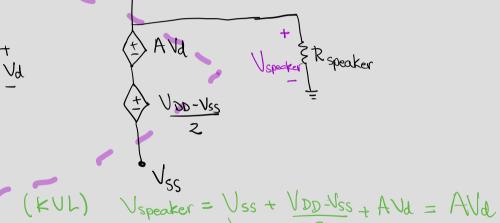


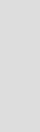
Digital to Analog Converter - DAC

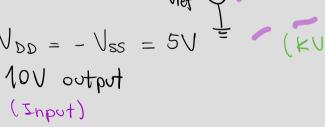






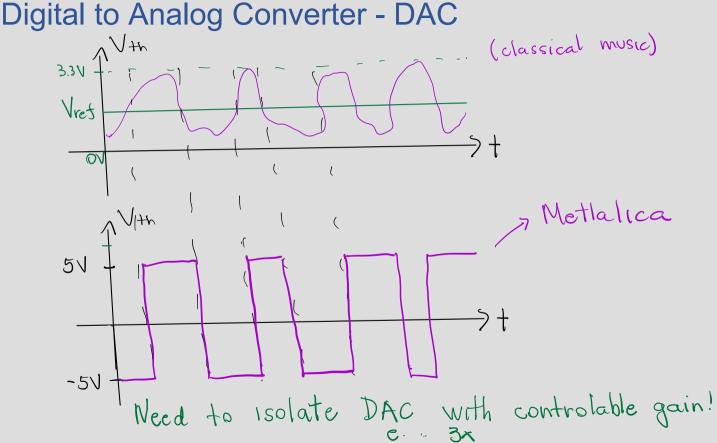






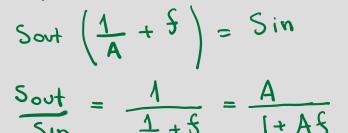
Vd= U+ _U = V+h - Vret

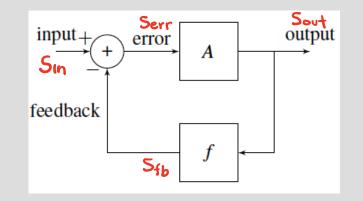
Uss< AUd < Von



Negative Feedback

$$\frac{S_{\text{out}}}{A} = S_{\text{in}} - S_{\text{fb}}$$
Sout $\left(\frac{1}{A} + \frac{1}{A}\right) = S_{\text{in}}$

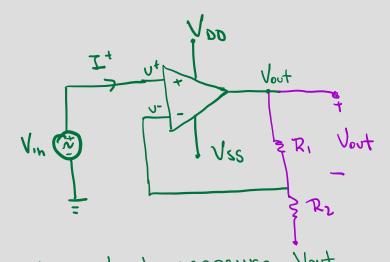




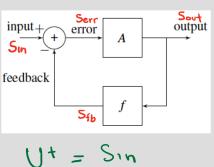
- Making small adjustments to correct output on the fly
- Basis of control theory
- Many examples in daily life:
- Biology - Self-driving car - Human driving car - Hand-eye coordination

Negative Feedback

Need to isolate the DAC from speaker - OP-Amp with NFB



· We want to measure Vout, take a portion of the signal and Seedback as V

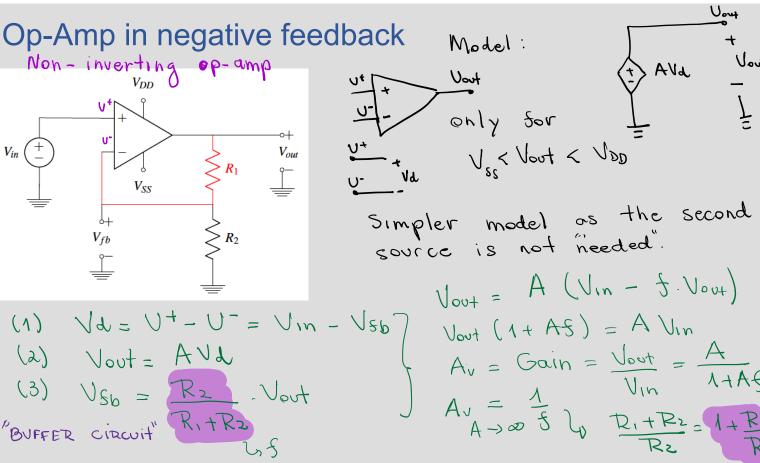


$$U^{+} = Sin$$

$$Vout = Sout$$

$$U^{-} = Sfb$$

$$U^{+} = Serr$$



Golden Rules of Op-Amps For our design we want A = 3

$$Vd = \frac{Vout}{A} \quad \text{if} \quad A \to D$$

Vd = 1 A Vin = Vin = 0

(2) U+=U- (only in NFB & A -> 00)

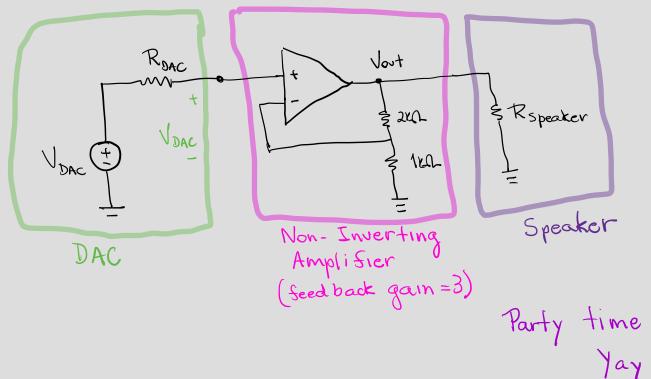
In NFB:
$$U^+=U^-$$
 and $A\to\infty$

Rules: (Golden Rules)

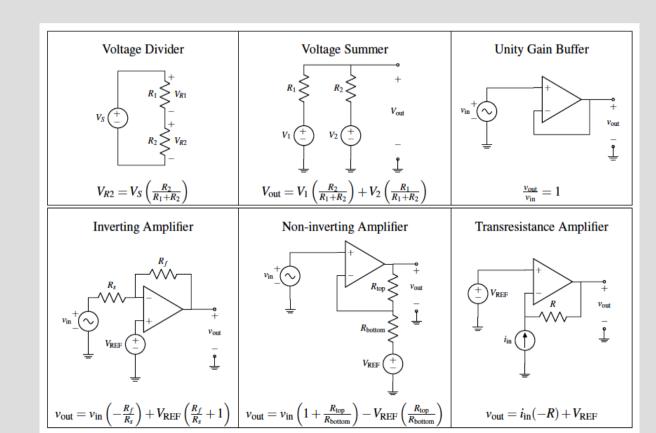
(1) $I^+=I^-=0$ (always true)

 $I^*=I^-=0$

Let's go back to playing music



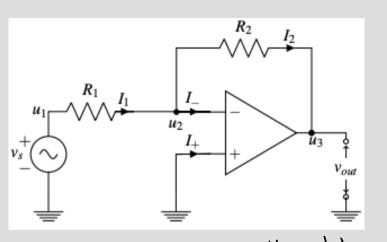
Today

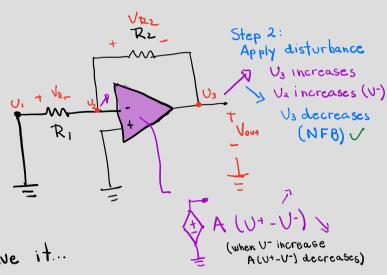


Checking for Negative Feedback (Determing the polarity of NFB)

Step 1 – Zero out all independent sources : replacing voltage sources with wires and current sources with open circuits as in superposition

- Step 2 Wiggle the output and check the loop to check how the feedback loop responds to a change.
 - if the error signal decreases, the output must also decrease. The circuit is in negative feedback
 - if the error signal increases, the output must also increase. The circuit is in positive feedback



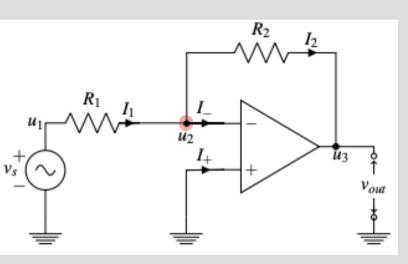


$$V_{1n} = V_{1n}$$

$$V_{2} = V_{2n}$$

$$V_{2n} = V$$

A faster way...

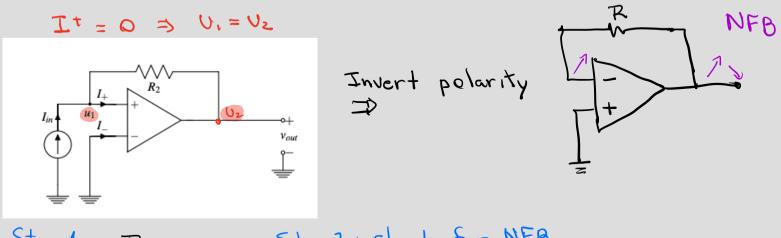


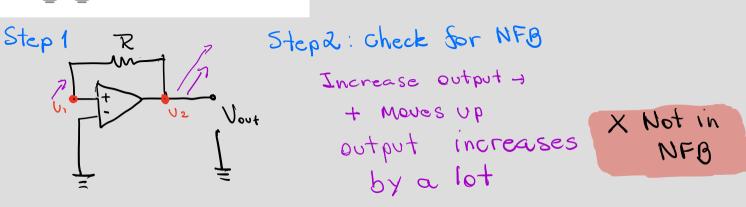
GRZ:
$$U^{\dagger} = U^{-}$$
 $V_{2} = V^{-}$
 $V^{\dagger} = 0 \Rightarrow V_{2} = 0$

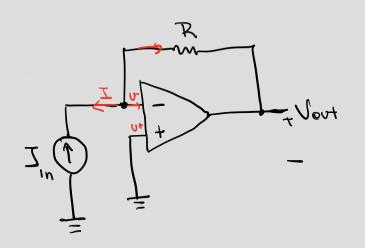
GR1+ KCL $\left(I_{1} = I_{2} + I^{-}\right)$
 $\frac{V_{2}^{2} - V_{1}}{R_{1}} = \frac{V_{3} - V_{2}}{R_{2}} + \frac{V_{0}}{R_{2}}$
 $\frac{V_{1}}{R_{1}} = \frac{V_{3}}{R_{2}}$

$$=-\frac{R_2}{R}$$

Example circuit 2 (trans-resistance amplifier)







 $V^{+} = V^{-}$ $V^{+} = 0 \rightarrow V^{-} = 0$

$$\frac{GZ + Z}{R} + (-I_m) + Z = 0$$

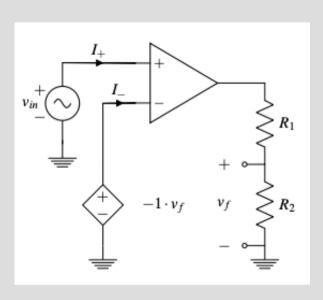
$$\frac{-U_{out}}{R} = I_m$$

 $\frac{V_{out}}{I_{in}} = -R$

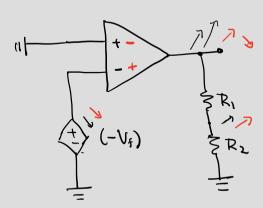
Vout = - In R

The input is current; output is Voltage: we use this model in the lab sor photo sensors.

Example circuit 3 -



Check NFB:



Voltage Divider

$$V_{\xi} = \frac{R_{z}}{R_{1}+R_{z}}$$

NFB (GR#2) U= Ut

 $V_{in} = -V_{\xi}$

U+

$$V_{in} = -\frac{R_2}{R_1 + R_2} V_{out} \rightarrow \frac{V_{in}}{V_{out}} = -\frac{R_2}{R_1 + R_2}$$

$$Av = \frac{V_{out}}{V_{in}} = -\frac{R_1 + R_2}{R_2} = -\left(1 + \frac{R_1}{R_2}\right)$$

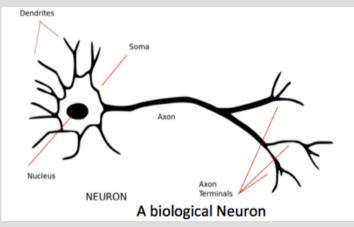
Artificial Neuron

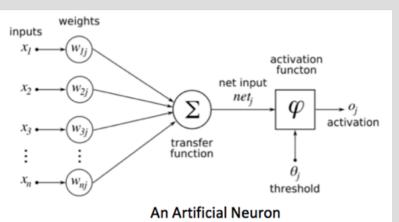
Neurons in our brain are interconnected.

Neuron Networks — Yes we can!

- The output of a single-neuron is dependent on inputs from several other neurons.
- This idea is represented with vector-vector multiplication the output is a linear combination of several inputs.
- An artificial neuron circuit must perform addition and multiplication.

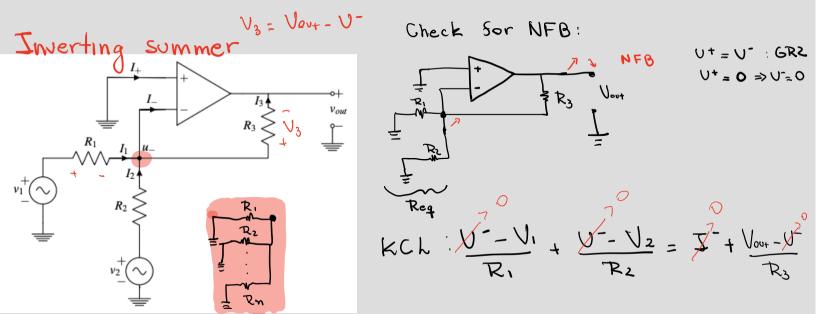
$$\begin{bmatrix} a_1 & a_2 \end{bmatrix} \cdot \begin{bmatrix} V_1 \\ V_2 \end{bmatrix} = a_1 V_1 + a_2 V_2$$





Artificial Neuron

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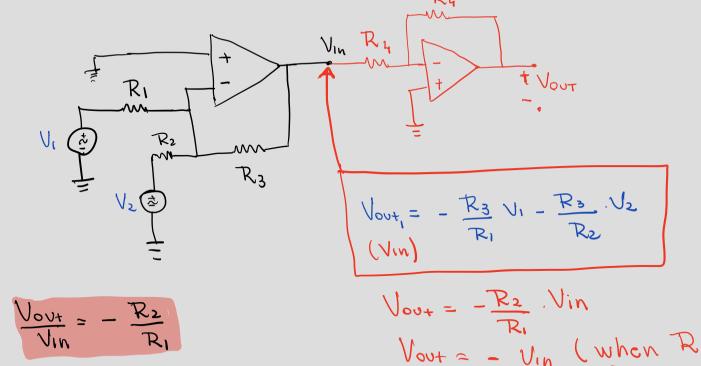


$$-\frac{V_1}{R_1} - \frac{V_2}{R_2} = \frac{V_{ovt}}{R_3}$$

$$V_{ovt} = -\frac{R_3}{R_1} \cdot V_1 + \left(-\frac{R_3}{R_2} \cdot V_2\right) + \cdots + \left(-\frac{R_3}{R_N} \cdot V_N\right)$$
only negative weights
$$Coef.$$

All weights are negative: How can we make a, and az positive?

Add another inverting amplifier circuit.



h result from Inverting amplifier Vout = - Vin (when Ri and Rz are thz Same)

Unity Gain Buffer

4 Allows us to isolate Design

loading

DetU=

U+ = Vin GRZ

U+ = U-Vin = VouT I+ = 0 = D U+ = VDAC Vout = Uspeaker = U-VDAC = Vspeaker