

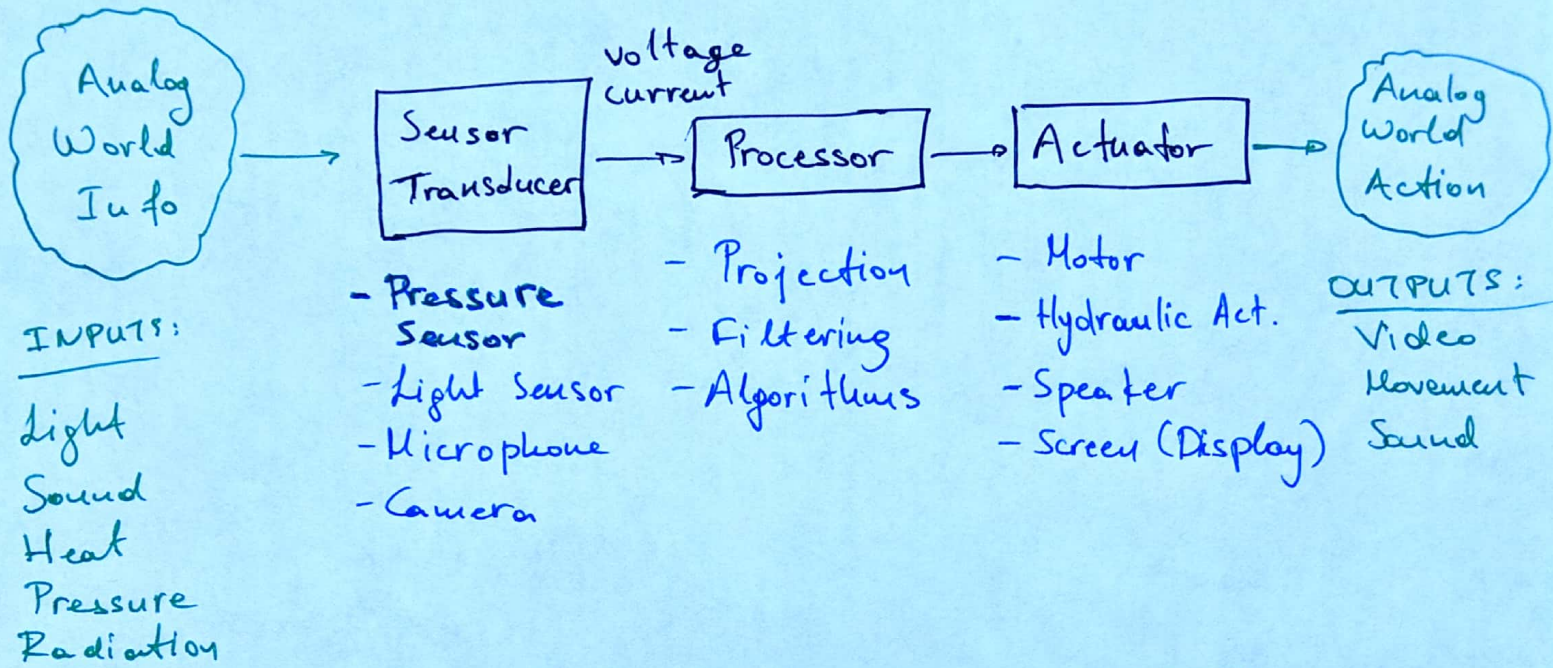
P1

Module 2, Lecture 1

ECS 16A
Panos Zarkos

Today: * Sensing, Processing and Actuating Systems
* Intro to Circuit Analysis

Note !!



Electrical Circuit Analysis (tool)

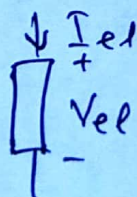
SPICE

Quantities	Analytical Symbol	Units
Voltage	V	[V] Volts
Current	I	[A] Amperes
Resistance	R	[Ω] Ohms

Circuit Diagram

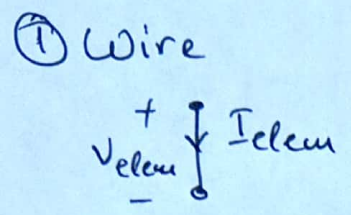
Collection of elements with some voltage across them and some current through them.

Generic Element:

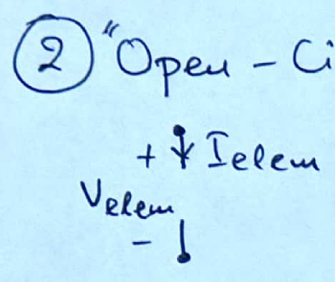
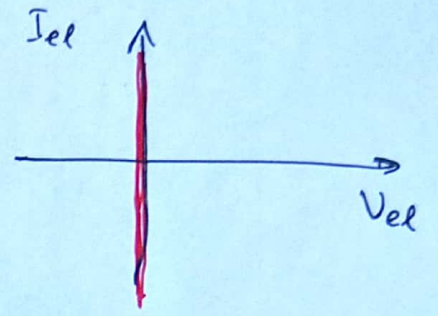


Note: Voltage is a relative quantity (always measured in between two points)

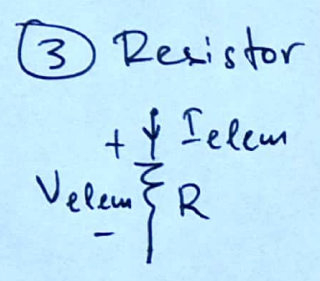
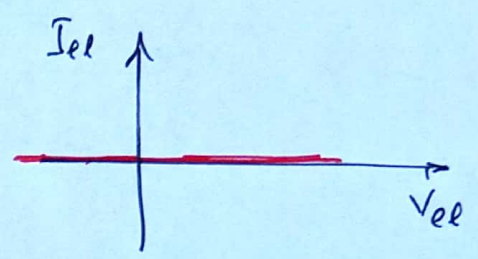
P2 Useful, Common Circuit Elements



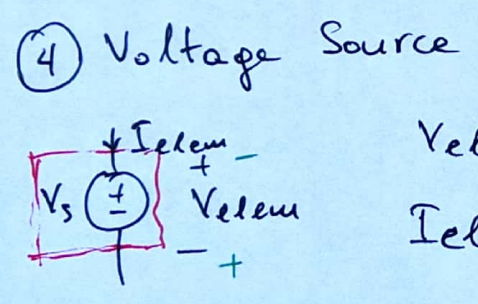
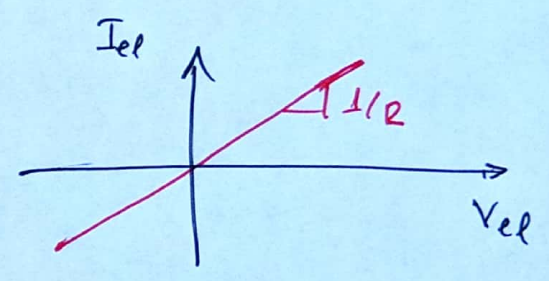
$V_{elem} = 0$
 $I_{elem} = ?$ (set by ext ckt)



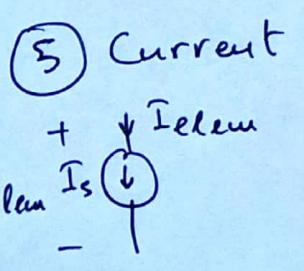
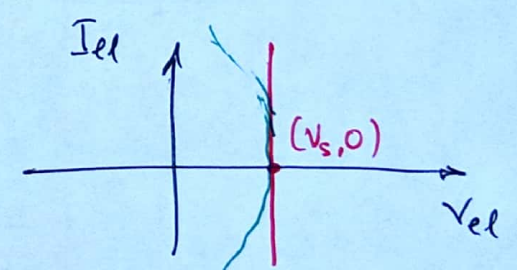
$V_{elem} = ?$ (set by ext ckt)
 $I_{elem} = 0$



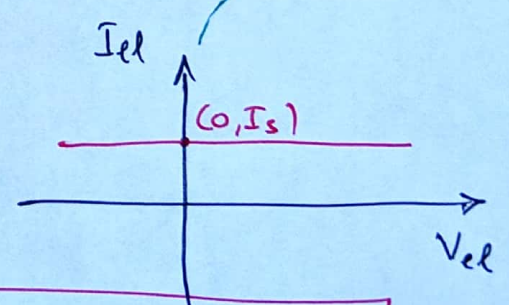
$V_{elem} = R \cdot I_{elem}$



$V_{elem} = -V_s$
 $I_{elem} = ?$ (set by ext ckt)



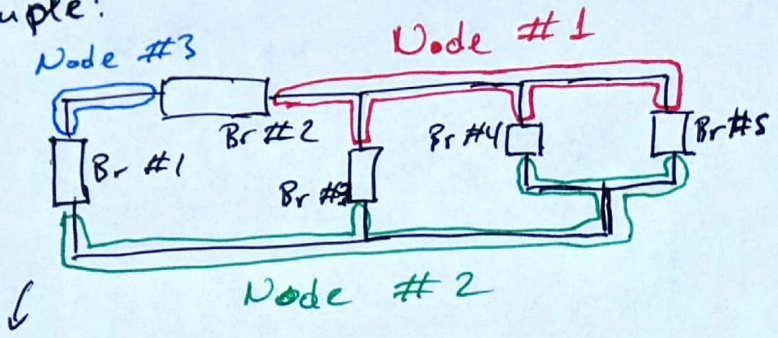
$V_{elem} = ?$ (set by ext ckt)
 $I_{elem} = I_s$



Note: I_{el} , V_{el} can be positive and negative!

Q3 Nomenclature of Ckt Diagrams: Nodes + Branches

example:



Node: Any point on the ckt where two or more elements intersect (meet)

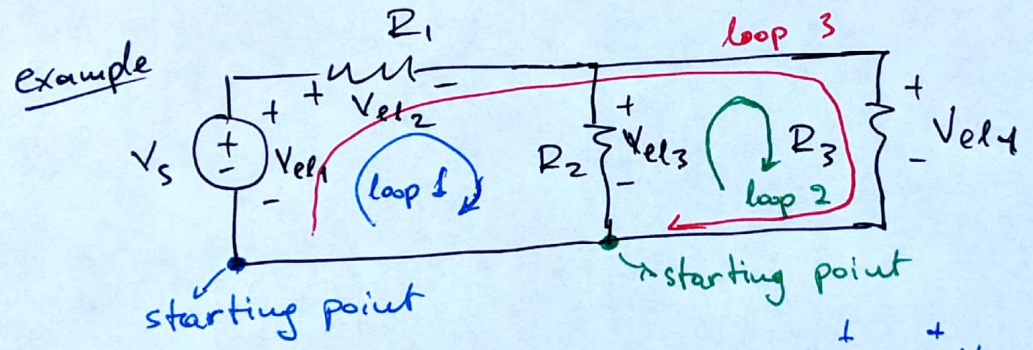
Branch: Any ckt element apart from wires and open ckt's

3 Nodes
5 Branches

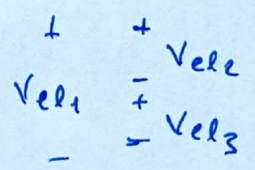
Rules (laws) for Circuit Analysis:

Ⓐ Kirchhoff's Voltage Law (KVL):

The sum of element voltages around a loop is equal to 0.



$$+V_{e1} - V_{e2} - V_{e3} = 0$$

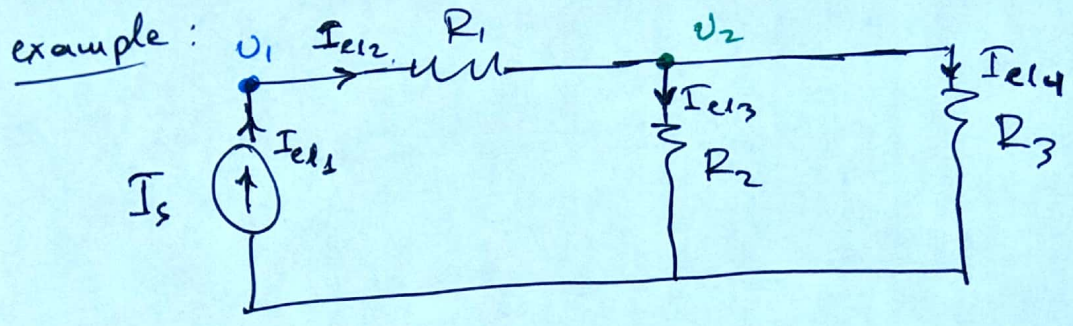


$$V_{e3} - V_{e4} = 0 \quad \begin{matrix} + & + \\ V_{e3} & V_{e4} \\ - & - \end{matrix}$$

$$V_{e1} - V_{e2} - V_{e4} = 0$$

(P4) (B) Kirchhoff Current Law (KCL):

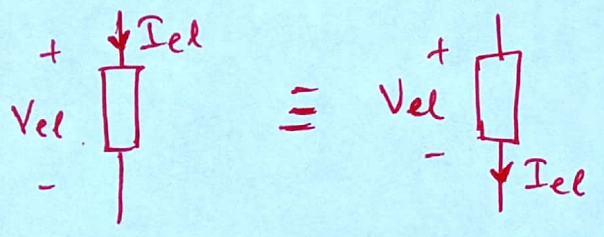
The sum of the current entering a node must equal the sum of ^{the} current exiting the node.



KCL on v_1 : $I_{el1} = I_{el2}$

KCL on v_2 : $I_{el2} = I_{el3} + I_{el4}$

KCL within the elements



Passive Sign Convention:

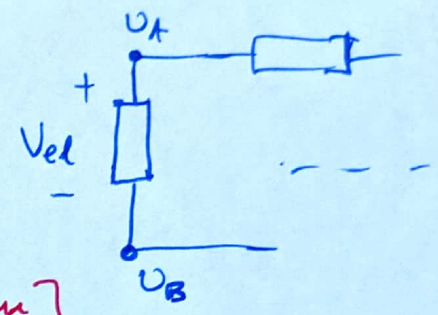
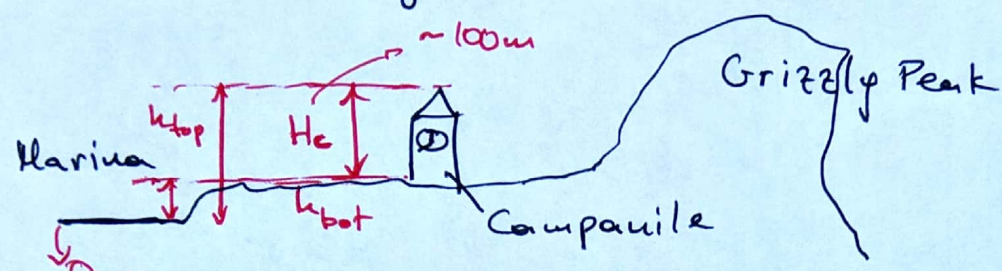
I_{el} is entering the "+" terminal or exiting the "-" terminal!

PS

Voltage Reference - Node Voltages (Potentials)

Element Voltage \leftrightarrow Height measurement

Node Voltage Potential \leftrightarrow Elevation (altitude) measurement



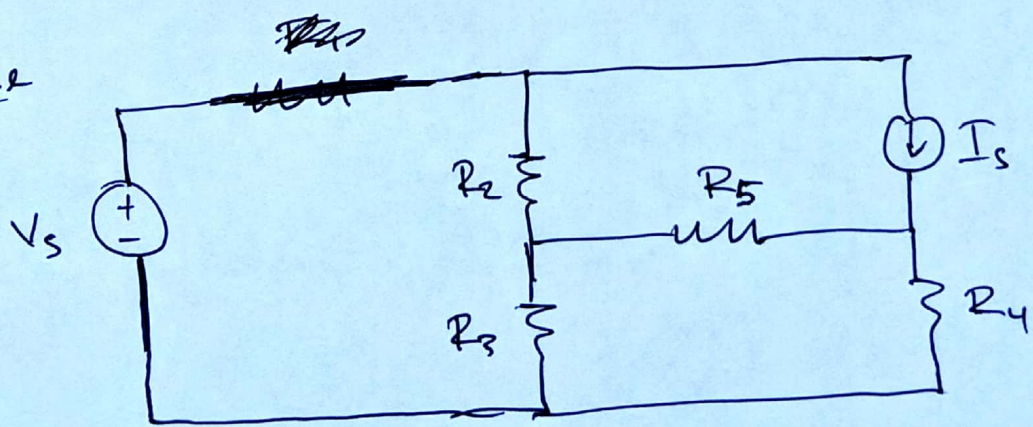
$H_c = h_{top} - h_{bot}$
 height [m] \rightarrow elevation measured from sea level (reference) [m]

element voltage [V] $V_{el} = V_A - V_B$ \rightarrow node voltages [V] measured from a reference point in our ckt (ground)

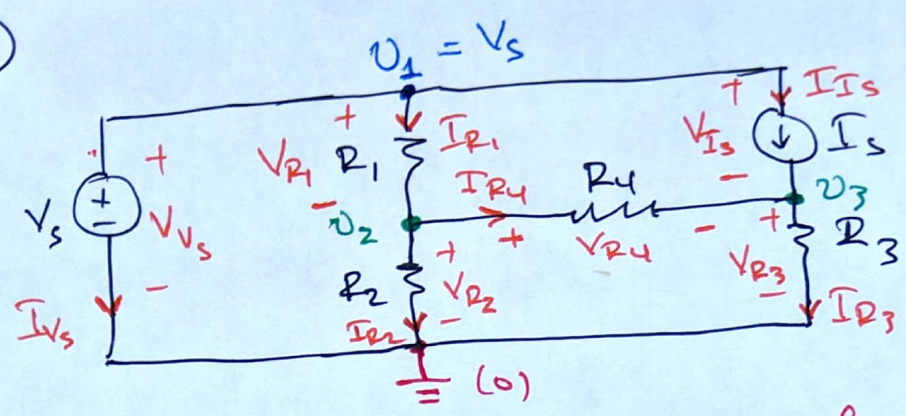
Node Voltage Analysis (NVA) algorithm

Goal: Find all node voltage and all element currents

example



(p6)



Step 1: Pick and label a reference node. (0 or ground)

Step 2: Label all nodes whose voltage is set by voltage sources

Step 3: Label all remaining nodes (unknown)

Step 4: Label all element current and element voltages according to passive sign convention.

Step 5: Write a KCL equation for every node with unknown voltage.

$$v_2 : I_{R1} = I_{R4} + I_{R2}$$

$$v_3 : I_{R4} + I_{Is} = I_{R3}$$

(p7)

Step 6: Express all element currents as functions of element voltages and circuit component characteristics

$$I_{R_1} = \frac{V_{R_1}}{R_1}, \quad I_{R_2} = \frac{V_{R_2}}{R_2}, \quad I_{R_3} = \frac{V_{R_3}}{R_3}, \quad I_{R_4} = \frac{V_{R_4}}{R_4}$$

(Ohm's law)

Step 7: Substitute all element voltages with node voltages in your step 6 equations.

$$I_{R_1} = \frac{V_{R_1}}{R_1} = \frac{U_1 - U_2}{R_1} = \frac{V_s - U_2}{R_1}$$

$$I_{R_2} = \frac{V_{R_2}}{R_2} = \frac{U_2 - 0}{R_2} = \frac{U_2}{R_2}$$

$$I_{R_3} = \frac{V_{R_3}}{R_3} = \frac{U_3 - 0}{R_3} = \frac{U_3}{R_3}$$

$$I_{R_4} = \frac{V_{R_4}}{R_4} = \frac{U_2 - U_3}{R_4}$$

Step 8: Substitute element currents from step 7 into the KCL equations from step 5.

$$I_{R_1} = I_{R_4} + I_{R_2} \Rightarrow \frac{V_s - U_2}{R_1} = \frac{U_2 - U_3}{R_4} + \frac{U_2}{R_2} \quad (1)$$

$$I_{R_4} + I_{I_3} = I_{R_3} \Rightarrow \frac{U_2 - U_3}{R_4} + I_s = \frac{U_3}{R_3} \quad (2)$$

Step 9: Solve

Use C.E. to solve the system of equations (1), (2).