## EE16B Designing Information Devices and Systems II Fall 2015 Section 11B

Partner 1:	Partner 2:

Rate yourself on your understanding/confidence for each midterm topic. Use a scale from 1 to 5, where 5 is very confident.

Topic	Details or Examples	Partner 1 Rating	Partner 2 Rating
Digital Logic	<ul><li>nMOS and pMOS transistors</li><li>Digital logic gates</li><li>Boolean algebra</li></ul>		
Digital Gate Delay	<ul> <li>1<sup>st</sup>-order differential equations</li> <li>RC transistor models</li> </ul>		
Digital System Power	Dynamic voltage scaling		
Sampling and Aliasing	<ul><li>Sampling continuous-time signals</li><li>Aliasing for sine and cosine functions</li><li>Rotating wheel effect</li></ul>		
Signal Reconstruction	<ul><li>Nyquist sampling theorem</li><li>Periodicity of sampled signals</li><li>Ideal low-pass filter placement</li></ul>		
Frequency Response	<ul><li>Magnitude response</li><li>Phase response</li><li>Bode plots</li></ul>		
Impedance	<ul><li>Impedance of inductors and capacitors</li><li>Finding frequency response of a circuit</li></ul>		
X/O Plots	<ul> <li>Making X/O plots via vector subtraction</li> <li>Finding frequency response from X/O plots</li> </ul>		
State Space Models	<ul><li>Discrete-time state space formulation</li><li>Open loop response</li><li>Closed loop response</li></ul>		
System Stability	<ul><li>Eigenvalues and eigenvectors</li><li>Linear phase portraits</li></ul>		

Now choose one of the topics above, and write and solve a problem about that topic that coul reasonably appear on the midterm.
Topic:
Problem Statement
Solution
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