

1) EECS 16B Thrus Feb 20
Frequency Response of LRC ckt - 'Resonance'

Analysis

$V_{in}(s) \rightarrow \frac{1}{j\omega L} \text{ --- } R \text{ --- } \frac{1}{j\omega C} \rightarrow V_o(s)$

$V_o(t) = \text{Re}\{V_o e^{j\omega t}\}$ (complex)
 $V_{in}(t) = V_{in} \cos \omega t = \text{Re}\{V_{in} e^{j\omega t}\}$ (real)

$$\frac{V_o}{V_{in}} = H(j\omega) = \frac{1/j\omega C}{1/j\omega C + R + j\omega L} = \frac{1/LC}{(j\omega)^2 + j\omega \frac{R}{L} + 1/LC}$$

2) Recall time domain analysis [last week]

$$A = \begin{bmatrix} 0 & 1/L \\ -1/L & -R/L \end{bmatrix}$$

char eq. $\det(\lambda I - A) = \lambda^2 + \frac{R}{L}\lambda + \frac{1}{LC}$

roots: $\lambda_{1,2} = -\frac{1}{2} \frac{R}{L} \pm \sqrt{(\frac{1}{2} \frac{R}{L})^2 - \frac{1}{LC}}$

eigenvalues: may have complex values

when $\frac{1}{LC} > (\frac{1}{2} \frac{R}{L})^2$ (increasing R)

Plot with R as parameter

3) For 2nd order systems, like our LRC ckt

$\omega_n = \sqrt{1/LC}$; $\zeta = \frac{1}{2} \frac{R}{\sqrt{LC}}$

omega rad/s = 2pi * Hz

Greek letter Xi

4) Homog. Resp. : Ex $V(s) = 1V$; $i(t) = 0A$

time waveform: $e^{\lambda t} = e^{-s*omega*t} \text{Re}\{e^{j\omega_n \sqrt{1-\zeta^2} t}\}$

$$= e^{-s*omega*t} \cos(\omega_n \sqrt{1-\zeta^2} t)$$

$T = \frac{2\pi}{\omega_n \sqrt{1-\zeta^2}}$

5) Frequency Domain: single frequency ω

- use phasor analysis
- can think of ω as a parameter

Simplified Parameters ω_n, ζ

$$\frac{V_o}{V_{in}} = H(j\omega) = \frac{\omega_n^2}{(j\omega)^2 + (j\omega)2\zeta\omega_n + \omega_n^2}$$

* Construct Bode Plot

1) Low frequencies: $\omega < \omega_n$
 $H(j\omega) \approx 1$

2) High frequencies: $\omega >> \omega_n$
 $H(j\omega) \approx -\omega_n^2/\omega^2$

6) $H(j\omega)$ Bode Plot

$Q = \frac{1}{2\zeta}$

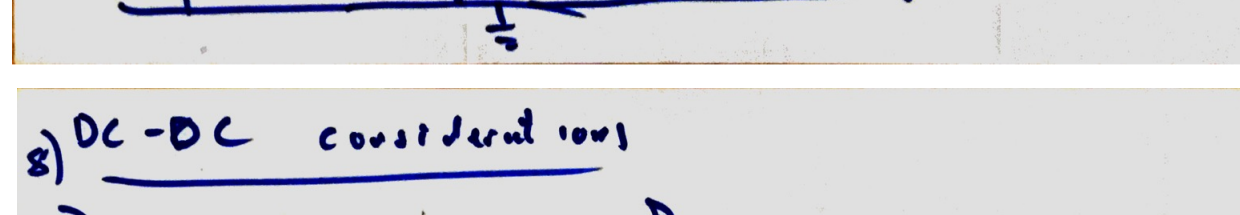
$\zeta = 0.1$

7) *Middle freqs*:
Try $\omega = \omega_n \Rightarrow H(j\omega)|_{\omega=\omega_n} = \frac{\omega_n^2}{j2\zeta\omega_n^2} = -j \frac{1}{2\zeta}$

7) Possible applications (i) radio - "matching" network

$V_{in} = a(t) \cos(\omega_n t)$
slowly varying

(ii) DC-DC converter



8) DC-DC considerations

- 1) NMOS - maybe parallel many NMOS devices to reduce resistance to keep loss low [Low resistance => low loss]
- 2) PMOS - analogous idea - parallel many
- 3) Inductor - try to make series resistance low: R small

9) Look at filter:

$V_o(t)$ analysis - how?

Approx $V_o(t)$ as sine wave raised above 0.

$$= \frac{1}{2} V_{BAT} \sin(\omega t) + \frac{1}{2} V_{BAT}$$

two voltage components into L-C filter:

- use superposition: $V_o(t) = \frac{1}{2} V_{BAT} + V_c(t)$

analysis of LRC at $\omega = \omega_n$

10) How to coordinate ω_s vs ω_n ?

$\omega = 2\pi \cdot 1 \text{ MHz}$

- choice: make $\omega >> \omega_n$

$|H(j\omega)| \ll 1$