

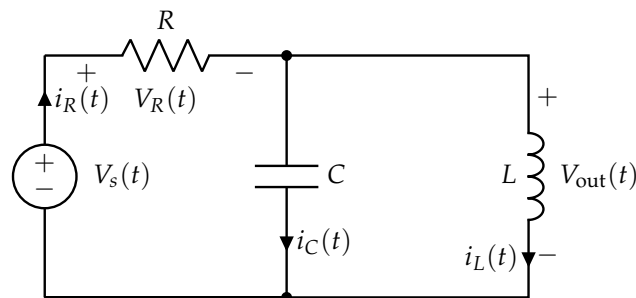
Homework 5

This homework is due on Friday, February 24, 2023, at 11:59PM. Self-grades and HW Resubmissions are due on the following Friday, March 3, 2023, at 11:59PM.

1. Phasor-Domain Circuit Analysis

The analysis techniques you learned previously in 16A for resistive circuits are equally applicable for analyzing circuits driven by sinusoidal inputs in the phasor domain. In this problem, we will walk you through the steps with a concrete example.

Consider the following circuit where the input voltage is sinusoidal. The end goal of our analysis is to find an equation for $V_{\text{out}}(t)$.



The components in this circuit are given by:

$$V_s(t) = 10\sqrt{2} \cos\left(100t - \frac{\pi}{4}\right) \quad (1)$$

$$R = 5 \Omega \quad (2)$$

$$L = 50 \text{ mH} \quad (3)$$

$$C = 2 \text{ mF} \quad (4)$$

- Give the amplitude V_0 , input frequency ω , and phase ϕ of the input voltage V_s .
- Transform the circuit into the phasor domain. What are the impedances of the resistor, capacitor, and inductor? What is the phasor \tilde{V}_S of the input voltage $V_s(t)$?
- Use the circuit equations to solve for \tilde{V}_{out} , the phasor representing the output voltage.

2. Hambley P6.55

Consider the circuit shown in Figure 1. The input signal is given by

$$v_{\text{in}}(t) = 5 + 5 \cos(2000\pi t) \quad (5)$$

Find an expression for the output $v_{\text{out}}(t)$ in steady-state conditions.

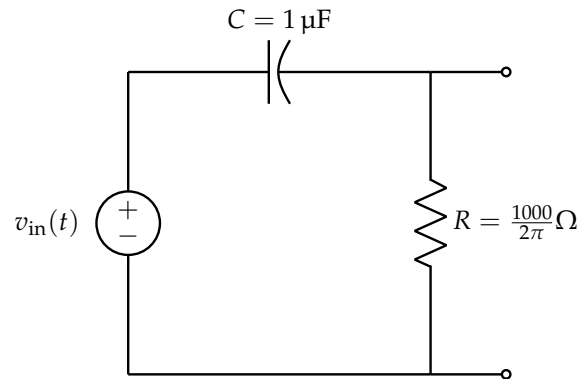


Figure 1: P6.55

(HINT: Use superposition. That is, find $v_{\text{out},1}(t)$ which is the output voltage if the input is $v_{\text{in},1}(t) = 5$, and then find $v_{\text{out},2}(t)$ which is the output voltage if the input is $v_{\text{in},2}(t) = 5 \cos(2000\pi t)$. What is $v_{\text{out}}(t)$ in terms of $v_{\text{out},1}(t)$ and $v_{\text{out},2}(t)$?)

3. Hambley P6.74

Derive an expression for the resonant frequency of the circuit shown in Figure 2. We define the resonant frequency to be the frequency at which the impedance is purely real (no imaginary component).

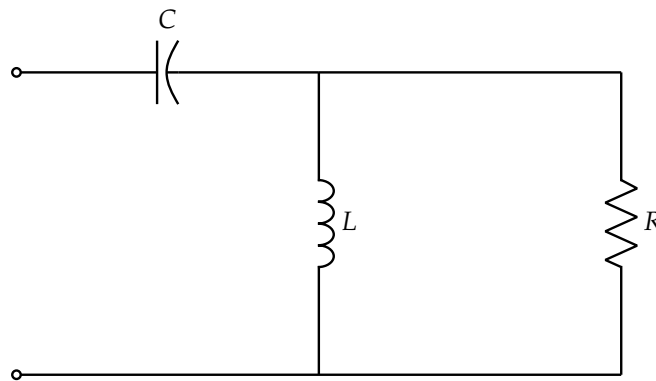


Figure 2: P6.74

4. Phasors and Eigenvalues

Suppose that we have the two-dimensional system of differential equations expressed in matrix/vector form:

$$\frac{d}{dt}\vec{x}(t) = A\vec{x}(t) + \vec{b}u(t) \quad (6)$$

where for this problem, the matrix A and the vector \vec{b} are both real.

- (a) Give a necessary condition on the eigenvalues λ_k of A such that any impact of an initial condition will eventually completely die out. (i.e. the system will reach steady-state.) You don't have to prove this. (*HINT: Read Lecture 9 Slide 6.*)
- (b) Now assume that $u(t)$ has a phasor representation \tilde{U} . In other words, $u(t) = \tilde{U}e^{+j\omega t} + \overline{\tilde{U}}e^{-j\omega t}$. Assume that the vector solution $\vec{x}(t)$ to the system of differential equations (6) can also be written in phasor form as

$$\vec{x}(t) = \tilde{X}e^{+j\omega t} + \overline{\tilde{X}}e^{-j\omega t}. \quad (7)$$

Derive an expression for \tilde{X} involving $A, \vec{b}, j\omega, \tilde{U}$, and the identity matrix I . (Here, we assume that $j\omega$, and $-j\omega$ are not eigenvalues of A , which indicates that $\det(j\omega I - A)$ and $\det(-j\omega I - A)$ are non-zero.)

(*HINT: Plug eq. (7) into eq. (6) and simplify, using the rules of differentiation and grouping terms by which exponential $e^{\pm j\omega t}$ they multiply.*)

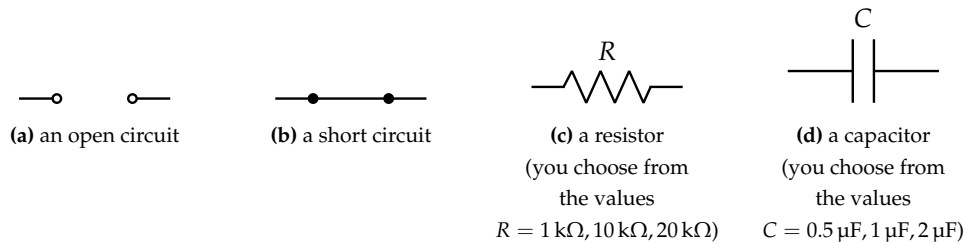
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5. Circuit Design

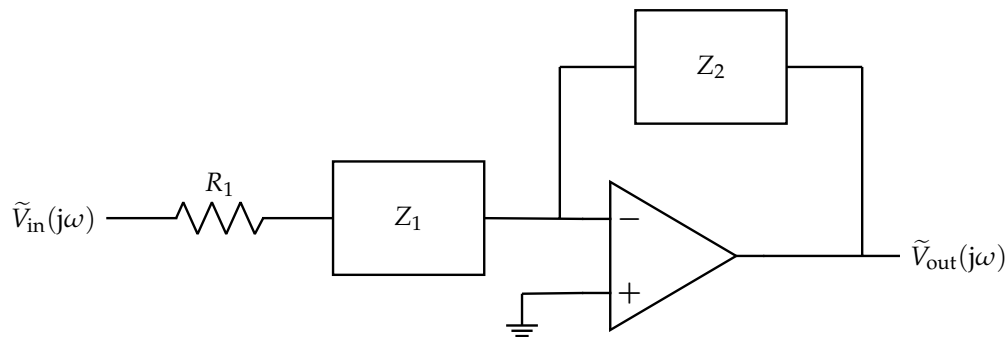
In this problem, you will find a circuit where several components have been left *blank* for you to fill in.

Assume that the op-amp is *ideal*. A special note on op amps in frequency domain analysis: The op-amps you learned about in 16A can be used in exactly the same way for setting up differential equations and even Phasor analysis in 16B. Treat them as ideal op-amps and invoke the Golden Rules.

You have at your disposal *only one of each* of the following components (not including R_1):



Consider the circuit below. The labeled voltages $\tilde{V}_{in}(j\omega)$ and $\tilde{V}_{out}(j\omega)$ are the phasor representations of $v_{in}(t)$ and $v_{out}(t)$ respectively, where $v_{in}(t)$ has the form $v_{in}(t) = v_0 \cos(\omega t + \phi)$. The transfer function $H(j\omega)$ is defined as $H(j\omega) = \frac{\tilde{V}_{out}(j\omega)}{\tilde{V}_{in}(j\omega)}$.



- (a) Let $Z_1(j\omega)$ and $Z_2(j\omega)$ are the impedances of the boxes shown in the circuit diagram. **Write the expression of the transfer function $H(j\omega)$.**
- (b) Let R_1 be $1 \text{ k}\Omega$. We have to find Z_1 and Z_2 , such that the circuit's transfer function $H(j\omega)$ has the following properties:
- $|H(j0)| = 0$.
 - $|H(j\infty)| = 10$.
 - $|H(j10^3)| = \sqrt{50}$.

Using the fact that the circuit is a high pass filter, infer the components (we will find values later) of Z_1 and Z_2 . Write the transfer function $H(j\omega)$ using these components.

(HINT: Try method of elimination: figure out what Z_2 cannot be. Once you find what Z_2 is, what does Z_1 have to be for the circuit to be a filter?)

- (c) **Now use the facts that $|H(j\infty)| = 10$ and $R_1 = 1 \text{ k}\Omega$ to find the component value of Z_2 .**

(d) **Finally use the fact that $|H(j10^3)| = \sqrt{50}$ and the values of R_1 and Z_2 to find the component value of Z_1 .**

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