EE 16B Midterm 1, February 15, 2017

| Name: |
|---------------------------------------------------------------------------------|
| SID #: |
| Discussion Section and TA: Discussion Section and TA: Lab Section and TA: |
| Name of left neighbor: |

Name of right neighbor:_____

Important Instructions:

Show your work. An answer without explanation is not acceptable and does not guarantee any credit.

Only the front pages will be scanned and graded. Back pages won't be scanned; you can use them as scratch paper.

Do not remove pages, as this disrupts the scanning. Instead, cross the parts that you don't want us to grade.

| PROBLEM | MAX | |
|---------|-----|--|
| 1 | 20 | |
| 2 | 20 | |
| 3 | 15 | |
| 4 | 30 | |
| 5 | 15 | |

"Well, Diotallevi and I are planning a reform in higher education. A School of Comparative Irrelevance, where useless or impossible courses are given. The school's aim is to turn out scholars capable of endlessly increasing the number of unnecessary subjects."

– Umberto Eco, Foucault's Pendulum

Problem 1 Warm up (20 points)

a) Consider the following circuit. Z_{eq} is the impedance looking into the circuit from the left, as shown. Provide an expression for Z_{eq} .



 $Z_{eq} =$

ω=

c) What logic function does the following circuit perform?



d) Consider the following four circuits. For each, we define the voltage transfer function, $H_v(\omega) = V_{out}/V_{in}$. With respect to $H_v(\omega)$, circle what class of frequency response each circuit performs.



"You can tell you've found a really interesting question when nobody wants you to answer it." — James S.A. Corey, *Nemesis Games*

Problem 2 (20 points)

Consider the circuit below. Assume an ideal op amp.



a) Find an expression that relates the derivative of v_{out} (d_{vout}/dt) to the input voltage (v_{in}) and/or its derivative (d_{vin}/dt).

| dv_{out} _ | | |
|--------------|--|--|
| dt – | | |

b) Now given that $C_s = 1 \text{ nF}$, $C_f = 5 \text{ nF}$, $C_p = 1 \text{ nF}$, $v_{Cf}(t<0) = v_{Cs}(t<0) = 0$ and $v_{in}(t\ge0) = 5*t$ [volts], provide an expression for $v_{out}(t)$ for $t\ge0$.

Consider now the different circuit below. Assume an ideal op amp.



c) Provide a symbolic expression for $v_{out}(t)$ for t≥0.

v_{out}(t)=

d) Assume $v_{in}(t \ge 0) = 5^{t} [volts]$ and $v_{Cf}(t < 0) = 0$. What is the value of $v_{out}(t)$ at t=1 s?

 $v_{out}(t)=$

Problem 3 (15 points)

The following circuit is part of a near field communication system. A realistic voltage source (Vs, Rs) is connected through a switch onto a three component circuit. The inductor represents an antenna; the voltage across it modulates how much energy is radiated away from the system. The switch alternates continuously between position A and position B; it has been doing this since $t = -\infty$. It spends π microseconds at each position.



We want the voltage on the inductor, V_L , to follow the curve plotted below. Specifically, we want to fulfill the following condition.

Condition: The inductor voltage should oscillate 5 times during period when the switch is in position A.



Plot of V_L as a function of time with switch positions labeled. Note the units of time (10⁻⁶ seconds)!

a) If $R \rightarrow \infty$ and L is non-zero and known, provide an expression for C such that the above condition is met. (Reminder: the condition is that the inductor voltage should oscillate 5 times during period when the switch is in position A.)

C =

b) Unfortunately, a colleague tells you that $R \neq \infty$; if L and C are known, provide an expression for R such that the above condition is met. (Reminder: the condition is that the inductor voltage should oscillate 5 times during period when the switch is in position A.)

R =

"Chang Tzu tells us of a persevering man who after three laborious years mastered the art of dragonslaying. For the rest of his days, he had not a single opportunity to test his skills." — Jorge Luis Borges, *The Book of Imaginary Beings*

Problem 4 (30 points)

Consider the circuit below.



a) What is i(0)?

Hint. What is the current flowing through L1 before the switch opens? Consequently, what is the current flowing through L2?

b) What is di/dt (0)?

c) What is the relationship between the voltages across L1 and R1?

d) Use KCL on Node A and the relationship derived above to arrive at a differential equation of the form,

$$\frac{\mathrm{d}^2 i}{\mathrm{d}t^2}(t) + a_1 \frac{\mathrm{d}i}{\mathrm{d}t}(t) + a_0 i(t) = 0$$

where i(t) is the current going through L2.

e) Let R1 = R2 = R and L1 = L2 = L. Recall that the above differential equation can be reshaped into the follow linear algebra problem:



What is the A matrix and what are its eigenvalues?

f) Will this circuit exhibit any oscillations?

"I am Groot." - Groot, *Guardians of the Galaxy*

Problem 5 (15 points)

Consider the circuit below.



a) Given an input voltage, $v_1(t)$, which is a sinusoid at frequency ω , and phasors corresponding to the input and output voltages, V_1 and V_2 , find an expression for V_2/V_1 .



b) If $v_1(t) = cos(\omega t)$ where $\omega = 10^6$ rad/s and L = 1 μ H, R = 1 Ω , and C = 0.5 μ F, solve for $v_2(t)$.

$v_2(t) =$