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EE 16B Midterm 1
Spring 2018

Name:____

SID #:____

(after the exam begins add your SID# in the top right corner of each page)

Discussion Section and TA:______________________________

Discussion Section and TA:______________________________

Lab Section and TA:_____________________________________

Name of left neighbor:__________________________________

Name of right neighbor:_________________________________

Instructions:

Show your work. An answer without explanation is not acceptable and does not guarantee any credit.

Only the front pages will be scanned and graded. Back pages won't be scanned; you can use them as scratch paper.

Do not remove pages, as this disrupts the scanning. If needed, cross out any parts that you don't want us to grade.

PROBLEM	MAX
1	15
2	25
3	15
4	20
5	25

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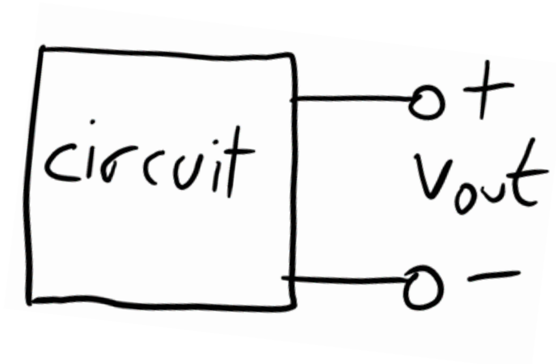
Problem 1 *Warm up* (15 points)

a) Consider the following circuit.



a) For t≥0, the following equation applies to **v_{OUT}(t)**. In addition, **v_{OUT}**(0) = V_0 and $\frac{dv_{OUT}}{dt} = 0$ at t=0.

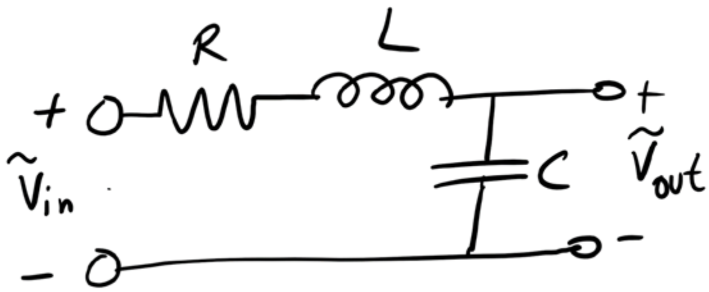
$$\frac{d^2 v_{out}}{dt^2} + \boldsymbol{A}\frac{dv_{out}}{dt} + \boldsymbol{B}v_{out} = 0$$

If $\boldsymbol{A} < 2\sqrt{\boldsymbol{B}}$, provide an expression for $v_{out}(t) \geq 0$**. (5 points)**

Solution:

$v_{out}(t) =$

b) Consider the circuit below.



What is $\tilde{\mathbf{H}}_{\text{out}}(\omega) = \frac{\tilde{\mathbf{V}}_{\text{out}}(\omega)}{\tilde{\mathbf{V}}_{\text{in}}(\omega)}$ for $\omega \rightarrow \infty$? **(5 points)**

Solution:
$\tilde{\mathbf{H}}_{\text{out}}(\omega \rightarrow \infty) =$

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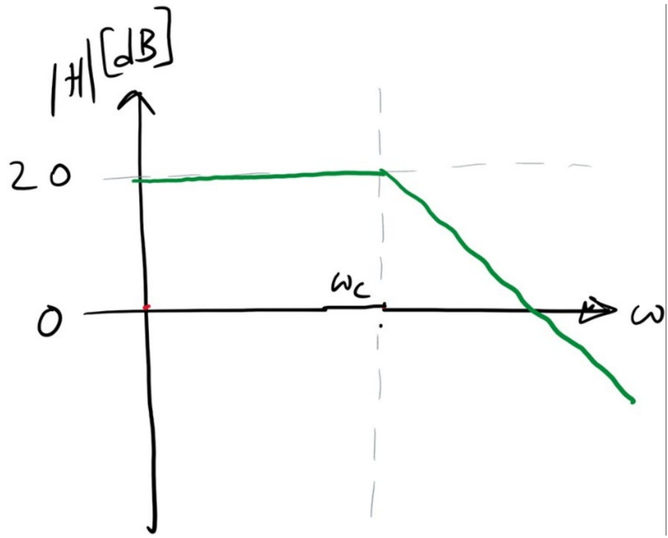
c) Consider the Bode plot below. **(5 points)**



This is a Bode magnitude plot of the transfer function $\widetilde{\mathbf{H}}(\omega)$. The expression for $\widetilde{\mathbf{H}}(\omega)$ is shown below.

$$\widetilde{\mathbf{H}}(\omega) = \frac{\widetilde{\mathbf{H}}_{x}(\omega)}{1 + j\left(\frac{\omega}{\omega_c}\right)}$$

What is $\widetilde{\mathbf{H}}_{x}(\omega)$?

Solution:

$\widetilde{\mathbf{H}}_{x}(\omega) =$

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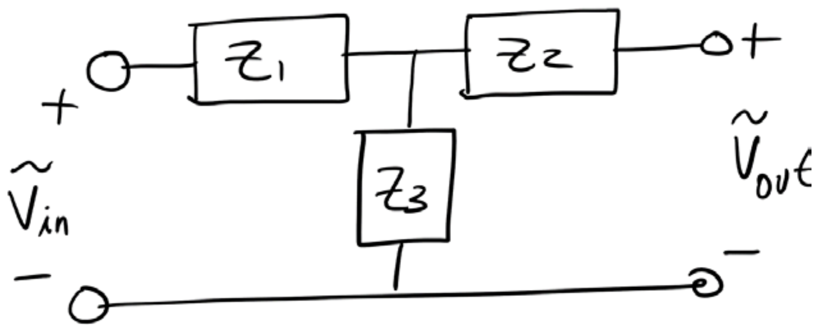
Problem 2 *H's and Bodes…* (25 points)

Consider the circuit below. There is nothing connected to the $\mathbf{V_{out}}$ terminal. **(5 points)**



a) Provide an expression for $\widetilde{\mathbf{H}}_{\mathrm{out}}(\omega) = \dfrac{\widetilde{\mathbf{V}}_{\mathrm{out}}(\omega)}{\widetilde{\mathbf{V}}_{\mathrm{in}}(\omega)}$

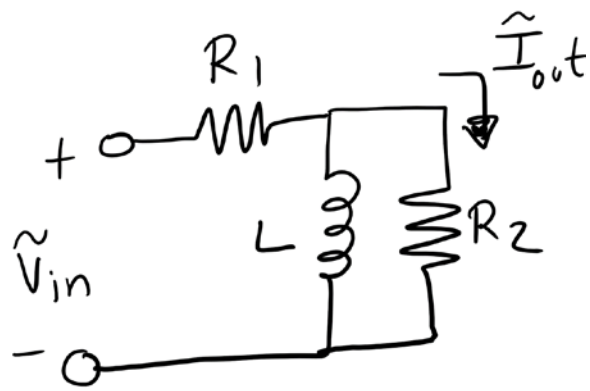
Solution:

$\widetilde{\mathbf{H}}_{\mathrm{out}}(\omega) =$

b) For this part of the problem, assume you have ONE capacitor, ONE inductor and ONE resistor. If $\mathbf{Z_2 = 0}$ for all ω, which components would you choose for $\mathbf{Z_1}$ and $\mathbf{Z_3}$ such that the filter response is a passive low pass filter with a slope of -20 dB/decade for frequencies beyond a single cutoff frequency? **(5 points)**

Solution:
Circle ONE component to go into the Z_1 box: **Capacitor** **Inductor** **Resistor** Circle ONE component to go into the Z_3 box: **Capacitor** **Inductor** **Resistor**

c) Consider the following circuit:



We define a transfer function $\widetilde{\mathbf{H}}_{\text{out}}(\omega) = \frac{\tilde{\mathbf{I}}_{\text{out}}(\omega)}{\widetilde{\mathbf{V}}_{\text{in}}(\omega)}$.

<u>LOOK AT THE DEFINITION OF THE TRANSFER FUNCTION CAREFULLY.</u>

Provide an expression in *<u>canonical form</u>* for $\widetilde{\mathbf{H}}_{\text{out}}(\omega)$. **(10 points)**

Solution:

$\widetilde{\mathbf{H}}_{\text{out}}(\omega) =$

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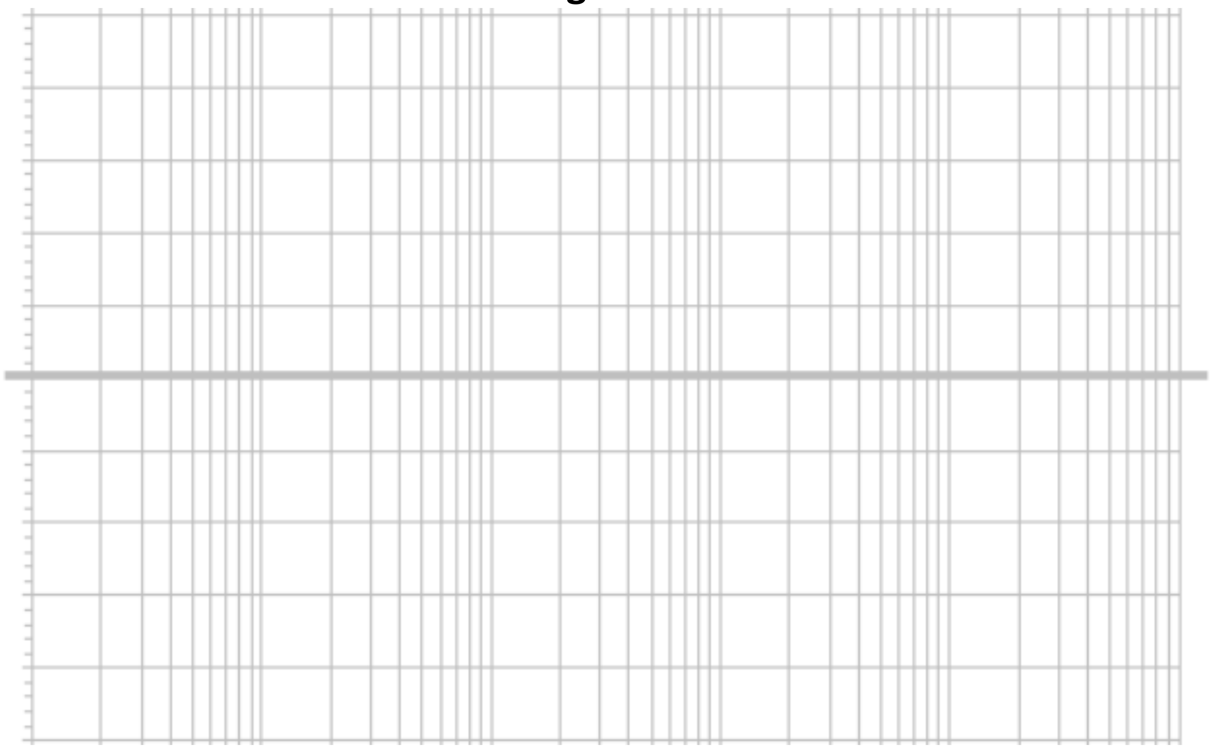
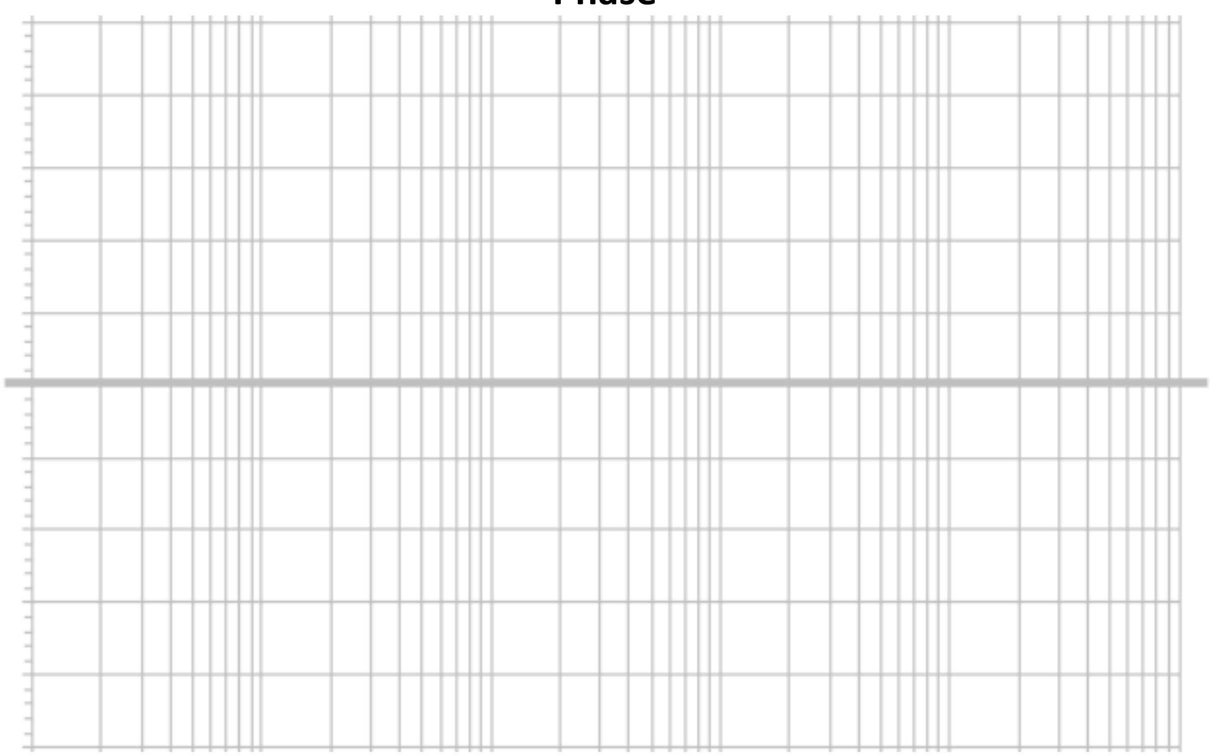
d) If L = 1 H and $R_1 = R_2 = 1\ \Omega$, provide below magnitude and phase Bode plots for $\widetilde{\mathbf{H}}_{\mathrm{out}}(\omega)$. **(5 points)**

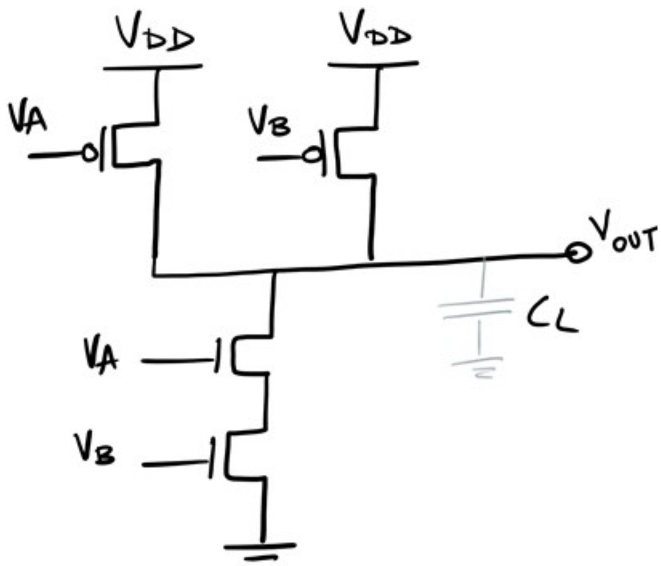
Magnitude



Phase



Problem 3 *Transistors and RC's* (15 points)
Consider the circuit below.



a) Fill in the truth table below for the circuit above. **V_A**, **V_B** and **V_{out}** are digital voltages that can only assume values of 0 or V_{DD}. **(5 points)**

V_A	V_B	V_{out}
0	0	
0	VDD	
VDD	0	
VDD	VDD	

For this part, assume that $\mathbf{V_A} = \mathbf{V_B} = V_{DD}$ for $-\infty < t < 0$. At t=0, $\mathbf{V_A}$ and $\mathbf{V_B}$ switch instantly from V_{DD} to 0. Assume all transistors behave as resistors with the same value, R, if in the ON state and that all capacitances are already accounted for in the circuit above.

b) Provide an expression for $\mathbf{V_{out}}(t \geq 0)$. **(10 points)**

Solution:

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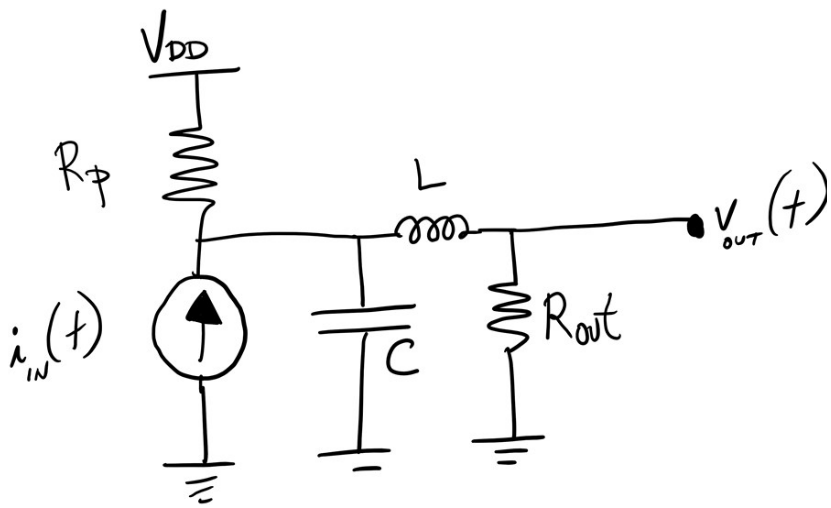
Problem 4 *Phasors!* **(20 points)**

Consider the circuit below.



We are going to solve this circuit, which contains both a sinusoidal *and* a DC source using *superposition* and *phasors*.

a) Solve for $v_{OUT}(t)$ if $i_{IN}(t) = 0$ and V_{DD} = a non-zero constant. **(5 points)**

Solution:

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b) Solve for $v_{OUT}(t)$ if $i_{IN}(t) = I_0\cos(\omega t)$ and $V_{DD} = 0$ V. **(10 points)**

Solution:

c) Solve for $v_{OUT}(t)$ if $i_{IN}(t) = I_0\cos(\omega t)$ and V_{DD} = a non-zero constant. **(5 points)**

Solution:

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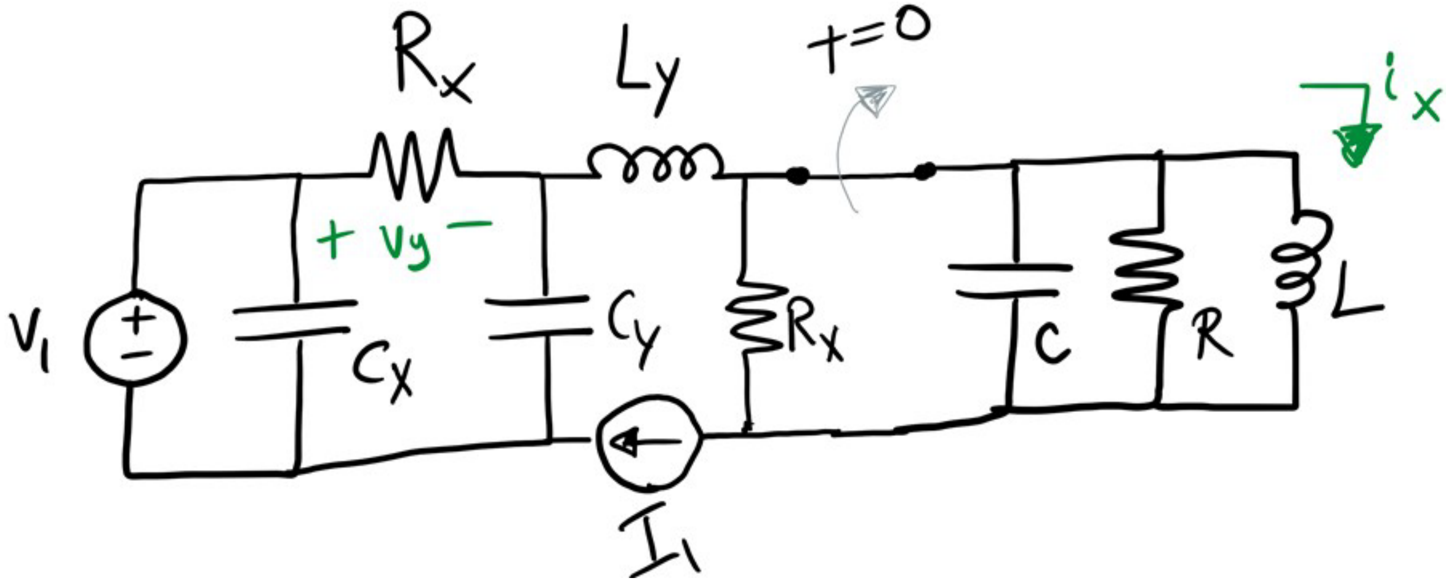
Problem 5 (25 points)

a) Consider the following circuit. The switch is closed for t<0, then opens at t=0. Both of the independent sources have a DC value (i.e. they do not change with time).



a) What is $i_x(t<0)$? **(5 points)**

Solution:

b) What is $v_Y(t<0)$? **(5 points)**

Solution:

c) Provide an equation in the variable $\mathbf{i_x(t)}$ that, when solved, would provide an expression for $\mathbf{i_x(t)}$ for $t \geq 0$.
DO NOT SOLVE THE EQUATION. **(10 points)**

Solution:

d) If Cx = Cy = C = 1 F and Rx = R = 1 Ω and Ly = L = 1 H, provide an expression for $\mathbf{i_x(t)}$ for $t \geq 0$. **(5 points)**

Solution:
$\mathbf{i_x(t)} =$

Extra Space

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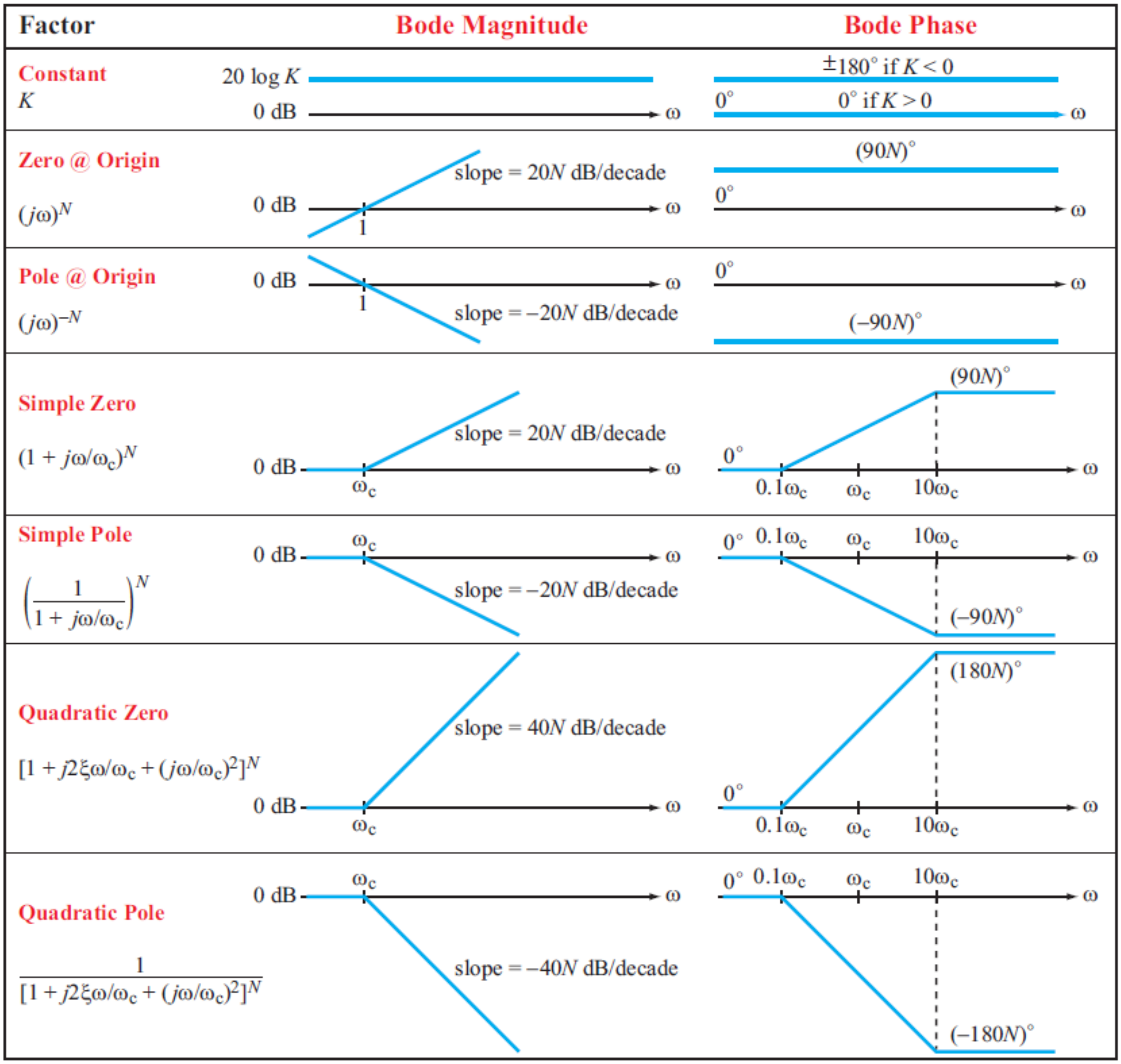
Factor
Bode Magnitude
Bode Phase
Constant
K
20 log K
0 dB
±180° if K < 0
0° if K > 0
Zero @ Origin
(jω)^N
slope = 20N dB/decade
(90N)°
Pole @ Origin
(jω)^−N
slope = −20N dB/decade
(−90N)°
Simple Zero
(1 + jω/ω_c)^N
slope = 20N dB/decade
(90N)°
0.1ω_c
ω_c
10ω_c
Simple Pole
(1/(1 + jω/ω_c))^N
slope = −20N dB/decade
(−90N)°
Quadratic Zero
[1 + j2ξω/ω_c + (jω/ω_c)^2]^N
slope = 40N dB/decade
(180N)°
Quadratic Pole
1/[1 + j2ξω/ω_c + (jω/ω_c)^2]^N
slope = −40N dB/decade
(−180N)°

Contributors:

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