

EE247

Recommended Publications

Filters

Continuous-Time Filters

1. Y. Tsvividis, M. Banu, and J. Khoury, "Continuous-Time MOSFET-C Filters in VLSI", *IEEE Journal of Solid State Circuits* Vol. SC-21, No.1 Feb. 1986, pp. 15-30 and *IEEE Transactions on Circuits and Systems*, Vol. CAS-33, No. 2, Feb. 1986, pp. 125-140.
2. Z. Czarnul, "Modification of the Banu-Tsvividis Continuous-Time Integrator Structure," *IEEE Transactions on Circuits and Systems*, Vol. CAS-33, No. 7, pp. 714-716, July 1986.
3. U-K Moon, and B-S Song, "Design of a Low-Distortion 22-kHz Fifth Order Bessel Filter," *IEEE Journal of Solid State Circuits*, Vol. 28, No. 12, pp. 1254-1264, Dec. 1993.
4. H. Khorramabadi and P.R. Gray, "High Frequency CMOS continuous-time filters," *IEEE Journal of Solid-State Circuits*, Vol.-SC-19, No. 6, pp.939-948, Dec. 1984.
5. K.S. Tan and P.R. Gray, "Fully integrated analog filters using bipolar FET technology," *IEEE, J. Solid-State Circuits*, vol. SC-13, no.6, pp. 814-821, December 1978.
6. V. Gopinathan, Y. Tsvividis, K-S Tan, R. Hester, "Design Considerations for High-Frequency Continuous-Time Filters and Implementation of an Antialiasing Filter for Digital Video," *IEEE Journal of Solid State Circuits*, Vol. SC-25, no. 6 pp. 1368-1378, Dec. 1990.
7. J. M. Khoury, "Design of a 15-MHz CMOS continuous-time filter with on-chip tuning," *IEEE Journal of Solid-State Circuits*, vol. 26, pp. 1988 - 1997, December 1991.
8. Durham, J. Hughes, and W. Redman-White, "Circuit Architectures for High Linearity Monolithic Continuous-Time Filtering," *IEEE Transactions on Circuits and Systems*, pp. 651-657, Sept. 1992.
9. Laber and Gray, "A 20MHz 6th Order BiCOM Parasitic Insensitive Continuous-time Filter and Second Order Equalizer Optimized for Disk Drive Read Channels," *IEEE Journal of Solid State Circuits*, Vol. 28, pp. 462-470, April 1993.
10. H. Khorramabadi, M. Tarsia and N. Woo, "Baseband Filters for IS-95 CDMA Receiver Applications Featuring Digital Automatic Frequency Tuning," *1996 International Solid State Circuits Conference*, pp. 172-173.
11. R. Castello, I. Bietti and F. Svelto, "High-Frequency Analog Filters in Deep-Submicron CMOS Technology", *ISSCC Digest of Technical Papers*, Feb. 1999, pp.74-75.
12. Y. Tsvividis, Z. Czarnul and S.C. Fang, "MOS transconductors and integrators with high linearity," *Electronics Letters*, vol. 22, pp. 245-246, Feb. 27, 1986.
13. I. Mehr and D.R. Welland, "A CMOS Continuous-Time Gm-C Filter for PRML Read Channel Applications at 150 Mb/s and Beyond", *IEEE J. of Solid-State Circuits*, Vol.32, No.4, April 1997, pp. 499-513.
14. D.A. Johns, K. Martin "Analog Integrated Circuits" *Wiley, 1997*, p.606
15. R. Alini, A. Baschiroto, and R. Castello, "Tunable BiCMOS Continuous-Time Filter for High-Frequency Applications," *IEEE Journal of Solid State Circuits*, Vol. 27, No. 12, pp. 1905-1915, Dec. 1992.

Switched Capacitor Filters

1. D. Senderowicz et. al, "A Family of Differential NMOS Analog Circuits for PCM Codec Filter Chip," *IEEE Journal of Solid-State Circuits*, Vol.-SC-17, No. 6, pp.1014-1023, Dec. 1982.
2. Tat C. Choi, "High-Frequency CMOS Switched-Capacitor Filters," U. C. Berkeley, Department of Electrical Engineering, Ph.D. Thesis, May 1983 (ERL Memorandum No. UCB/ERL M83/31).
3. B.S. Song, P.R. Gray "Switched-Capacitor High-Q Bandpass Filters for IF Applications," *IEEE Journal of Solid State Circuits*, Vol. 21, No. 6, pp. 924-933, Dec. 1986.
4. R. Gregorian, G. Temes, "Analog CMOS Integrated Circuits," Wiley, 1986, pp 277 .
5. K.Martin, A. Sedra, "Effect of the Opamp Finite Gain & Bandwidth on the Performance of Switched-Capacitor Filters," *IEEE Trans. Circuits Syst.*, vol. CAS28, no. 8, pp. 822-829, Aug 1981.
6. K.L. Lee, "Low Distortion Switched-Capacitor Filters," U. C. Berkeley, Department of Electrical Engineering, Ph.D. Thesis, Feb. 1986 (ERL Memorandum No. UCB/ERL M86/12).
7. K. Martin and A. S. Sedra, "Strays-insensitive switched-capacitor filters based on the bilinear z transform," *Electron. Lett.*, vol. 19, pp. 365-6, June 1979.