### EE247 Administrative

- Homework #1 will be posted on EE247 site and is due <u>Sept. 9<sup>th</sup></u>
- Office hours held @ 201 Cory Hall:
  - Tues. and Thurs.: 4 to 5pm

EECS 247

Lecture 3: Filters

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### EE247 Lecture 3

- Active Filters
  - Active biquads-
  - -How to build higher order filters?
    - · Integrator-based filters
      - Signal flowgraph concept
      - First order integrator-based filter
      - Second order integrator-based filter & biquads
  - High order & high Q filters
    - Cascaded biquads & first order filters
      - Cascaded biquad sensitivity to component mismatch
    - · Ladder type filters

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Lecture 3: Filters

#### **Filters**

#### 2<sup>nd</sup> Order Transfer Functions (Biquads)

• Biquadratic (2<sup>nd</sup> order) transfer function:

$$H(s) = \frac{1}{1 + \frac{s}{\omega_P Q_P} + \frac{s^2}{\omega_P^2}}$$

$$|H(j\omega)| = \frac{1}{\sqrt{\left(1 - \frac{\omega^2}{\omega_P^2}\right)^2 + \left(\frac{\omega}{\omega_P Q_P}\right)^2}} \longrightarrow \begin{cases} |H(j\omega)|_{\omega=0} = 1\\ |H(j\omega)|_{\omega\to\infty} = 0\\ |H(j\omega)|_{\omega=\omega_P} = Q_P \end{cases}$$

Biquad poles @: 
$$s = -\frac{\omega_P}{2Q_P} \left( 1 \pm \sqrt{1 - 4Q_P^2} \right)$$

Note: for  $Q_P \le \frac{1}{2}$  poles are real, complex otherwise

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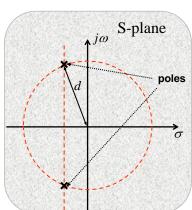
## **Biquad Complex Poles**

 $Q_P > \frac{1}{2} \rightarrow \text{Complex conjugate poles:}$ 

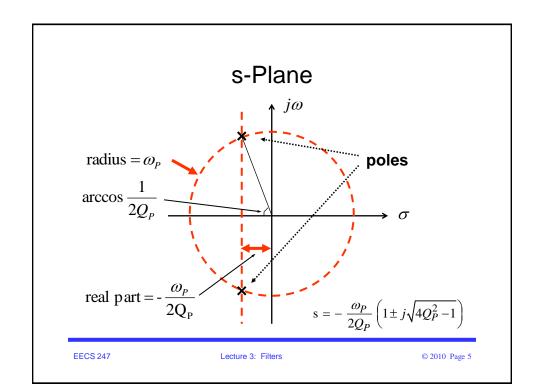
$$s = -\frac{\omega_P}{2Q_P} \left( 1 \pm j\sqrt{4Q_P^2 - 1} \right)$$

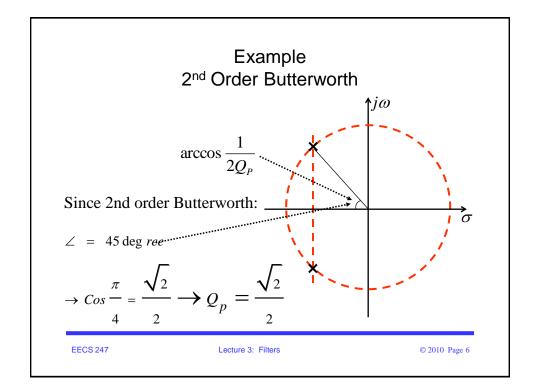
Distance from origin in s-plane:

$$d^{2} = \left(\frac{\omega_{p}}{2Q_{p}}\right)^{2} \left(1 + 4Q_{p}^{2} - 1\right)$$
$$= \omega_{p}^{2}$$



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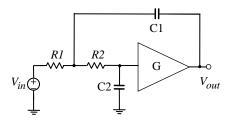


## Implementation of Biquads

- Passive RC: only real poles → can't implement complex conjugate poles
- · Terminated LC
  - Low power, since it is passive
  - Only fundamental noise sources → load and source resistance
  - As previously analyzed, not feasible in the monolithic form for f < 350 MHz
- · Active Biguads
  - Many topologies can be found in filter textbooks!
  - Widely used topologies:
    - Single-opamp biquad: Sallen-Key
    - Multi-opamp biquad: Tow-Thomas
    - · Integrator based biquads

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# Active Biquad Sallen-Key Low-Pass Filter



$$1 + \frac{s}{\omega_P Q_P} + \frac{s^2}{\omega_P^2}$$

$$\omega_P = \frac{1}{\sqrt{R_1 C_1 R_2 C_2}}$$

$$Q_P = \frac{\omega_P}{\frac{1}{\sqrt{R_1 C_1 R_2 C_2}} + \frac{1 - G}{\sqrt{R_1 C_1 R_2 C_2}}$$

- · Single gain element
- Can be implemented both in discrete & monolithic form
- · "Parasitic sensitive"
- · Versions for LPF, HPF, BP, ...
  - → Advantage: Only one opamp used
  - → Disadvantage: Sensitive to parasitic all pole no finite zeros

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## Addition of Imaginary Axis Zeros

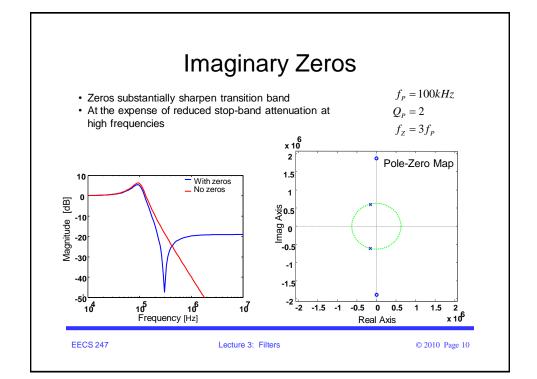
- · Sharpen transition band
- Can "notch out" interference
  - Band-reject filter
- High-pass filter (HPF)

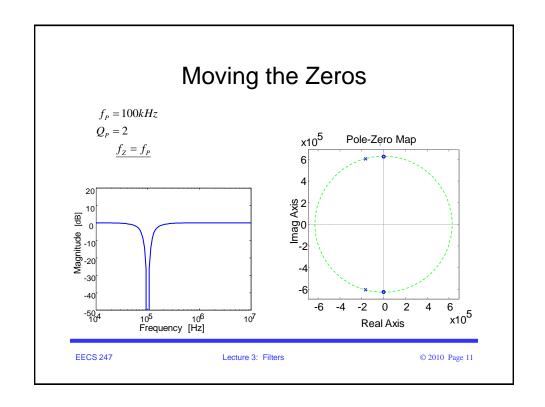
$$H(s) = K \frac{1 + \left(\frac{s}{\omega_Z}\right)^2}{1 + \frac{s}{\omega_P Q_P} + \left(\frac{s}{\omega_P}\right)^2}$$

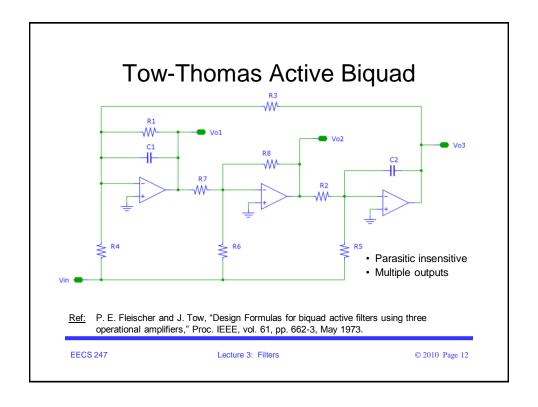
$$|H(j\omega)|_{\omega\to\infty} = K \left(\frac{\omega_P}{\omega_Z}\right)^2$$

Note: Always represent transfer functions as a product of a gain term, poles, and zeros (pairs if complex). Then all coefficients have a physical meaning, and readily identifiable units.

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## Frequency Response

$$\begin{split} \frac{V_{o1}}{V_{in}} &= -k_2 \frac{\left(b_2 a_1 - b_1\right) s + \left(b_2 a_0 - b_0\right)}{s^2 + a_1 s + a_0} \\ \frac{V_{o2}}{V_{in}} &= \frac{b_2 s^2 + b_1 s + b_0}{s^2 + a_1 s + a_0} \\ \frac{V_{o3}}{V_{in}} &= -\frac{1}{k_1 \sqrt{a_0}} \frac{\left(b_0 - b_2 a_0\right) s + \left(a_1 b_0 - a_0 b_1\right)}{s^2 + a_1 s + a_0} \end{split}$$

- V<sub>o2</sub> implements a general biquad section with arbitrary poles and zeros
- $\bullet$   $V_{o1}$  and  $V_{o3}$  realize the same poles but are limited to at most one finite zero
- · Possible to use combination of 3 outputs

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## **Component Values**

$$b_0 = \frac{R_8}{R_3 R_5 R_7 C_1 C_2} \qquad \qquad \text{given } a_i, b_i, k_i, C_1, C_2 \text{ and } R_8$$
 
$$B_1 = \frac{1}{R_1 C_1} \left( \frac{R_8}{R_6} - \frac{R_1 R_8}{R_4 R_7} \right) \qquad \qquad R_2 = \frac{k_1}{\sqrt{a_0} C_2} \qquad \text{it follows that}$$
 
$$b_2 = \frac{R_8}{R_6} \qquad \qquad R_3 = \frac{1}{k_1 k_2} \frac{1}{\sqrt{a_0} C_1} \qquad \qquad \omega_p = \sqrt{\frac{R_8}{R_2 R_3 R_7 C_1 C_2}}$$
 
$$a_0 = \frac{R_8}{R_2 R_3 R_7 C_1 C_2} \qquad \qquad R_4 = \frac{1}{k_2} \frac{1}{a_1 b_2 - b_1} \frac{1}{C_1} \qquad \qquad Q_p = \omega_p R_1 C_1$$
 
$$a_1 = \frac{1}{R_1 C_1} \qquad \qquad R_5 = \frac{k_1 \sqrt{a_0}}{b_0 C_2}$$
 
$$k_1 = \sqrt{\frac{R_2 R_8 C_2}{R_3 R_7 C_1}} \qquad \qquad R_6 = \frac{R_8}{b_2}$$
 
$$k_2 = \frac{R_7}{R_c} \qquad \qquad R_7 = k_2 R_8$$

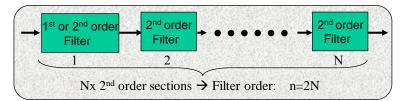
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### Higher-Order Filters in the Integrated Form

One way of building higher-order filters (n>2) is via cascade of 2<sup>nd</sup> order biquads & 1<sup>st</sup> order, e.g. Sallen-Key,or Tow-Thomas, & RC



Cascade of 1st and 2nd order filters:

- Easy to implement
- Highly sensitive to component mismatch -good for low Q filters only
- → For high Q applications good alternative: Integrator-based ladder filters

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## **Integrator Based Filters**

- Main building block for this category of filters
   → Integrator
- By using signal flowgraph techniques
   → Conventional RLC filter topologies can be converted to integrator based type filters
- · How to design integrator based filters?
  - Introduction to signal flowgraph techniques
  - 1st order integrator based filter
  - 2nd order integrator based filter
  - High order and high Q filters

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### What is a Signal Flowgraph (SFG)?

- SFG → Topological network representation consisting of nodes & branches- used to convert one form of network to a more suitable form (e.g. passive RLC filters to integrator based filters)
- Any network described by a set of linear differential equations can be expressed in SFG form
- · For a given network, many different SFGs exists
- Choice of a particular SFG is based on practical considerations such as type of available components

\*Ref: W.Heinlein & W. Holmes, "Active Filters for Integrated Circuits", Prentice Hall, Chap. 8, 1974.

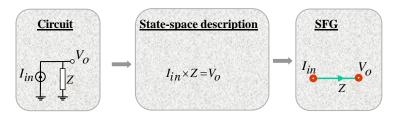
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#### What is a Signal Flowgraph (SFG)?

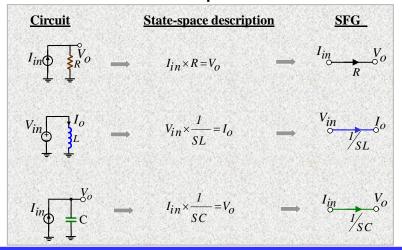
- Signal flowgraph technique consist of nodes & branches:
  - Nodes represent variables (V & I in our case)
  - Branches represent transfer functions (we will call the transfer function branch multiplication factor or BMF)
- To convert a network to its SFG form, KCL & KVL is used to derive state space description
- · Simple example:



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# Signal Flowgraph (SFG) Examples



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### Useful Signal Flowgraph (SFG) Rules

• Two parallel branches can be replaced by a single branch with overall *BMF* equal to sum of two *BMF*s

$$V_{I} \diamond \underbrace{\begin{matrix} b \\ V_{I} \diamond \end{matrix}}_{a.V_{I}+b.V_{I}=V_{2}} V_{2}$$

$$(a+b).V_{I}=V_{2}$$

• A node with only one incoming branch & one outgoing branch can be eliminated & replaced by a single branch with *BMF* equal to the **product** of the two *BMF*s

$$V_{l} \circ \xrightarrow{a} \overset{V_{3}}{\circ} \overset{b}{\circ} \overset{V_{2}}{\circ} \longrightarrow V_{2}$$

$$a.V_{l}=V_{3} \quad (1)$$

$$b.V_{3}=V_{2} \quad (2)$$

$$Substituting for V3 from (1) in (2) \qquad (a.b).V_{l}=V_{2}$$

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#### Useful Signal Flowgraph (SFG) Rules

• An intermediate node can be multiplied by a factor (k). BMFs for incoming branches have to be multiplied by k and outgoing branches divided by k



$$a.V_1 = V_3$$
$$b.V_3 = V_2$$

$$b.V_2=V$$

Multiply both sides of (1) by k (a.k).  $V_1 = k \cdot V_3$ 

Divide & multiply left side of (2) by k

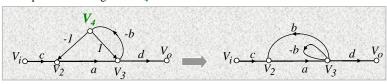
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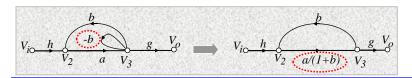
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### Useful Signal Flowgraph (SFG) Rules

- · Simplifications can often be achieved by shifting or eliminating nodes
- Example: eliminating node  $V_4$



• A self-loop branch with BMF y can be eliminated by multiplying the BMFof incoming branches by 1/(1-y)

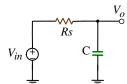


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# Integrator Based Filters 1st Order LPF

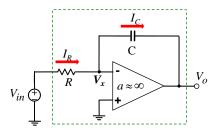
 Conversion of simple lowpass RC filter to integratorbased type by using signal flowgraph techniques



$$\frac{V_O}{V_{in}} = \frac{I}{I + s \, R \, C}$$

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# What is an Integrator? Example: Single-Ended Opamp-RC Integrator



- Node x: since opamp has high gain  $V_x = -V_o/a \rightarrow 0$
- Node x is at "virtual ground"
  - $\rightarrow$  No voltage swing at  $V_x$  combined with high opamp input impedance
  - → No input opamp current

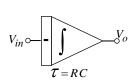
$$\frac{V_{in}}{R} = -V_o s C \quad , \quad V_o = -V_{in} \sqrt{\frac{I}{sRC}} \quad , \quad V_o = -\frac{I}{RC} \int V_{in} dt$$

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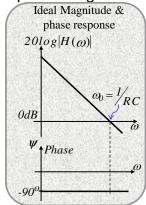
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# What is an Integrator? Example: Single-Ended Opamp-RC Integrator



$$V_o/V_{in} = -\frac{1}{sRC}$$



Note: Practical integrator in CMOS technology has input & output both in the form of voltage and not current → Consideration for SFG derivation

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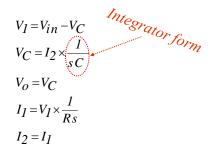
#### 1st Order LPF Convert RC Prototype to Integrator Based Version

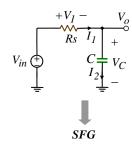
 Start from circuit prototype-Name voltages & currents for <u>all</u> components

$$V_{in} \stackrel{+V_{I}}{\bigoplus} V_{in} V_{in} \stackrel{+V_{I}}{\bigoplus} V_{in} \stackrel{+V_{I}}{\bigoplus} V_{in} \stackrel{+V_{I}}{\bigoplus} V_{in} \stackrel{+V_{I}}{\bigoplus} V_{in} V_{in} V$$

- Use KCL & KVL to derive state space description in such a way to have BMFs in the <u>integrator</u> form:
  - ightarrow Capacitor voltage expressed as function of its current  $V_{Cap.} = f(I_{Cap.})$
  - $\rightarrow$  Inductor current as a function of its voltage  $I_{Ind.} = f(V_{Ind.})$
- Use state space description to draw signal flowgraph (SFG) (see next page)

#### Integrator Based Filters First Order LPF





- $V_{in}$  I  $V_{I}$  -1  $V_{C}$  I  $V_{C}$  I  $V_{C}$
- · All voltages & currents → nodes of SFG
- Voltage nodes on top, corresponding current nodes below each voltage node

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#### Normalize

- Since integrators are the main building blocks → require in & out signals in the form of voltage (not current)
  - → Convert all currents to voltages by multiplying current nodes by a scaling resistance R<sup>\*</sup>
  - → Corresponding *BMF*s should then be scaled accordingly

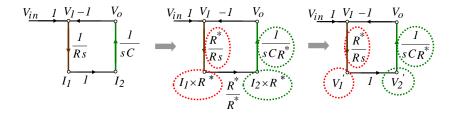
$$V_{I} = V_{in} - V_{o}$$

$$V_{I} = V_{I} - V$$

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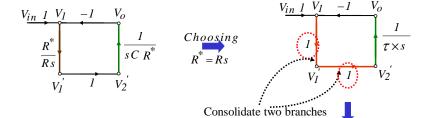
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### 1<sup>st</sup> Order Lowpass Filter SGF Normalize



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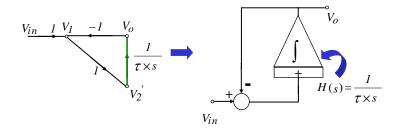
## 1<sup>st</sup> Order Lowpass Filter SGF Synthesis



$$R^* = Rs$$
 ,  $\tau = R^* \times C$ 

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# First Order Integrator Based Filter



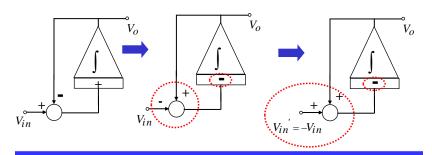
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## 1<sup>st</sup> Order Filter Built with Opamp-RC Integrator

- Single-ended Opamp-RC integrator has a sign inversion from input to output
  - → Convert SFG accordingly by modifying BMF

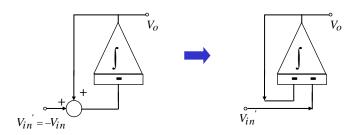


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## 1<sup>st</sup> Order Filter Built with Opamp-RC Integrator

• To avoid requiring an additional opamp to perform summation at the input node:

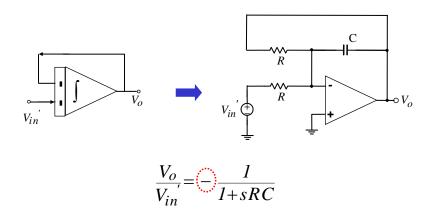


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## 1<sup>st</sup> Order Filter Built with Opamp-RC Integrator (continued)



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#### Opamp-RC 1st Order Filter Noise

Identify noise sources (here it is resistors & opamp) Find transfer function from each noise source to the output (opamp noise next page)

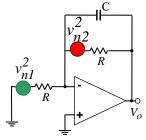
$$\overline{v_o^2} = \sum_{m=1}^k \int_0^\infty |H_m(f)|^2 S_m(f) df$$

 $\overline{v_o^2} = \sum_{m=1}^k \int_0^\infty |H_m(f)|^2 S_m(f) df$   $S_i(f) \to Noise \ spectral \ density \ of i^{th} \ noise \ source$   $|H_I(f)|^2 = |H_2(f)|^2 = \frac{1}{I + (2\pi fRC)^2}$ 

$$v_{n1}^2 = v_{n2}^2 = 4KTR\Delta f$$

$$\sqrt{\overline{v_o^2}} = \sqrt{2 \frac{k T}{C}}$$

$$\alpha = 2 \int_{\mathbf{Q}}^{\mathbf{Q}} \mathbf{Q}$$



Typically, α increases as filter order increases

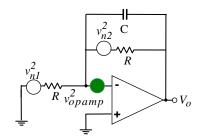
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## Opamp-RC Filter Noise **Opamp Contribution**

- So far only the fundamental noise sources are considered
- In reality, noise associated with the opamp increases the overall noise
- For a well-designed filter opamp is designed such that noise contribution of opamp is negligible compared to other noise sources
- The bandwidth of the opamp affects the opamp noise contribution to the total noise

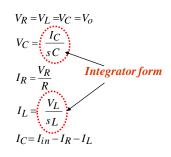


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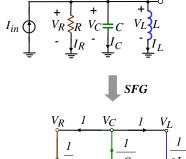
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# Integrator Based Filter 2<sup>nd</sup> Order RLC Filter

•State space description:



• Draw signal flowgraph (SFG)



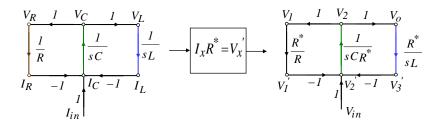
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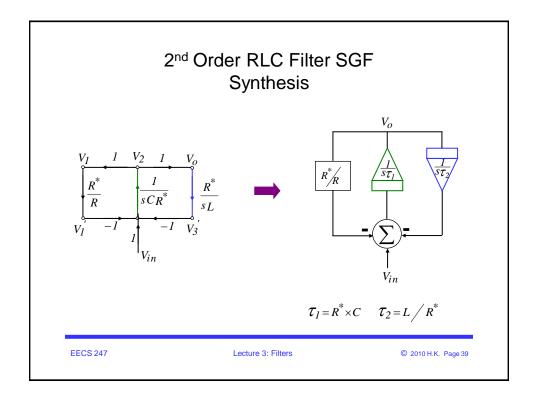
### 2<sup>nd</sup> Order RLC Filter SGF Normalize

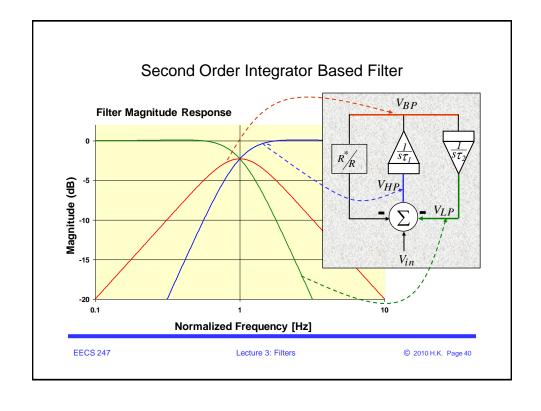
- Convert currents to voltages by multiplying all current nodes by the scaling resistance  $R^*$ 



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#### Second Order Integrator Based Filter

$$\frac{V_{BP}}{V_{in}} = \frac{\tau_{2}s}{\tau_{1}\tau_{2}s^{2} + \beta\tau_{2}s + 1}$$

$$\frac{V_{LP}}{V_{in}} = \frac{1}{\tau_{1}\tau_{2}s^{2} + \beta\tau_{2}s + 1}$$

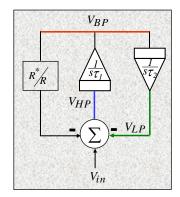
$$\frac{V_{HP}}{V_{in}} = \frac{\tau_{1}\tau_{2}s^{2}}{\tau_{1}\tau_{2}s^{2} + \beta\tau_{2}s + 1}$$

$$\tau_{1} = R^{*} \times C \quad \tau_{2} = L / R^{*}$$

$$\beta = R^{*} / R$$

$$\omega_{0} = 1 / \sqrt{\tau_{1}\tau_{2}} = 1 / \sqrt{LC}$$

$$Q = 1/\beta \times \sqrt{\tau_{1} / \tau_{2}}$$



From matching point of view desirable:

$$\tau_1 = \tau_2 \rightarrow Q = \frac{R}{R^*}$$

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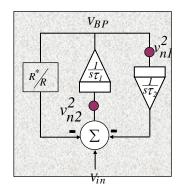
### Second Order Bandpass Filter Noise

$$\overline{v_o^2} = \sum_{m=1}^k \int_0^\infty |H_m(f)|^2 S_m(f) df$$

- Find transfer function of each noise source to the output
- Integrate contribution of all noise sources
- Here it is assumed that opamps are noise free (not usually the case!)

$$v_{n1}^{2} = v_{n2}^{2} = 4KTRdf$$

$$\sqrt{v_{o}^{2}} = \sqrt{2Q \frac{kT}{C}}$$



Typically,  $\alpha$  increases as filter order increases Note the noise power is directly proportion to Q

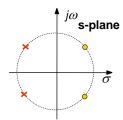
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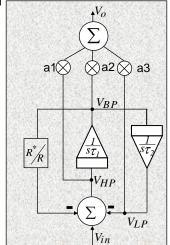
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# Second Order Integrator Based Filter Biquad

• By combining outputs can generate general biquad function:

$$\frac{V_0}{V_{in}} = \frac{a_1 \tau_1 \tau_2 s^2 + a_2 \tau_2 s + a_3}{\tau_1 \tau_2 s^2 + \beta \tau_2 s + I}$$





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### Summary Integrator Based Monolithic Filters

- Signal flowgraph techniques utilized to convert RLC networks to integrator based active filters
- Each reactive element (L& C) replaced by an integrator
- Fundamental noise limitation determined by integrating capacitor value:

- For lowpass filter: 
$$\sqrt{v_o^2} = \sqrt{\alpha \frac{kT}{C}}$$

- Bandpass filter: 
$$\sqrt{\overline{v_o^2}} = \sqrt{\alpha Q \frac{kT}{C}}$$

where  $\alpha$  is a function of filter order and topology

## **Higher Order Filters**

- How do we build higher order filters?
  - Cascade of biquads and 1st order sections
    - Each complex conjugate pole built with a biquad and real pole with 1st order section
    - · Easy to implement
    - In the case of high order high Q filters  $\rightarrow$  highly sensitive to component mismatch
  - Direct conversion of high order ladder type RLC filters
    - SFG techniques used to perform exact conversion of ladder type filters to integrator based filters
    - More complicated conversion process
    - Much less sensitive to component mismatch compared to cascade of biquads

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#### Higher Order Filters Cascade of Biguads

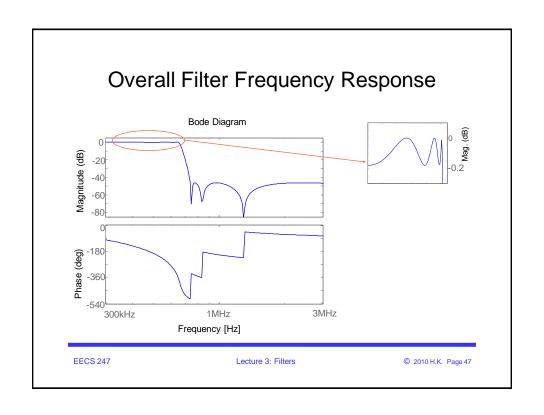
Example: LPF filter for CDMA cell phone baseband receiver

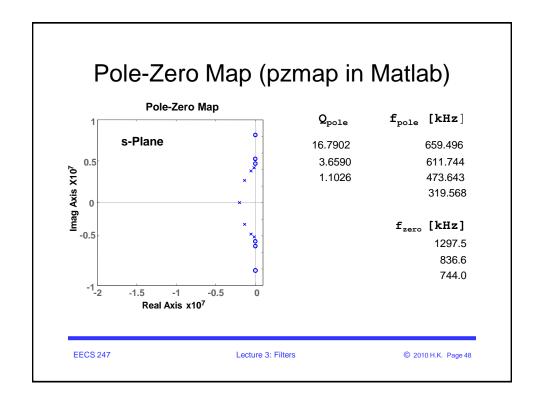
- · LPF with
  - fpass = 650 kHz
     fstop = 750 kHz
     Rpass = 0.2 dB
     Rstop = 45 dB
  - Assumption: Can compensate for phase distortion in the digital domain
- Matlab used to find minimum order required → 7th order Elliptic
- Implementation with cascaded Biquads

Goal: Maximize dynamic range

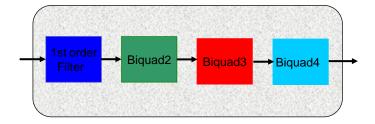
- Pair poles and zeros
- In the cascade chain place lowest Q poles first and progress to higher Q poles moving towards the output node

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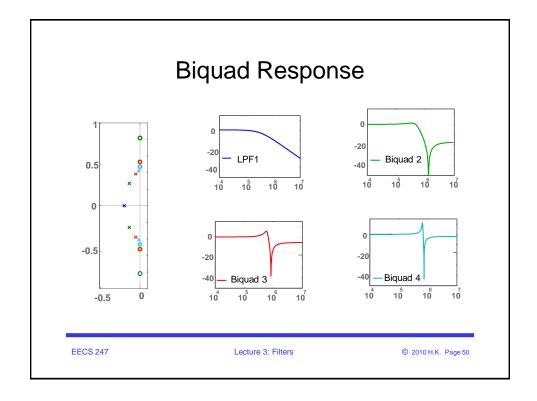
# CDMA Filter Built with Cascade of 1st and 2nd Order Sections

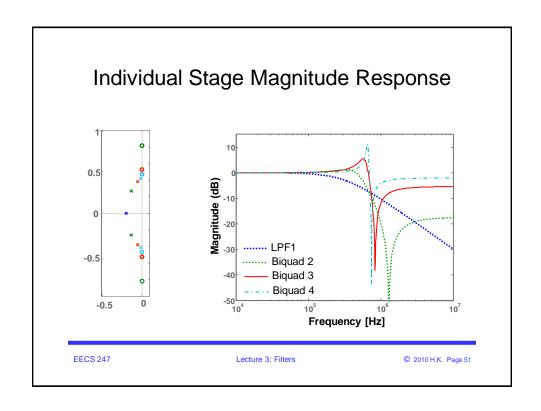


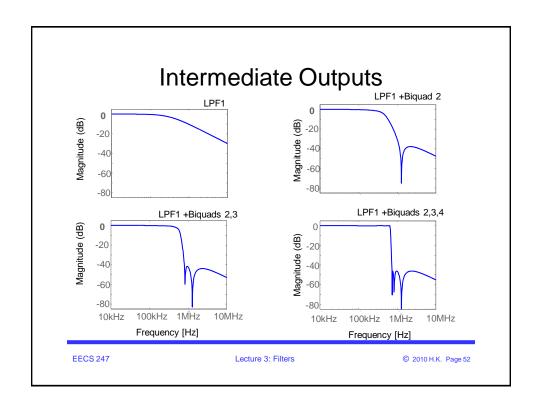
- 1st order filter implements the single real pole
- Each biquad implements a pair of complex conjugate poles and a pair of imaginary axis zeros

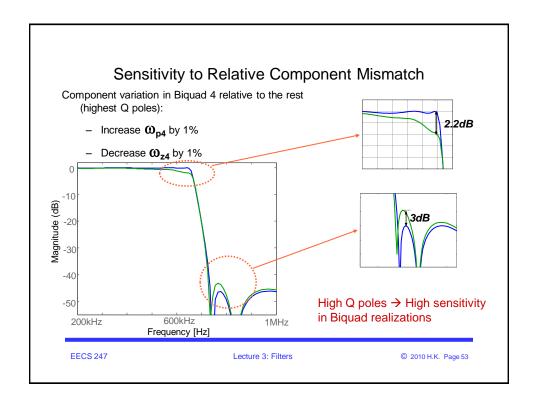
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## High Q & High Order Filters

- Cascade of biquads
  - Highly sensitive to component mismatch → not suitable for implementation of high Q & high order filters
  - Cascade of biquads only used in cases where required Q for all biquads <4 (e.g. filters for disk drives)</li>
- Ladder type filters more appropriate for high Q & high order filters (next topic)
  - Will show later →Less sensitive to component mismatch

## Ladder Type Filters

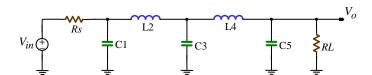
- Active ladder type filters
  - For simplicity, will start with all pole ladder type filters
    - Convert to integrator based form- example shown
  - Then will attend to high order ladder type filters incorporating zeros
    - Implement the same 7th order elliptic filter in the form of ladder RLC with zeros
      - Find level of sensitivity to component mismatch
      - Compare with cascade of biquads
    - · Convert to integrator based form utilizing SFG techniques
  - Effect of integrator non-Idealities on filter frequency characteristics

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# RLC Ladder Filters Example: 5<sup>th</sup> Order Lowpass Filter



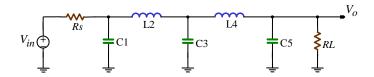
- Made of resistors, inductors, and capacitors
- Doubly terminated or singly terminated (with or w/o  $R_I$ )

Doubly terminated LC ladder filters → Lowest sensitivity to component mismatch

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## LC Ladder Filters



- First step in the design process is to find values for Ls and Cs based on specifications:
  - Filter graphs & tables found in:
    - A. Zverev, Handbook of filter synthesis, Wiley, 1967.
    - A. B. Williams and F. J. Taylor, Electronic filter design, 3<sup>rd</sup> edition, McGraw-Hill. 1995.
  - CAD tools
    - Matlab
    - · Spice

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### LC Ladder Filter Design Example

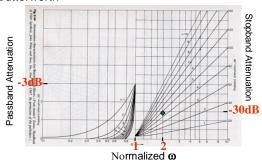
Design a LPF with maximally flat passband:

f-3dB = 10MHz, fstop = 20MHz

Rs >27dB @ fstop

- Maximally flat passband → Butterworth
  - Find minimum filter order
  - Here standard graphs from filter books are used

fstop/f-3dB = 2 Rs >27dB



From: Williams and Taylor, p. 2-37

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