

Lecture 5w: Benefits of Scaling IIILecture 5: Benefits of Scaling IIIAnnouncements:

- HW#1 issued today; due in two weeks
- Hopefully, you've watched the video lectures from last week; otherwise, you'll have a hard time understanding this lecture
- Quiz Thursday? yes / no

Today:

- Reading: Senturia, Chapter 1

Lecture Topics:

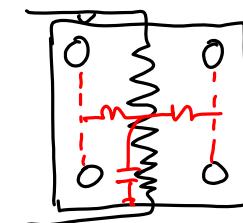
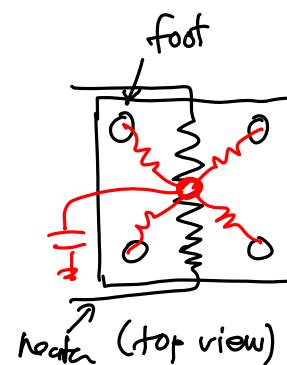
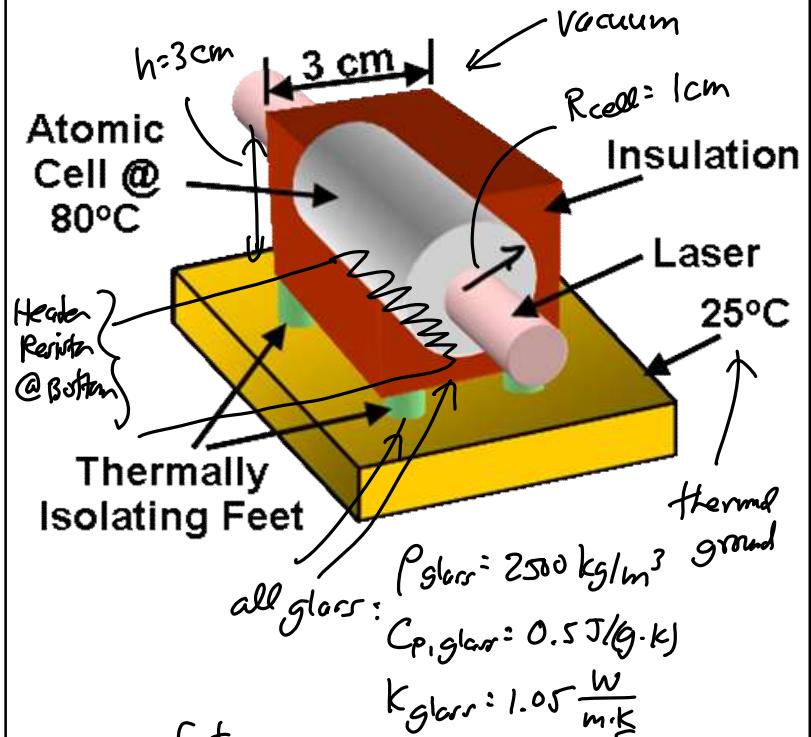
- ↳ Benefits of Miniaturization
- ↳ Examples
 - GHz micromechanical resonators
 - Chip-scale atomic clock
 - Thermal Circuits
 - Micro gas chromatograph

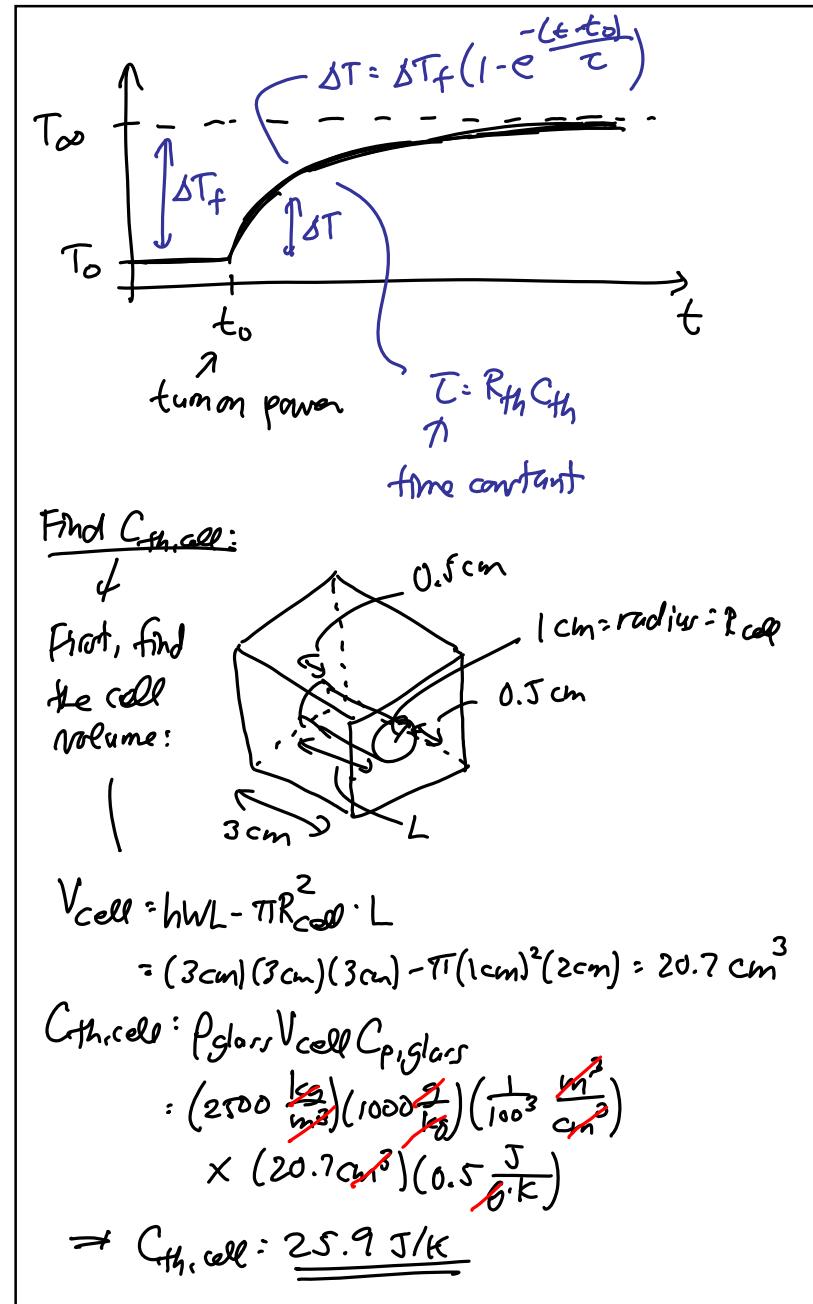
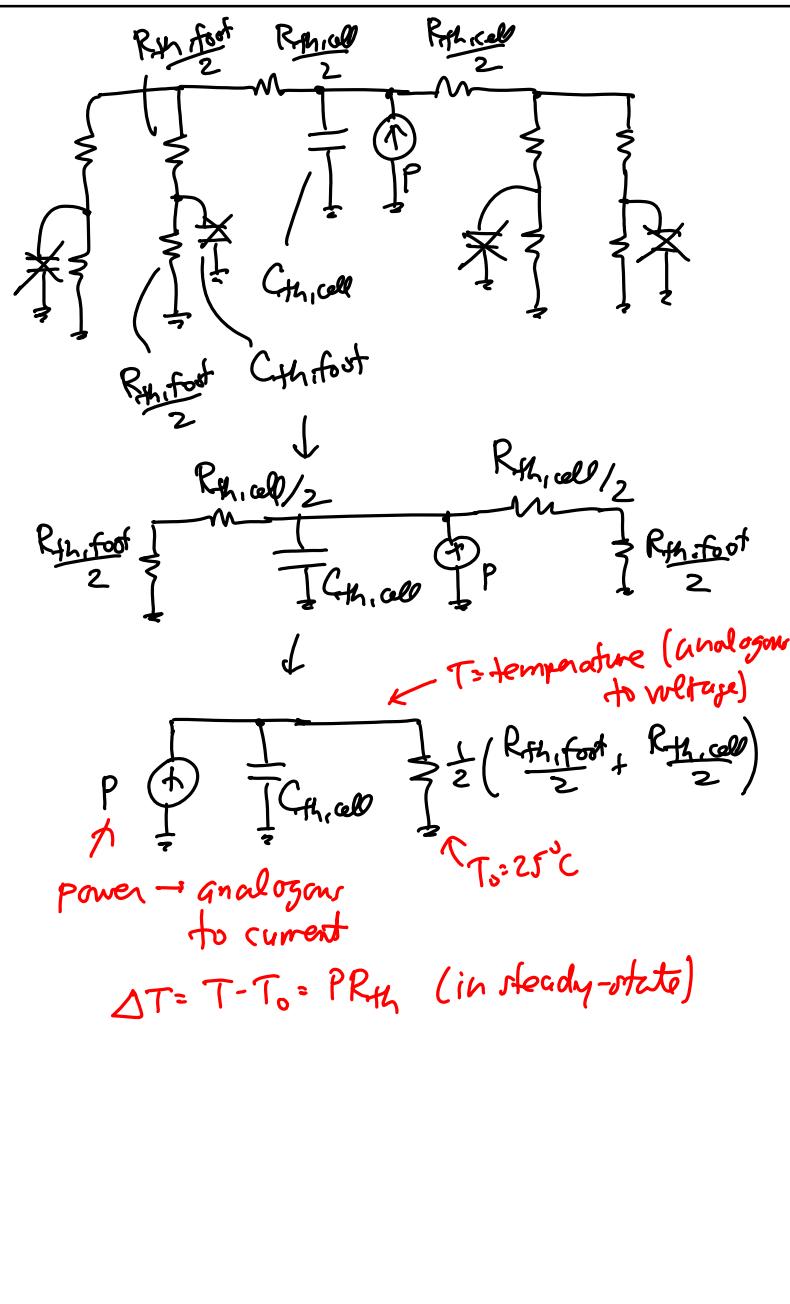
Probably won't get to it, but next up is:

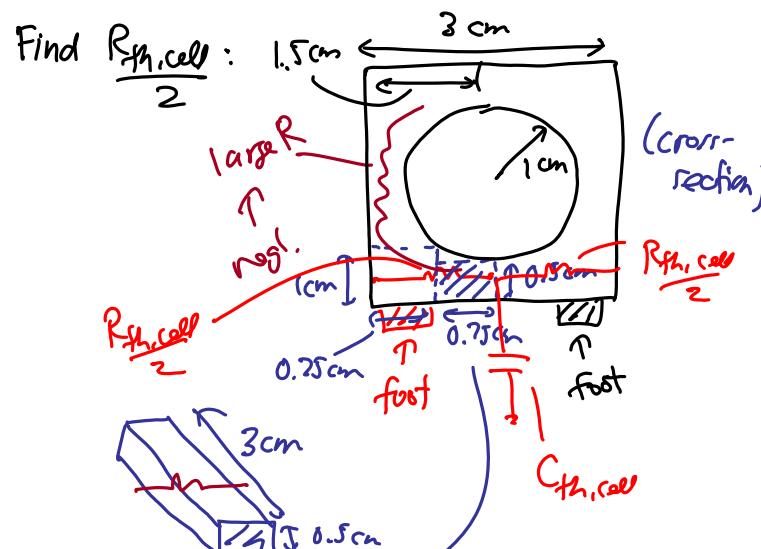
- Senturia, Chpt. 3; Jaeger, Chpt. 2, 3, 6
 - ↳ Example MEMS fabrication processes
 - ↳ Photolithography
 - ↳ Etching
 - ↳ Oxidation
 - ↳ Film Deposition
 - ↳ Ion Implantation
 - ↳ Diffusion

Last Time: Thermal circuit modelingExample: Thermal Clock.

⇒ determine the power needed to get this atomic cell to 80°C (from RT) & how fast



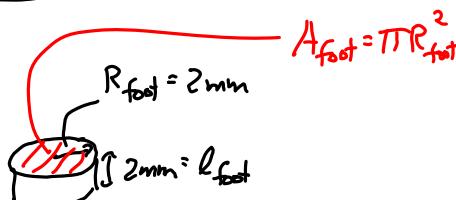
Lecture 5w: Benefits of Scaling III

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$$\frac{R_{th,cell}}{2} = \frac{\frac{3}{4}}{k(3)(\frac{1}{2})} + \frac{\frac{3}{4}}{k(3)(1)} = \frac{1}{k} \left(\frac{1}{8} + \frac{1}{4} \right) = \frac{3.1}{8k}$$

$$\left[R_{th} = \frac{l}{KA} \right] \therefore \frac{R_{th,cell}}{2} = \frac{3}{8} \frac{1}{1.05} \times (100 \frac{cm}{m}) = 35.7 \text{ K/W}$$

Find $R_{th,foot}$:



$$\therefore R_{th,foot} = \frac{l_{foot}}{KA_{foot}} = \frac{2 \text{ mm}}{(1.05 \frac{W}{m \cdot K}) \pi (2 \text{ mm})^2} = 151.6 \frac{K}{W}$$

Then:

$$R_{th} = \frac{1}{2} \left(\frac{R_{th,foot}}{2} + \frac{R_{th,cell}}{2} \right) \\ = \frac{1}{2} \left(\frac{151.6}{2} + \frac{35.7}{2} \right) \rightarrow R_{th} = \underline{\underline{55.8 \text{ K/W}}}$$

\Rightarrow Find the power req'd to maintain $T_{qs} = 80^\circ\text{C}$ in steady-state:

$$P = \frac{T_{qs} - T_0}{R_{th}} = \frac{(80 - 25)}{55.8} = 0.99 \text{ W} \rightarrow \underline{\underline{1 \text{ W}}}$$

\Rightarrow find the time constant:

$$T = R_{th} C_{th,cell} = (55.8 \text{ K/W})(25.9 \text{ J/K}) = \underline{\underline{24 \text{ min.}}}$$

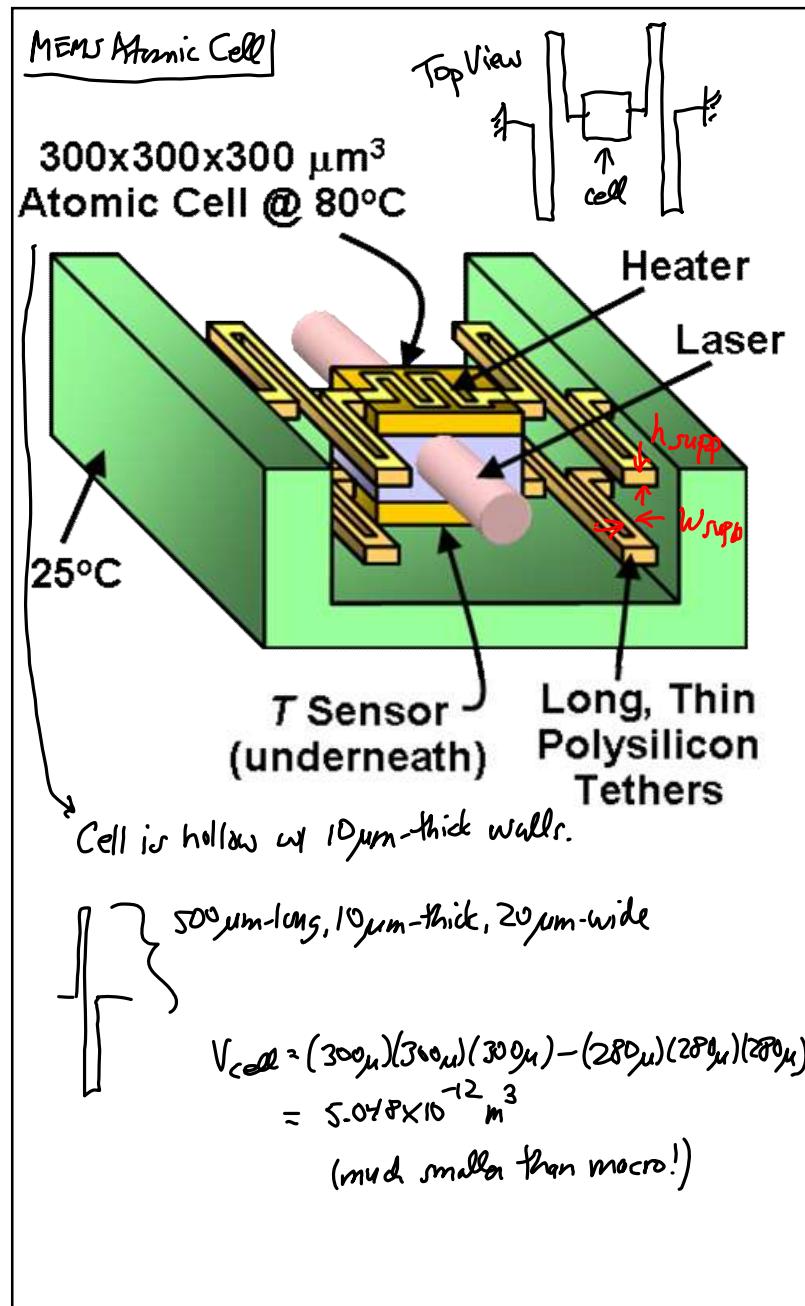
\hookrightarrow It takes $\sim 3T$ to reach steady-state
 \therefore must wait 72 min. before using this atomic cell

How about using MEMS, i.e., scaling down?

\Rightarrow much smaller cell volume $\rightarrow V \downarrow$ $\rightarrow C_{th} \downarrow$
 cont'd \downarrow weight \downarrow

Macro: \leftarrow mass of atomic cell
 \downarrow shrink dimensions

Micro:
 Can do this \rightarrow use long-thin supports to suspend the cell



$$C_{th,cell} = \rho_{glass} V_{cell} C_p, glass$$

$$= (2580 \frac{\text{kg}}{\text{m}^3})(5.048 \times 10^{-12} \text{ m}^3)(500 \frac{\text{J}}{\text{g}\cdot\text{K}})$$

$$= 6.31 \times 10^{-6} \frac{\text{J}}{\text{K}} \leftarrow 4 \text{ million } \times \text{ smaller than macro!}$$

$$R_{th,supp} = \frac{l_{supp}}{k_{polySi} W_{supp} h_{supp}} = \frac{500 \mu\text{m}}{(30 \frac{\text{W}}{\text{m}\cdot\text{K}})(20 \mu\text{m})(10 \mu\text{m})}$$

$$\Rightarrow R_{th,supp} = 83,333 \text{ K/W}$$

... and...

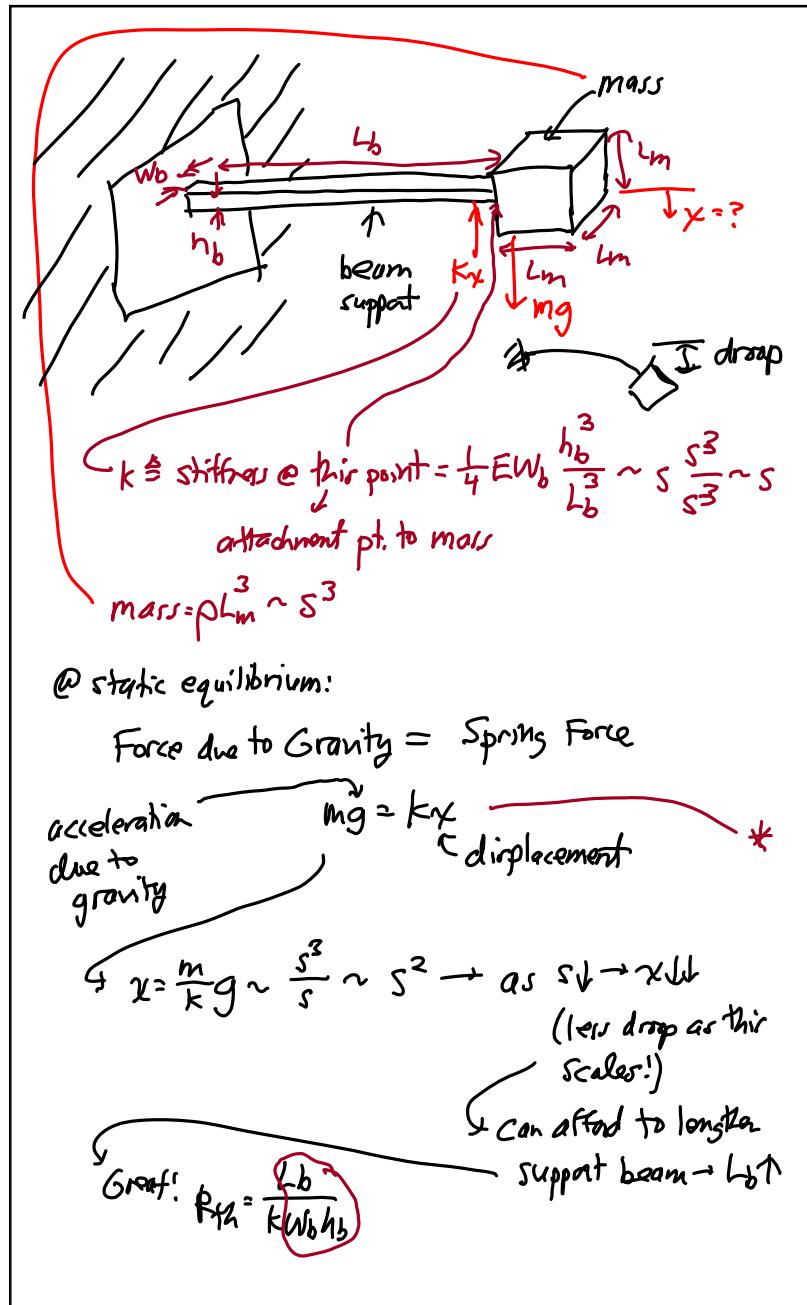
$$P = \frac{4(80-25)}{83,333} = 2.64 \text{ mW} \rightarrow 548 \text{ X smaller}$$

$$T = 0.13 \text{ s} \leftarrow 7300 \text{ X faster}$$

All Due to Scaling!

Remarks: (What makes this possible)

- ① Scaling reduces $C_{th} \sim l^3 \rightarrow s^3$
 $\downarrow s \downarrow \rightarrow C_{th} \downarrow$
- ② Scaling allows use of long, thin tether supports
 $R_{th} \downarrow$



$$\cancel{\rho L_m^3 g = \frac{1}{4} E w_b \frac{h_b^3}{L_b^3} x}$$

$$\frac{L_b}{w_b h_b} = \frac{1}{4} E \frac{h_b^2}{L_b^2} x \frac{1}{\rho L_m^3 g} \sim \frac{S^2}{S^2} \frac{1}{S^3} \sim \frac{1}{S^3}$$

?

π

Const.

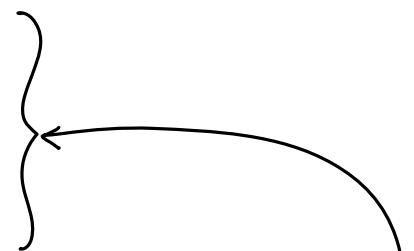
$\sim R_{th}$

as $S \downarrow \rightarrow \frac{L_b}{w_b h_b} \sim R_{th} \uparrow$

- Go through slides 30-31 and 37-48 in Module 2 to finish up Thermal Circuits and cover Micro Gas Analyzers

Lecture 5w: Benefits of Scaling IIIProcess Module Overview:

- Lecture Topics:
 - ↳ Photolithography
 - ↳ Etching
 - ↳ Oxidation
 - ↳ Film Deposition
 - ↳ Ion Implantation
 - ↳ Diffusion
- As stated earlier, this is now assumed knowledge
- I will gloss over this material to review it a bit, but will not go over it in detail
- You can watch my lectures from EE245, Fall 2012, on the Webcast Berkeley site for more in depth coverage: Lectures 6-8
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Process Modules

⇒ there are actually only a few basic modules used for processing

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Combination of these in the correct sequence yields an integrated circuit technology that provides transistors, MEMS, nanodevices, etc.

⇒ For each module, need to understand:

- ① Physics and engineering of each module in detail.
- ② Interaction between modules.
- ③ The effect of each module on the finished device.