EE 247B/ME218: Introduction to MEMS Design Lecture 9m1: Surface Micromachining

prototyping and "foundry" services

• Designed to service as many

users as possible; basically

an attempt to provide a universal MEMS process

• \$4,900 for 1 cm² dies

• 8 photomasks

• Three-level polysilicon surface micromachining process for

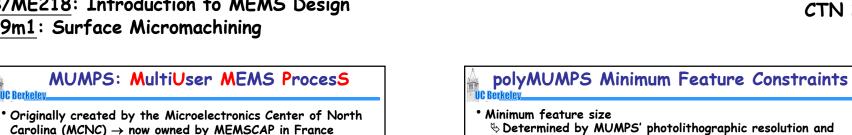
IETA

Micromotor fabricated

via MUMPS

10¥n

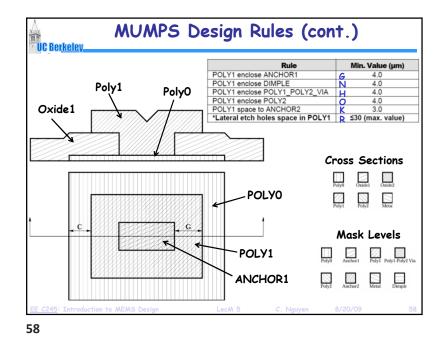
30KU



- betermined by MUMPS' photolithographic resolution and alignment precision
- & Violations result in missing (unanchored), under/oversized, or fused features
- Use minimum feature only when absolutely necessary

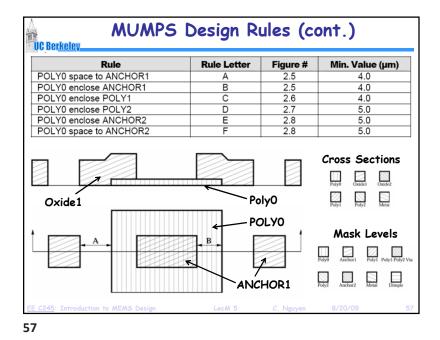
	Nominal [µm]	Min Feature [µm]	Min Spacing [µm]
POLYO, POLY1, POLY2	3	2	2
POLY1_POLY2_VIA	3	2	2
ANCHOR1, ANCHOR2	3	3	2
DIMPLE	3	2	3
METAL	3	3	3
HOLE1, HOLE2	4	3	3
HOLEM	5	4	4
EE C245: Introduction to MEMS Design	LecM 5	C. Nguyen	8/20/09 5

56



55

UC Berkeley

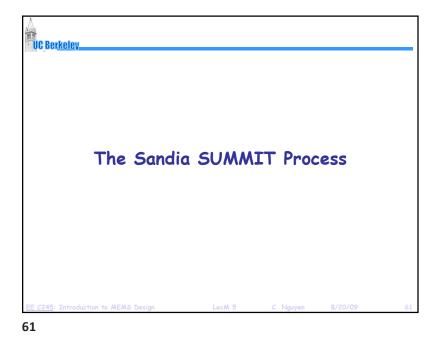


<u>EE 247B/ME218</u>: Introduction to MEMS Design <u>Lecture 9m1</u>: Surface Micromachining

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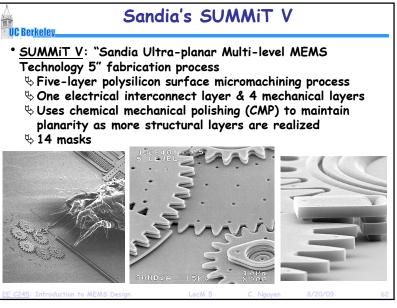
Rule	Rule Letter	Figure #	Min. Value (µm)
POLY0 space to ANCHOR1	A	2.5	4.0
POLY0 enclose ANCHOR1	B	2.5	4.0
POLY0 enclose POLY1	C	2.6	4.0
POLY0 enclose POLY2	D	2.7	5.0
POLY0 enclose ANCHOR2	E	2.8	5.0
POLY0 space to ANCHOR2	F	2.8	5.0
Rule	Rule Letter	Figure #	Min. Value (µm)
POLY1 enclose ANCHOR1	G	2.6	4.0
POLY1 enclose DIMPLE	N	2.13	4.0
POLY1 enclose POLY1_POLY2_VIA	н	2.9, 2.11	4.0
POLY1 enclose POLY2	0	2.14	4.0
POLY1 space to ANCHOR2	K	2.11	3.0
*Lateral etch holes space in POLY1	R	2.15	≤30 (max. value)
Rule	Rule Letter	Figure #	Min. Value (µm)
POLY2 enclose ANCHOR2	J	2.7,2.10	5.0
POLY2 enclose POLY1_POLY2_VIA	L	2.9	4.0
POLY2 cut-in POLY1	P	2.14	5.0
POLY2 cut-out POLY1	Q	2.14	4.0
POLY2 enclose METAL	M	2.12	3.0
POLY2 space to POLY1	I 1	2.10	3.0
HOLE2 enclose HOLE1	Т	2.16	2.0
HOLEM enclose HOLE2	U	2.16	2.0
*Lateral etch holes space in POLY2	s	2.15	≤30 (max. value)

59



Level 1	Level 2	Min. Feature	Min. Spacing	Enclose	Spacing	Cut- In	Cut- Ou
POLY0		2	2				
	ANCHOR1			4/B/2.5	4/A/2.5		
	POLY1			4/C/2.6			
	ANCHOR2			5/E/2.8	5/F/2.8		
	POLY2			5/D/2.7			
POLY1		2	$2/2.5^{2}$				
	POLY0		2.2.0				
	ANCHOR1			4/G/2.6			
	ANCHOR2				3/K/2.11		
	POLY2			4/0/2.14			
	DIMPLE			4/N/2.13			
	POLY1 POLY2 VIA			4/H/2.9			
POLY2		2	2/2.52				
	POLY0						
	POLY1				3/1/2.10	5/P/2.14	4/Q/2.14
	VIA			4/L/2.9			
	ANCHOR2			5/J/2.7			
	METAL			3/M/2.12			
HOLEM	HOLE2			2/U/2.16			
HOLE2	HOLE1			2/T/2.16			
	.7. PolyMUMPs design 1 1, respectively.	ule reference	sheet. Table :	shows minim	um dimensio	ıs (µm), rule	name, and

60



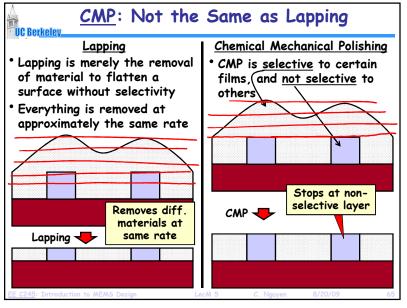


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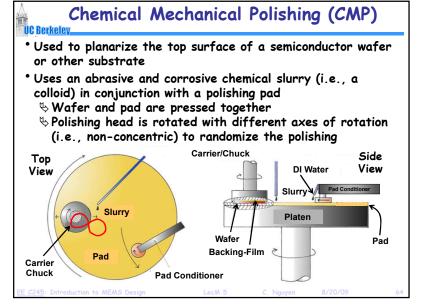
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SUMMiT V Layer Stack **UC Berkele** 2.25 µm mmpoly4 2.0 µm sacox4 (CMP) 0.2 µm dimple4 backfill 2.25 µm mmpoly3 2.0 µm sacox3 (CMP) 0.4 µm dimple3 backfill 1.5 µm mmpoly2 0.3 µm Sacox2 1.0 μm mmpolv1 2.0 um sacox1 0.3 µm mmpoly0 0.80 µm Silicon Nitric Substrate 6 inch wafer, <100>, n-type-0.5 µm dimple1 gap * Uses chemical mechanical polishing (CMP) to maintain planarity as more structural layers are realized

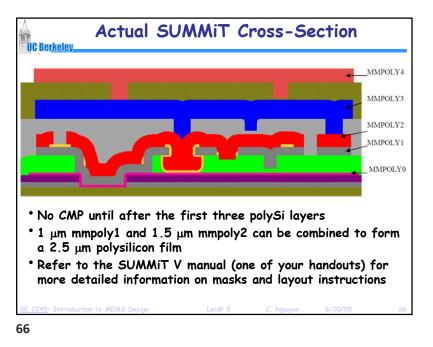
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64



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