

Administrivia

- HW #5 changes
- HW #6
 - Will be up by tonight, 5 problems
 - Due next Wednesday (08/06)
 - Will be a good review for the midterm
- Return HWs 4 and 5 on Monday (08/04)
- Return HW 3 and corrected HWs 1 and 2 on Friday (08/01)
- Midterm II next Wednesday, 08/06/03!
- Practice problems up by tonight
- Review session?

Last time...

- CMOS voltage transfer characteristic
 - SUGGESTION: Go over lecture 16 STEP-BY-STEP
 - Ask questions in discussion and office hours!



This time...

- CMOS inverter propagation delay analysis
 - Complete our propagation delay model
 - Understand MOS capacitances
 - Switch-RC model of the CMOS inverter
 - Understand τ_{LH} and τ_{HL}
- CMOS layouts and fabrication steps
- Extract capacitances, W and L from layout/crosssection
- References: Lectures 18 and 19 (Fall 1999) and Lecture 21 (Spring 2003)

Conclusion

- Completed our propagation delay model
- Switch-RC model of the CMOS inverter
- T_{LH} and T_{HL} calculations
- CMOS layouts and fabrication steps
- References: Lectures 18 and 19 (Fall 1999) and Lecture 21 (Spring 2003)

Next time...

- Barring unforeseen circumstances: GUEST LECTURE BY PROF. TSU JAE-KING
- HW #5 due!
- START PREPARING FOR MIDTERM!