EE 40 Syllabus -- Spring 2003 (Revised April 3, 2003)

Date (Lecture #)	Topic	Text Covered
January 21 (1)	Introduction	
January 23 (2)	Circuit Elements and Quantities, Non-Ideal Elements	Sections 1.1, 1.2, 2.1, 2.2, 5.1
January 28 (3)	Kirchoff's Laws, RC Circuit	Sections 1.3, 8.1 (pp. 282-290)
January 30 (4)	RC Circuits, Time Constant	Section 8.1 (pp. 282-290)
February 4 (5)	Equivalent R and C, Voltage/Current Divider, Charging	Sections 2.2, 2.5, 2.6, 5.2
February 6 (6)	Nodal Analysis	Section 2.3
February 11 (7)	Dependent Sources	Section 4.1
February 13 (8)	Thevenin Equivalents	Section 3.1
February 18 (9)	Operational Amplifiers	Sections 4.2, 4.3, 4.4
February 20	**Midterm 1 (Lectures 2-8)**	
February 25 (10)	Differential Amplifiers/Comparator	Section 4.2
February 27 (11)	Digital Logic	Sections 11.1, 11.2
March 4 (12)	More Digital Logic	Sections 11.1, 11.2
March 6 (13)	Semiconductors, P/N Junction	Sections 13.1, 13.2
March 11 (14)	Load Line, Diode I-V	Sections 3.2, 13.2
March 13 (15)	Diode Circuits	Section 13.2
March 18 (16)	NMOS and PMOS Transistors, I-V	Section 13.4
March 20	**Midterm 2 (Lectures 9-15)**	
March 25 (17)	Spring Break	
March 27 (18)	Spring Break	
April 1 (19)	Fabrication and Layout	
April 3 (20)	NMOS Circuits	
April 8 (21)	CMOS Inverter	Section 15.2
April 10 (22)	CMOS NAND/NOR, Switch Model, Intermediate States	Section 13.5
April 15 (23)	Computing Gate Delay	
April 17 (24)	Catch Up/Review	
April 22 (25)	Schmitt Trigger/Feedback	
April 24	**Midterm 3 (Lectures 16-23)**	
April 29 (26)	Waveform Shaping Circuits	
May 1 (27)	Putting It All Together: Analyzing Multistage Circuits	
May 6 (28)	Fun Designs (Open to Student Presentation)	
May 8 (29)	Fun Designs (Open to Student Presentation)	
May 13 (30)	Mathematical Issues in Circuit Analysis	

Final Exam: Friday, May 23, 12:30-3:30 PM (Exam Group 19)   **Location: TBA**