

# EE 42 – Introduction to Electronics for Computer Science



Fall 2003,  
Dept. EECS, 510 Cory  
UC Berkeley  
Course Web Site

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**Office Hours** M 1, Tu, Tu 10:30-11:30, F 11  
<http://www-inst.eecs.berkeley.edu/~ee42/>

## EECS 42 Introduction to Electronics for Computer Science

**Brief Overview:** This 3-unit, lower-division EECS course covers fundamental hardware topics at the device and circuit level. Topics include: (1) Essential quantities for circuit analysis, (2) Circuit laws and DC circuit analysis, (3) Transients and step response of RC circuits, (4) Digital logic and gates, (5) Essential analog elements for digital circuits, (6) CMOS integrated circuits and static logic gates (7) Switch models and performance of CMOS logic gates.

**Textbook:** "Electrical Engineering, An Introduction", Schwarz and Oldham, 2<sup>nd</sup> ed., 1993, Saunders. Handouts for CMOS material.

**WEB Information:** [www-inst.eecs.berkeley.edu/~ee42](http://www-inst.eecs.berkeley.edu/~ee42) Lectures, homework and solutions, sample exams, Faculty/TA office hours, emails, access to our NEWSGROUP, etc. **There will be no handouts of lecture notes at class so print them in advance from the web.**

**Discussion Sections:** M 11, M 3, W 11, Th 1, F 1 293 Cory (5 Sections based on enrollment) **Discussion Sections start Monday August 25<sup>th</sup>.**

**Homework:** Posted Tuesday. Due at 1 PM the Wednesday of the following week. Work should be **labeled by section** and placed in the boxes marked 'EECS 42 Homework' in the hall near room 275 Cory. **Homework will not be collected in lecture** and will be **returned in section**. You are encouraged to learn together with fellow students, but **the work submitted must be your own (See EECS/class Policy).**

**Examinations:** There will be two in-class midterms (Thursday, October 2 and Thursday, November 6th) and a Final (Friday December 12, 8-11 AM). All exams are closed book, but we supply a list of formulas with the exam.

**Laboratory:** We encourage students to take EECS 43, a P/NP lab that reinforces through 'hands-on' work (including a microcontroller-based Calbot vehicle) most of the basic concepts taught in this course. **See the EE43 web site at <http://inst.EECS.Berkeley.EDU/~ee43/> and the door to 140 Cory to sign-up.**

**Grading:** The course grade is made up as follows: Homework: 5% ; Midterm 1: 23% ; Midterm 2: 23% ; Final Exam: 49%. The grading is a balance between being "curved" and being based on overall performance. Typical ranges A: >85 ; B: 70 to 85 ; C: 55 to 70.