10/16/03

## EE 42 – Introduction to Electronics for Computer Science

Fall 2003, Dept. EECS, 510 Cory UC Berkeley Course Web Site Prof. A. R. Neureuther neureuth@eecs.berkeley.edu 642 - 4590 Office Hours M1, Tu, Th 10:30-11:30, F 11 http://www - inst.eecs.berkeley.edu/~ee42/

## Problem Set # 7 Due: 1 PM Oct 22nd, 2003 in box outside 240 Cory

## Announcement:

**Reading Week #7:** Review Schwarz and Oldham 4.1, 4.2, 4.3 **Topics:** Circuit Analysis with dependent sources and operational amplifiers.

**7.1 Dependent Sources** Use the circuit to the right. The dependent source on the graph is A(V+ - V-)

a) Find Vob) Is the gain (Vo/V<sub>IN</sub>) greater than unity?

**7.2 Ideal Op-Amp** Use the circuit to the right.

a) Find Vout in terms of  $V_1$ ,  $V_2$  and the resistances

b) Explain why the value of Vout is independent of  $R_L$ 

c) Explain why the value of Vout is independent of R<sub>2</sub>

**7.3 Cascade Op-Amps** Use the circuit to the right.

a) Find an expression for Vo<sub>1</sub>

b) Find an expression for Vo<sub>2</sub>

**7.4 Dependent Sources** Use the circuit to the right. **NOTE:** VDD is a D.C. (constant) voltage source and thus does not need to be included in the analysis and in fact can be considered an a.c. ground that grounds the top end of the current source and RO ; Be sure to assume the output is shorted; Leave RO in your analysis (even though it is not present in the solution that you are not supposed to be looking at).

a) Use KCL, find  $V_E$ 

b) Find the voltage across the AA' terminal, Vtest

c) Find the resistance seen looking into AA'

