

EECS 40 Op Amps Prelab

Name: _____

TA: _____

Section: _____

1. Draw your plan to build the non-inverting amplifier configuration on the breadboard, clearly numbering and marking all pins of the op-amp chip that are used.

2. In the configuration you drew above (assuming an ideal op-amp model), what are the current values going into Pins 2 and 3? What is the voltage difference between Pins 2 and 3? What is common about the voltage signal on pins 2 and 3 as referenced to ground?

3. Draw the voltage follower schematic configuration. What is the gain of this circuit?

Mention the most important use of this configuration.

4. In order to achieve a gain of -50 in the following circuit, what value of R_2 should be used? What is the output voltage for this circuit?

