## EECS 16A Designing Information Devices and Systems I Fall 2021 Lecture Notes

### 11.1 Node Voltage Analysis (NVA)

### 11.1.1 Objective

Find all voltages (and currents) in an electronic circuit.

### 11.1.2 Procedure

The method proceeds in several steps, illustrated below for the following example circuit:


- Step 1: Reference Node

Select a reference (ground) node. Any node can be chosen for this purpose. In this example, we choose the node at the bottom of the circuit diagram.


- Step 2: Label Nodes with Voltage set by Voltage Sources

Voltage sources set the voltage of the node they are connected to. In the example, there is only one source, $V_{s}$, and we label the corresponding source $u_{1}$ (names are arbitrary, but must be unique).


- Step 3: Label Remaining Nodes

Now we label all remaining nodes in the circuit except the reference. In this example there are two, $u_{2}$ and $u_{3}$.


- Step 4: Label Element Voltages and Currents

Next we mark all element voltages and currents.
Start with the current. The direction is arbitrary (top to bottom, bottom to top, it won't matter, but stick with your choice in subsequent steps). Then mark the element voltages following the passive sign convention discussed on the following page.
The element voltage for $I_{s}$ is not marked in the example since it will not be needed in the calculations below. Same for the voltage source. There is no harm in marking those, too.


## Passive sign convention

The passive sign convention dictates that positive current should enter the positive terminal and exit the negative terminal of an element. Below is an example for a resistor:


As long as this convention is followed consistently, it does not matter which direction you arbitrarily assigned each element current to; the voltage referencing will work out to determine the correct final sign. When we discuss power later in the module, you will see why we call this convention "passive."

## - Step 5: KCL Equations

Write KCL equations for all nodes with unknown voltage, $u_{2}$ and $u_{3}$ in the example. Refer to Note 11 A for a reminder of how to write KCL equations.
At $u_{2}$ we get (sum of all currents entering the node equals sum of currents exiting):

$$
I_{R_{1}}=I_{R_{2}}+I_{R_{4}}
$$

Similar for $u_{3}$ :

$$
I_{R_{4}}+I_{I_{s}}=I_{R_{3}}
$$

- Step 6: Element Currents

Find expressions for all element currents in terms of voltage and element characteristics (e.g. Ohm's law) for all circuit elements except voltage sources. In the example there are five, $R_{1}, R_{2}, R_{3}, R_{4}, I_{s}$.

Find expressions for element currents for all elements (except the voltage source) using their characteristics. Applying Ohm's law to the two resistors, we find that

$$
\begin{aligned}
I_{R_{1}} & =\frac{V_{R_{1}}}{R_{1}} \\
I_{R_{2}} & =\frac{V_{R_{2}}}{R_{2}} \\
I_{R_{3}} & =\frac{V_{3}}{R_{3}} \\
I_{R_{4}} & =\frac{V_{R_{4}}}{R_{4}} \\
I_{I_{s}} & =I_{s}
\end{aligned}
$$

We also have

$$
u_{1}=V_{s} .
$$

- Step 7: Substitute all element voltages in your step 6 equations with node voltages. For example, $V_{R_{1}}=u_{1}-u_{2}=V_{s}-u_{2}$ and $V_{R_{2}}=u_{2}-0=u_{2}$.

$$
\begin{aligned}
I_{R_{1}} & =\frac{V_{s}-u_{2}}{R_{1}} \\
I_{R_{2}} & =\frac{u_{2}}{R_{2}} \\
I_{R_{3}} & =\frac{u_{3}}{R_{3}} \\
I_{R_{4}} & =\frac{u_{2}-u_{3}}{R_{4}} \\
I_{I_{s}} & =I_{s}
\end{aligned}
$$

- Step 8: Substitute Element Currents in KCL Equations

Now we substitute the expressions derived in Step 7 into the KCL equations from Step 5.

$$
\begin{aligned}
\frac{V_{s}-u_{2}}{R_{1}} & =\frac{u_{2}}{R_{2}}+\frac{u_{2}-u_{3}}{R_{4}} \\
I_{s}+\frac{u_{2}-u_{3}}{R_{4}} & =\frac{u_{3}}{R_{3}}
\end{aligned}
$$

Let's make this a bit nicer by grouping the unknowns ( $u_{2}$ and $u_{3}$ ) on the left side and the known terms on the right:

$$
\begin{aligned}
u_{2}\left(\frac{1}{R_{1}}+\frac{1}{R_{2}}+\frac{1}{R_{4}}\right)+u_{3}\left(-\frac{1}{R_{4}}\right) & =\frac{V_{s}}{R_{1}} \\
u_{2}\left(-\frac{1}{R_{4}}\right)+u_{3}\left(\frac{1}{R_{3}}+\frac{1}{R_{4}}\right) & =I_{s}
\end{aligned}
$$

- Step 9: Solve

Now we can solve for the unknown node voltages, $u_{2}$ and $u_{3}$ in the example. First, rearrange terms to cast the equations as a linear algebra problem:

$$
\left[\begin{array}{cc}
\frac{1}{R_{1}}+\frac{1}{R_{2}}+\frac{1}{R_{4}} & -\frac{1}{R_{4}} \\
-\frac{1}{R_{4}} & \frac{1}{R_{3}}+\frac{1}{R_{4}}
\end{array}\right]\left[\begin{array}{l}
u_{2} \\
u_{3}
\end{array}\right]=\left[\begin{array}{c}
\frac{V_{s}}{R_{1}} \\
I_{s}
\end{array}\right]
$$

Then compute the solution using Gaussian Elimination (or let the computer do the work, here using sympy):

```
                                    _images/Note_10code_nva.pdf"
    from sympy import *
    init_printing(use_unicode=True)
R1, R2, R3, R4 = symbols('R1 R2 R3 R4')
Y = Matrix([[ 1/R1+1/R2+1/R4, -1/R4], [-1/R4, 1/R3+1/R4]])
V1, I1 = symbols('V1 I1')
b = Matrix([ V1/R1, I1 ])
Vn1, Vn2 = linsolve((Y, b)).args[0]
```

Algebraic result:
>>> Vn1
$\frac{R_{2} \cdot\left(I \cdot 1 \cdot \mid R_{1} \cdot R_{3}+R_{3} \cdot V V_{1}+R_{4} \cdot V_{1}\right)}{\left.R_{1} \cdot R_{2}\right]+R_{1} \cdot R_{3} \cdot R_{1} \cdot R_{4}\left[R_{4}+R_{2} \cdot R_{3}+R_{2} \cdot R_{4}\right.}$
>>> Vn2

$-R_{1} \cdot R_{2} \cdot R_{3}+\left(R_{3}+R_{4}\right) \cdot\left(R_{1} \cdot R_{2}+R_{1} \cdot R_{4}+R_{2} \cdot R_{4}\right)$

Numerical result:

```
>>> values = {R1:1, R2:2, R3:3, R4:4, I1:0.5, V1:1}
>>>
>>> f"Vn1 = {Vn1.evalf(3, subs=values)} V"
Vn1 = 0.739 V
>>> f"Vn2 = {Vn2.evalf(3, subs=values)} V"
Vn2 = 1.17 V
```


### 11.1.3 Branch Currents

Sometimes we want to solve for branch currents. These are easily obtained from the node voltages and element equations. For example, the current $I_{R_{4}}$ through resistor $R_{4}$ is

$$
I_{R_{4}}=\frac{V_{R_{4}}}{R_{4}}=\frac{u_{2}-u_{3}}{R_{4}}
$$

Numerical result using the values provided in Step 9 of 11.1.2: $I_{R_{4}}=-0.109 \mathrm{~A}$.

### 11.1.4 Summary

Node voltage analysis can be used to determine voltages and currents from component values in electronic circuits.

Reference: Schaum's Outline of Electric Circuits, Seventh Edition, Section 4.4.

