Homework 5

This homework is due on Friday, February 25, 2022, at 11:59PM. Self-grades and HW Resubmissions are due on the following Friday, March 4, 2022, at 11:59PM.

1. **Hambley P6.55**

   Consider the first-order highpass filter shown in Figure 1. The input signal is given by

$$v_{\text{in}}(t) = 5 + 5\cos(2000\pi t) \tag{1}$$

   Find an expression for the output $v_{\text{out}}(t)$ in steady-state conditions.

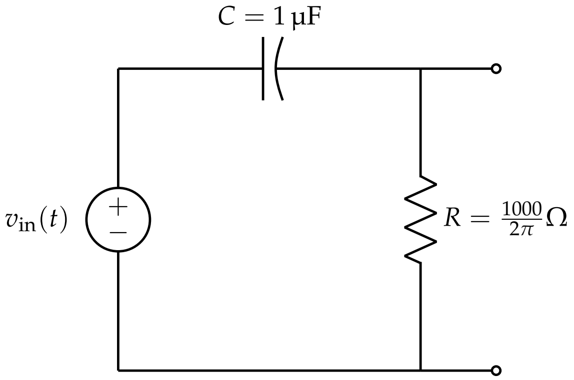


Figure 1: P6.55

2. Hambley P6.71

Consider the series resonant circuit shown in Figure 2, with $L = 20\,\mu\text{H}$, $R = 14.14\,\Omega$, and $C = 1000\,\text{pF}$. Compute the resonant frequency, the bandwidth, and the half-power frequencies. Assuming that the frequency of the source is the same as the resonant frequency, find the phasor voltages across the elements and sketch a phasor diagram.

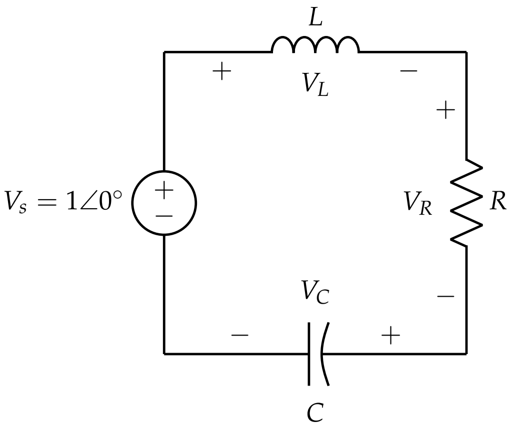


Figure 2: P6.71

3. **Bandpass Half Power Derivation**

For a series resonance circuit bandpass filter, prove that the two half-power frequencies can be written as

$$f_H = f_0 + \frac{B}{2} \tag{2}$$

$$f_L = f_0 - \frac{B}{2} \tag{3}$$

when $Q_s \gg 1$, where $B = \frac{f_0}{Q_s}$ and $Q_s = 2\pi f_0 \frac{L}{R}$.

4. **Hambley P6.73**

   Suppose we have a series resonant circuit for which $B = 15\,\text{kHz}$, $f_0 = 300\,\text{kHz}$, and $R = 40\,\Omega$. Determine the values of L and C.

5. **Hambley P6.74**

Derive an expression for the resonant frequency of the circuit shown in Figure 3. (Recall that we have defined the resonant frequency to be the frequency for which the impedance is purely resistive).

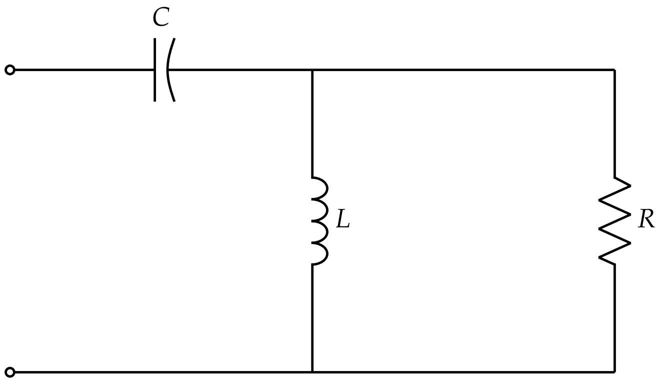


Figure 3: P6.74

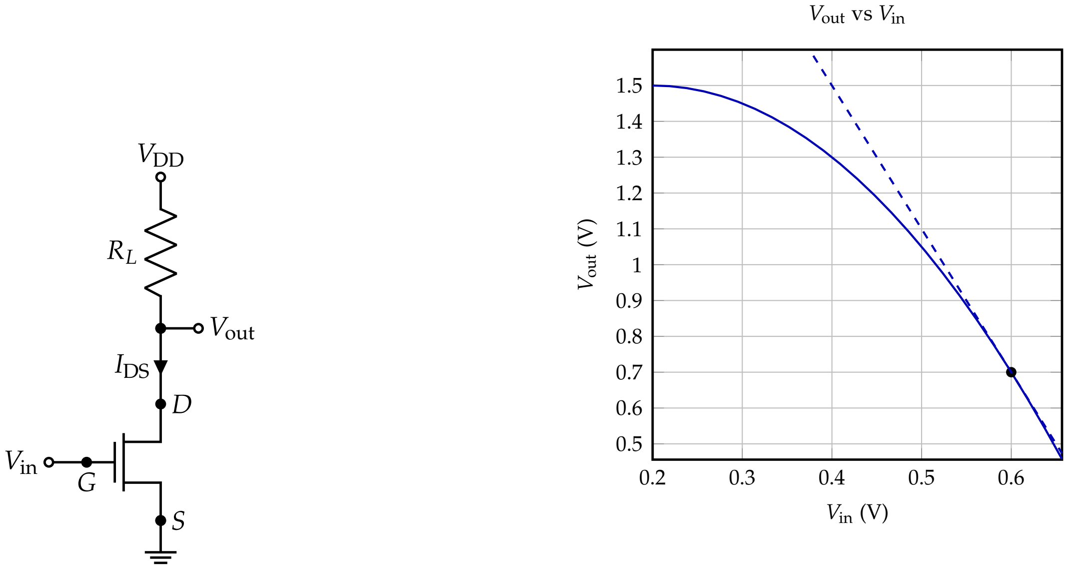
6. Using a Nonlinear NMOS Transistor for Amplification

Consider the following schematic where $V_{\text{DD}} = 1.5\,\text{V}$, $R_L = 400\,\Omega$ and the NMOS transistor has threshold voltage $V_T = 0.2\,\text{V}$. We are interested in analyzing the response of this circuit to input voltages of the form $V_{\text{in}}(t) = V_{\text{in,DC}} + v_{\text{in,AC}}(t)$, where $V_{\text{in,DC}}$ is some constant voltage and $v_{\text{in,AC}}(t) = 0.001\cos(\omega t)\text{V}$ is a sinusoidal signal whose magnitude is much smaller than $V_{\text{in,DC}}$.

The I-V relationship of an NMOS can be modeled as non-linear functions over different regions of operation. For simplicity, let's just focus on the case when $0 \le V_{\text{GS}} - V_T < V_{\text{DS}}$. In this regime of interest, the relevant I-V relationship is given by

$$I_{\text{DS}}(V_{\text{GS}}) = \frac{K}{2}(V_{\text{GS}} - V_T)^2 \tag{4}$$

where K is a constant that depends on the NMOS transistor size and properties.



(a) NMOS Transistor circuit

(b) V_{out} vs V_{in} in the regime of interest. Tangent is drawn at the operating point $V_{\text{in,DC}} = 0.6\,\text{V}$, $V_{\text{out,DC}} = 0.7\,\text{V}$

Figure 4: NMOS figures.

From Ohm's law and KCL, we know that

$$V_{\text{out}}(t) = V_{\text{DD}} - R_L I_{\text{DS}}(t). \tag{5}$$

Note from Figure 4a that $V_{\text{in}} = V_{\text{GS}}$ and $V_{\text{out}} = V_{\text{DS}}$. In Figure 4b, we can see the curve of V_{out} vs V_{in} in the transistor operating regime of interest.

(a) **Using eq. (4) and eq. (5), express $V_{\text{out}}(t)$ as a function of $V_{\text{in}}(t)$ symbolically.** (You can use $V_{\text{DD}}, R_L, V_{\text{in}}, K, V_T$ in your answer.)

(b) We can decompose the input into constant (i.e., DC) and time-varying (i.e., AC) components to obtain $V_{\text{in}}(t) = V_{\text{in,DC}} + v_{\text{in,AC}}(t)$. Furthermore, we can consider $V_{\text{out}}(t)$ as a function of $V_{\text{in}}(t)$ and approximate it as a linear equation, as in

$$\widehat{V_{\text{out}}}(V_{\text{in}}) = V_{\text{out}}(V_{\text{in,DC}}) + \frac{\mathrm{d}V_{\text{out}}}{\mathrm{d}V_{\text{in}}}\Big|_{V_{\text{in}}=V_{\text{in,DC}}}(V_{\text{in}} - V_{\text{in,DC}}) \tag{6}$$

Solve for this approximation, i.e., find $\widehat{V_{\text{out}}}(V_{\text{in}})$. Write your answer in terms of the transconductance gain (g_m), $V_{\text{in,DC}}$, $v_{\text{in,AC}}$, and other constants provided in the problem. *This technique is called linearization, which we will cover later in this course.*

(c) Next, we can also decompose the output V_{out} into DC and AC components to obtain $V_{\text{out}} = V_{\text{out,DC}} + v_{\text{out,AC}}(t)$. **What is $V_{\text{out,DC}}$ from the linear approximation in part 6.b? Simplify the linear approximation to be in terms of $v_{\text{out,AC}}(t)$ and $v_{\text{in,AC}}(t)$, for very small $v_{\text{in,AC}}(t)$. Then, find the AC input-output gain (i.e., find $\frac{v_{\text{out,AC}}(t)}{v_{\text{in,AC}}(t)}$)**

(HINT: If $v_{\text{in,AC}}(t)$ is small, then $V_{\text{in,DC}} - V_T$ is small, which means our approximation $\widehat{V_{\text{out}}}(V_{\text{in}})$ is very close to the true V_{out}.)