

EECS 16B Designing Information Devices and Systems II Lecture 3

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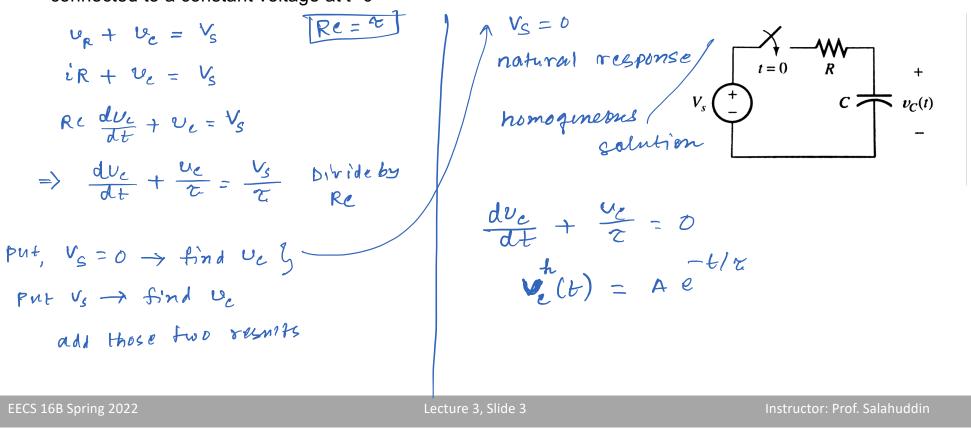
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Transient Response

- Outline
 - R-C circuits
 - R-L circuits
 - R-L-C Circuits
- Reading- Hambley text sections 4.1-4.5, slides

Recap: R-C circuits: Response in time

We now ask a slightly different question. What happens if a capacitor that had initially no charge is connected to a constant voltage at t=0



R-C circuits: Response in time

We now ask a slightly different question. What happens if a capacitor that had initially no charge is connected to a constant voltage at t=0

$$\frac{dv_{c}}{dt} + \frac{v_{c}}{t} = \frac{v_{s}}{2} - 0$$
First order, line or differential equation
if we can convert eq^h (i) to look like

$$\frac{dy}{dt} = g(t) \Rightarrow y(t) = \int_{0}^{t} g(t) dt$$
Multiply eq^h (i) by an unknown function $f(t)$

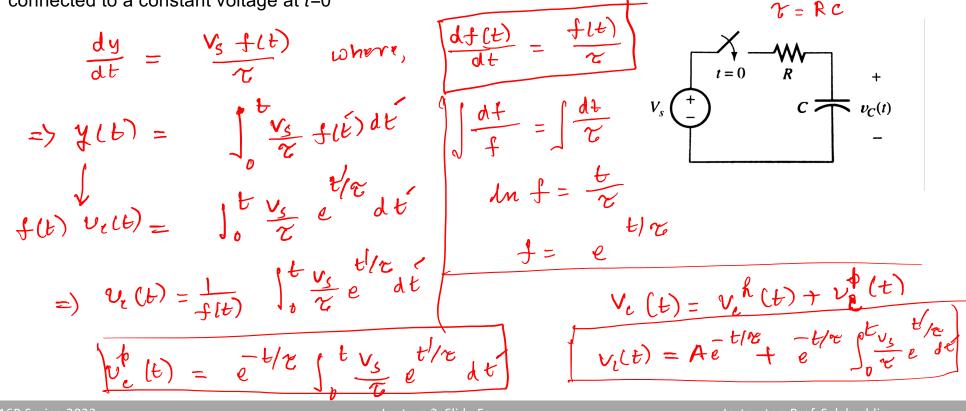
$$f(t) \frac{dv_{c}}{dt} + \frac{f(t)}{t} v_{c} = v_{s} \frac{f(t)}{t} \int_{0}^{t} \frac{d}{dt} (mn) = m \frac{dn}{dt} + n \frac{dm}{dt}$$

$$\frac{f(t) \frac{dv_{c}}{dt} + v_{c} \frac{df(t)}{dt} = v_{s} \frac{f(t)}{t} \Rightarrow if \cdot \frac{df(t)}{dt} = \frac{f(t)}{t}$$

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R-C circuits: Response in time

We now ask a slightly different question. What happens if a capacitor that had initially no charge is connected to a constant voltage at t=0

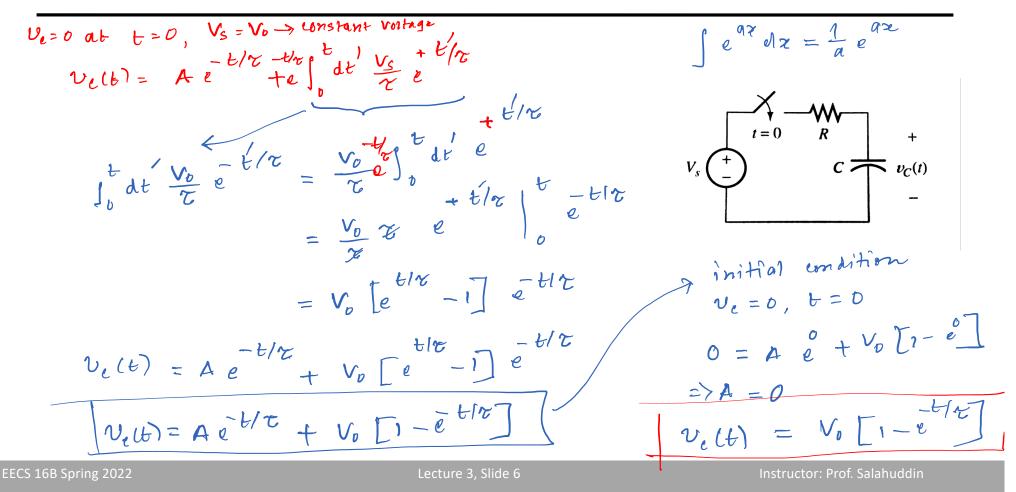


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Charging and Discharging a Capacitor



General Solution of the First order, Linear, Differential Equation

Particular Solution (Integrating Factor Method):

b(t)f(t)

For a first order, linear differential equation of the form

 $\frac{dy}{dt} + ay(t) = b(t)$ where we assume a to be a constant

Homogeneous/Compl ementary solution

$$\frac{dy}{dt} + ay(t) = 0$$

$$\Rightarrow \frac{dy}{y} = -a$$

$$\Rightarrow \ln(y) = -at + C$$

$$\Rightarrow y(t) = Ke^{-at}$$

$$\frac{dy}{dt} + ay(t) = b(t)$$
We want to find a multiplier function $f(t)$
such that
$$f(t)\frac{dy}{dt} + af(t)y(t) = b(t)f(t)$$
can be written as

$$\frac{a}{dt}[y(t)f(t)] = b(t)f(t) \quad \text{--(A)}$$

 $v(t) = Ke^{-at} + e^{-at} \int e^{at} b(t) dt$

For equation (A) to hold

$$\frac{df(t)}{dt} = af(t)$$

$$\Rightarrow f(t) = e^{at}$$

Then from (A)

$$y(t) = \frac{1}{f(t)} \int b(t)f(t)dt$$

$$\Rightarrow y_p(t) = e^{-at} \int e^{at}b(t)dt$$

K is determined using initial condition

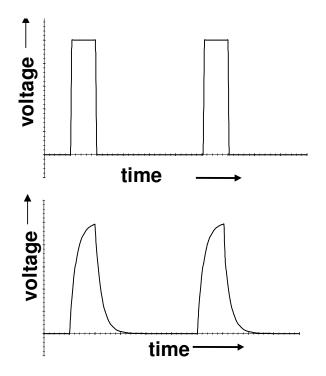
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 $\Rightarrow \frac{dy}{y} = -a$

Digital Signals to a RC circuit

• Every node in a real circuit has capacitances

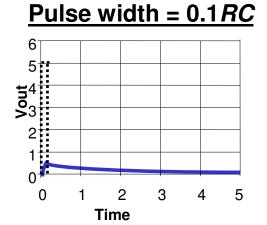
 Even if we send in very 'pure' square looking pulses what we actually get is how it looks in the right due to capacitor charging and discharging <u>unless we go</u> <u>very very slow</u>



Pulse Distortion

The input voltage pulse width must be large enough; otherwise the pulse is distorted







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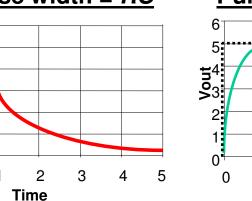
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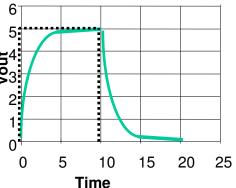
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0

0



Pulse width = 10RC



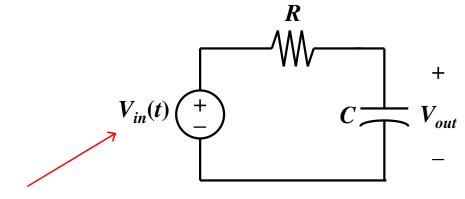
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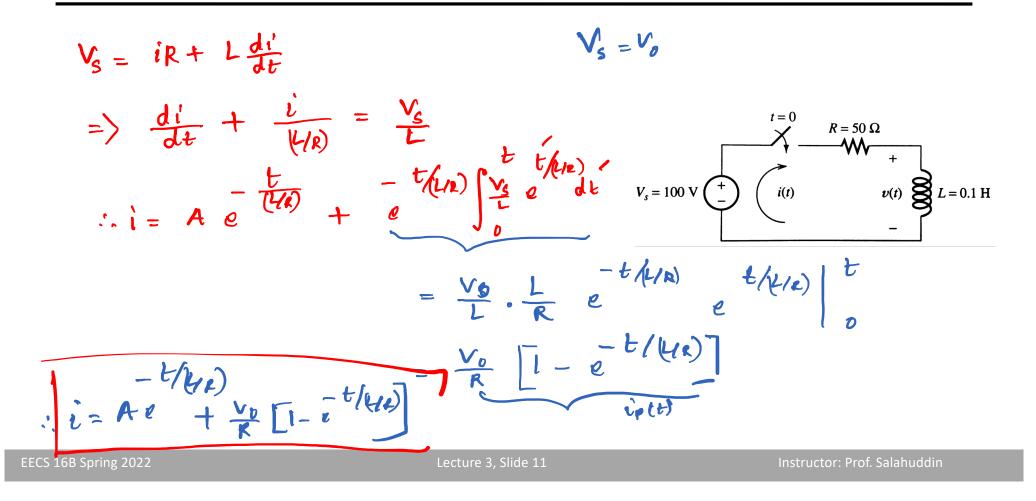
Computers are RC circuits (almost)

- Digital circuits are predominantly RC circuits (other than the communication part)
- Simplistically a logic gate can be model as a RC circuit
- The speed of the computer is limited by the RC time constant

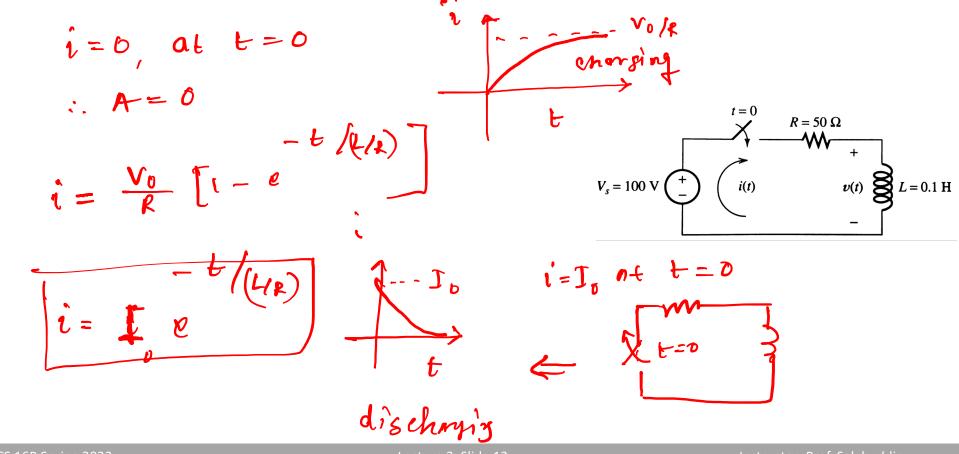


switches between "low" (logic 0) and "high" (logic 1) voltage states

R-L Circuits



R-L Circuits



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Circuits with a constant source

$$V_{c}(t) = A e^{-t/r} + V_{0} \begin{bmatrix} 1 - e^{-t/r} \end{bmatrix}$$

$$= (A - V_{0}) e^{-t/r} + V_{0}$$

$$B + V_{1} = 0 \Rightarrow B = -V_{0}$$

$$B + V_{1} = 0 \Rightarrow B = -V_{0}$$

$$V_{c}(t) = B e^{-t/r} + V_{0}$$

$$V_{c}(t) = V_{0} \begin{bmatrix} 1 - e^{-t/r} \end{bmatrix}$$

$$V_{c}(t) = V_{0} \begin{bmatrix} 1 - e^{-t/r} \end{bmatrix}$$

$$Same as before$$

Particular Solution: Observations

$$\frac{dy}{dt} + ay = b$$

y follows the same form as b
if b is constant y is constant
if b is simult $y = Asinwt + BUSWt$
if b = At $y = Bt + C$

Complex Numbers

- $e^{i\theta} = \cos(\theta) + i\sin(\theta)$
- Read the note j