$(\mathbf{r}, \mathbf{r})^2$

Spring 2024.

Your name: **EECS 251B** SID: 1234567890

Quiz 2. Please show all of your work.

For this problem, all transistors are minimum length. Technology parameters are given below: $V_{DD} = 1$ V, $V_{Th} = 0.4$ V, L=30nm. Effective mobility is the same for NMOS and PMOS devices. $V_A \rightarrow \infty$.

Equations reminder:

$$I_{DSlin} = \frac{\mu C_{ox}}{1 + (V_{DS} / E_C L)} \frac{W}{L} \left((V_{GS} - V_{Th}) V_{DS} - \frac{V_{DS}^2}{2} \right)$$

$$V_{DS} = \frac{(V_{GS} - V_{Th}) E_C L}{(V_{DS} - V_{Th}) E_C L}$$

$$I_{DSat} = \frac{W}{L} \frac{\mu_{eff} C_{ox} E_C L}{2} \frac{(V_{GS} - V_{Th})}{(V_{GS} - V_{Th}) + E_C L} \qquad V_{DSat} = \frac{(V_{GS} - V_{Th}) E_C L}{(V_{GS} - V_{Th}) + E_C L}$$

If an optimally sized NAND2 gate has a logical effort of 1.25, what is the logical effort of an optimally sized NAND3?

(Hint: Recall that the logical effort is the ratio of the gate's input capacitance to that of an inverter of equivalent drive strength.)

Since the effective mobility is the same for NMOS and PMOs devices, we have $W_p: W_n = 1:1$ for an inverter. Let the sizing of the NMOS in the NAND2 gate be x, as shown below:



Tuesday, March 12, 2024. in class.

The logical effort is given by (1 + x)/2 = 1.25. We can solve for x to get x = 1.5. We can solve for $E_{c}L$ of a single device using the provided I_{dsat} equation, since we know that the stack has double the effective channel length and that the NAND2 sizing is chosen such that the I_{dsat} for the NMOS stack remains the same as the I_{dsat} of an inverter.

Setting $I_{dsat,inv} = I_{dsat,nand2}$ and canceling common terms, we have the following equation:

$$W_{inv}/(V_{GS} - V_{th} + E_{C}L) = W_{nand2}/(V_{GS} - V_{th} + 2E_{C}L).$$

We have $W_{nand2}/W_{inv} = 1.5$ and V_{GS} - $V_{th} = 0.6$ V. Solving for E_CL , we have $E_CL = 0.6$ V.

We can now solve for W_{nand3} using a similar equation:

$$W_{inv}/(V_{GS} - V_{th} + E_{C}L) = W_{nand3}/(V_{GS} - V_{th} + 3E_{C}L).$$

Plugging in values, we get $W_{nand3} = 2$. This corresponds to a logical effort of (1 + 2)/2 = 1.5.